


REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



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PROJECT TITLE:
18-bit PRECISION DATA ACQUISITION SYSTEM

DRAWING TITLE:
ADC INPUT SIGNALS

SIZE: C	HARDWARE NUMBER: <HARDWARE_NUMBER>	DATE: <DATE>
ENGINEER: <ENGINEER>	DRAWN BY: <DRAWN_BY>	REV.: REV3
	TEMPLATE REV.: <TEMPLATE_REVISION>	SHEET 2 OF 7


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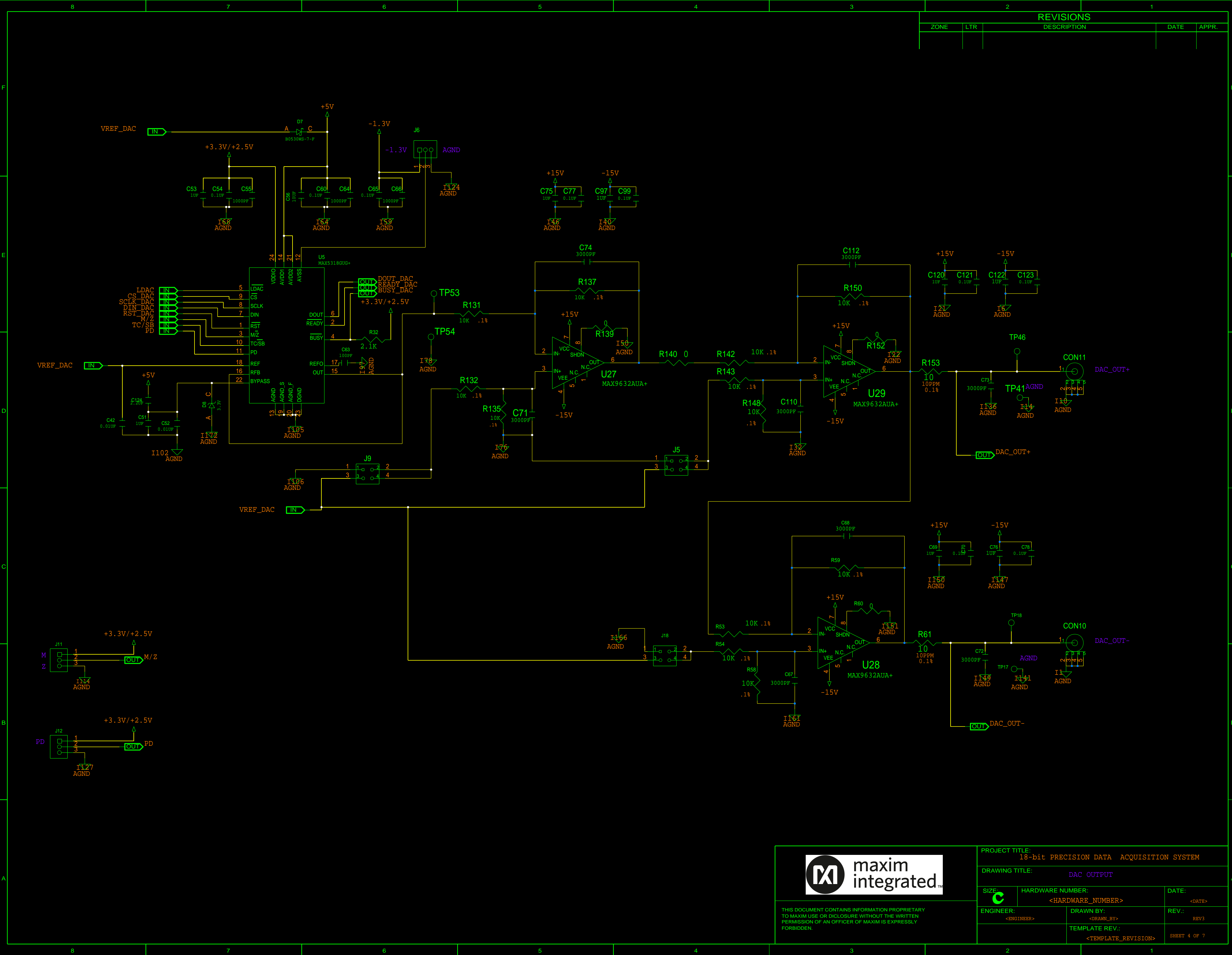
PROJECT TITLE:	18-bit PRECISION DATA ACQUISITION SYSTEM
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DRAWING TITLE: ADC AND VREF


SIZE: 	HARDWARE NUMBER: <HARDWARE NUMBER>	DATE: <DATE>
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ENGINEER:	DRAWN BY:	REV.:
<ENGINEER>	<DRAWN_BY>	REV3

	TEMPLATE REV.: <TEMPLATE_REVISION>	SHEET 3 OF 7
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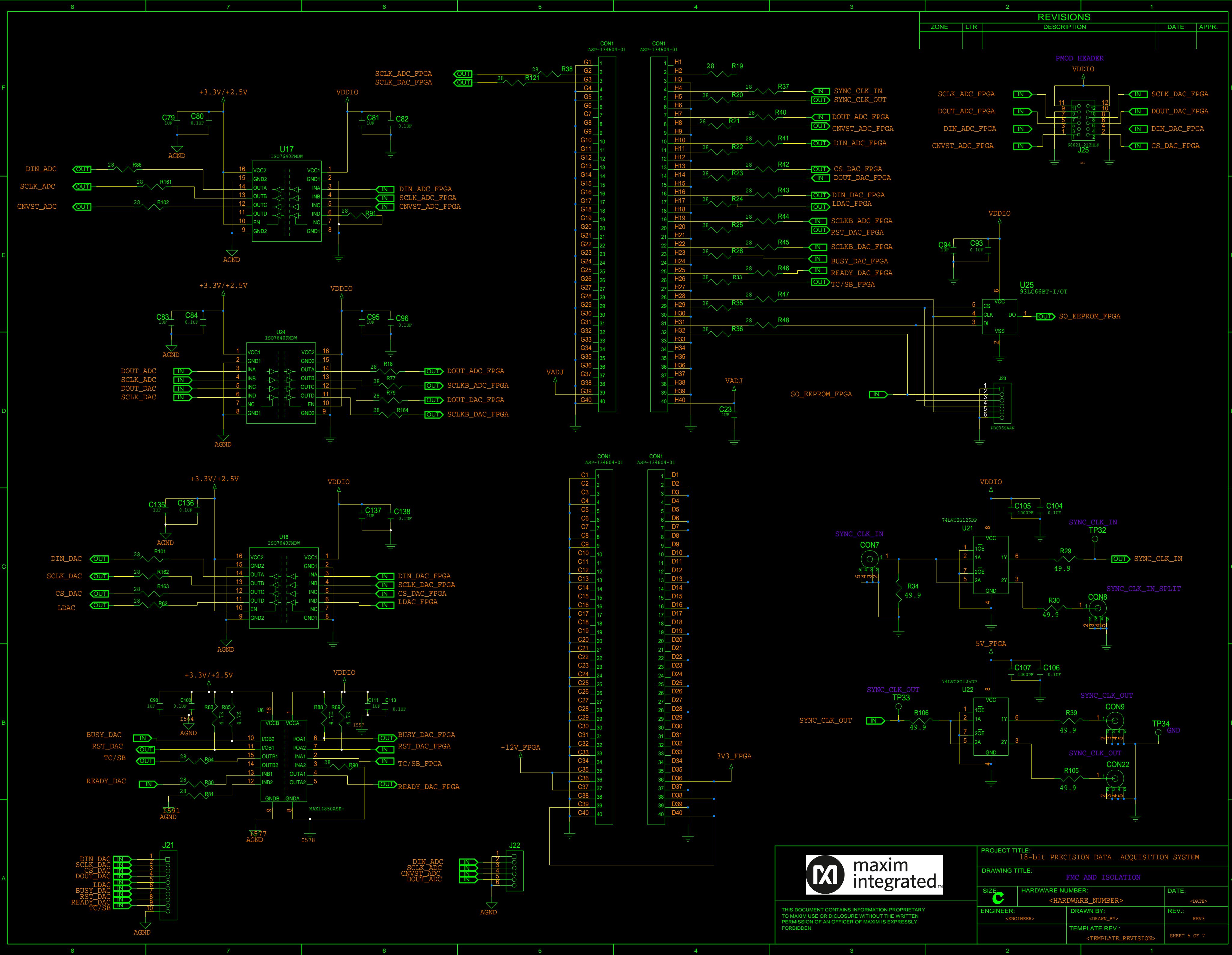
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PROJECT TITLE:
18-bit PRECISION DATA ACQUISITION SYSTEM


DRAWING TITLE:
DAC OUTPUT

SIZE: C	HARDWARE NUMBER: <HARDWARE_NUMBER>	DATE: <DATE>
ENGINEER: <ENGINEER>	DRAWN BY: <DRAWN_BY>	REV.: REV3
TEMPLATE REV.: <TEMPLATE_REVISION>		SHEET 4 OF 7

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REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



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PROJECT TITLE:
18-bit PRECISION DATA ACQUISITION SYSTEM

DRAWING TITLE:
FMC AND ISOLATION

SIZE: C	HARDWARE NUMBER: <HARDWARE_NUMBER>	DATE: <DATE>
ENGINEER: <ENGINEER>	DRAWN BY: <DRAWN_BY>	REV.: REV3
TEMPLATE REV.: <TEMPLATE_REVISION>		SHEET 5 OF 7

