


8	7	6	5	4	3	2		1		
						REVISIONS				
						ZONE	LTR	DESCRIPTION	DATE	APPR.
F									F	
E									E	
D									D	
C									C	
B									B	
A									A	
8	7	6	5	4	3	2		1		

MAXREFDES74# (REGGIO)

18-BIT PRECISION DATA ACQUISITION SYSTEM

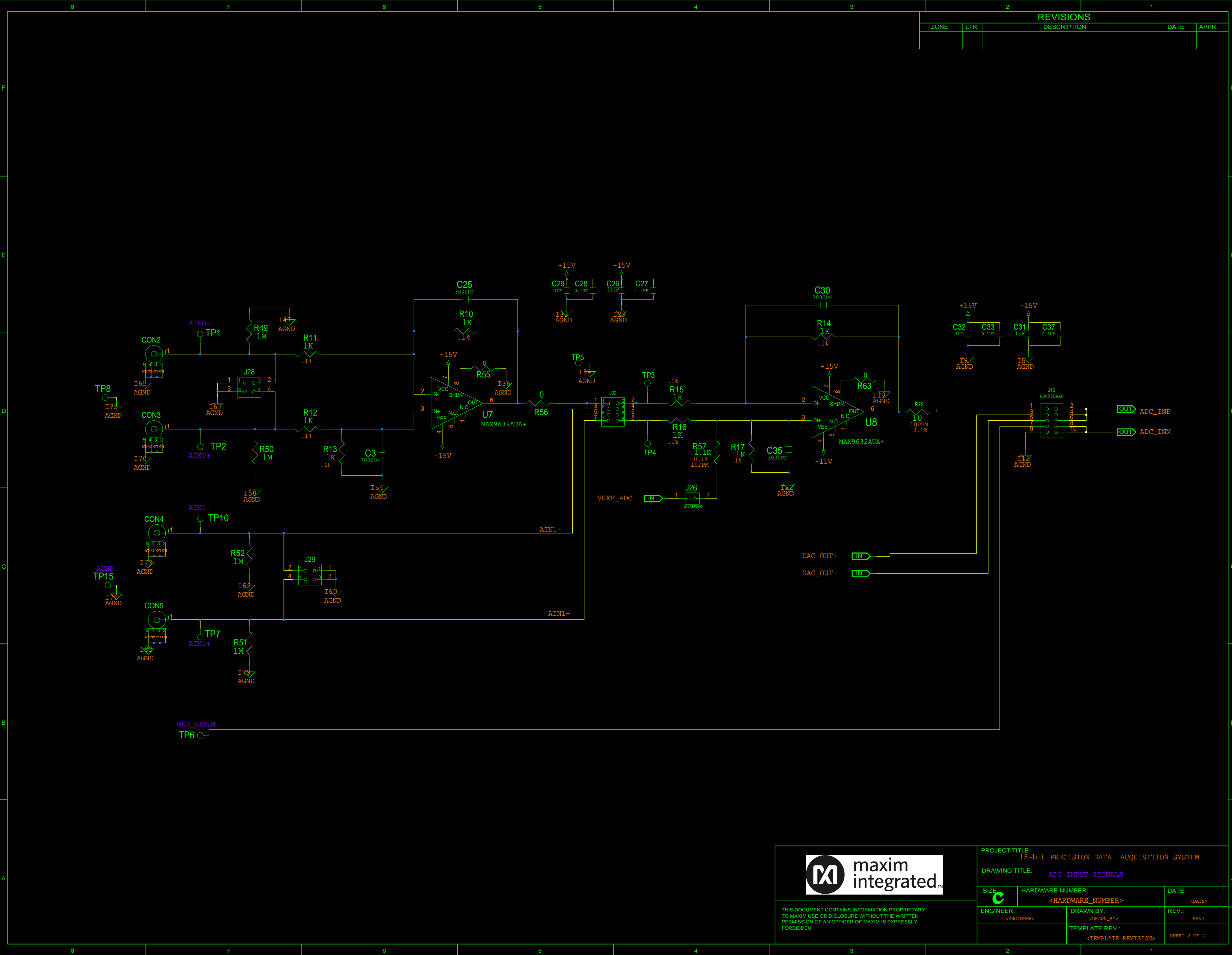
MAX11156 ADC AND MAX5318 DAC

REV 3




THIS DOCUMENT CONTAINS INFORMATION PROPRIETARY TO MAXIM USE OR DICLOSURE WITHOUT THE WRITTEN PERMISSION OF AN OFFICER OF MAXIM IS EXPRESSLY FORBIDDEN.

PROJECT TITLE: 18-bit PRECISION DATA ACQUISITION SYSTEM		
DRAWING TITLE:		
SIZE: <b>C</b>	HARDWARE NUMBER: <HARDWARE_NUMBER>	DATE: <DATE>
ENGINEER: <ENGINEER>	DRAWN BY: <DRAWN_BY>	REV.: REV3
TEMPLATE REV.: <TEMPLATE_REVISION>		SHEET 1 OF 7



REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



maxim  
integrated™

PROJECT TITLE:  
18-bit PRECISION DATA ACQUISITION SYSTEM

DRAWING TITLE:  
ADC INPUT SIGNALS

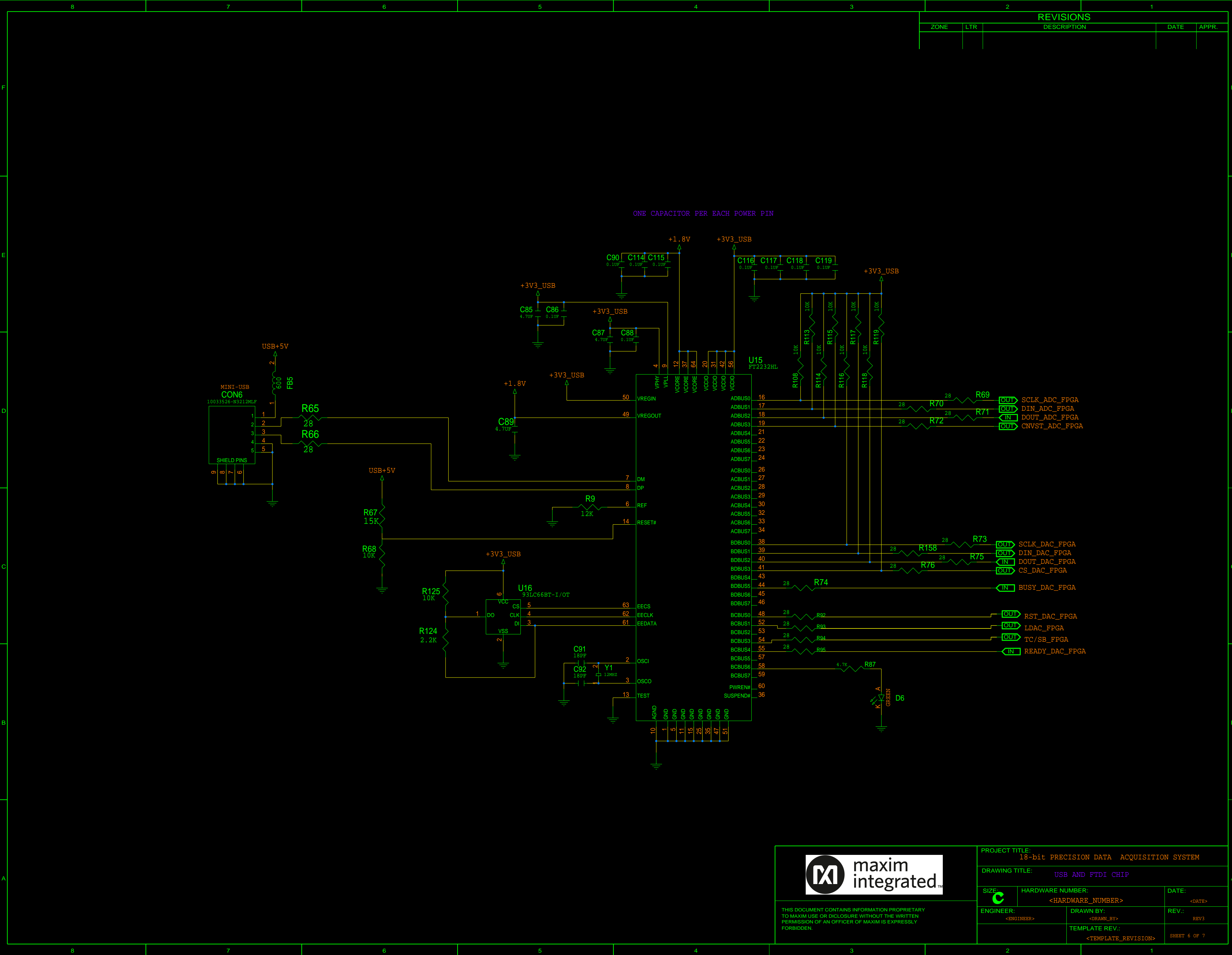
SIZE: <b>C</b>	HARDWARE NUMBER: <HARDWARE_NUMBER>	DATE: <DATE>
ENGINEER: <ENGINEER>	DRAWN BY: <DRAWN_BY>	REV.: REV3
TEMPLATE REV.: <TEMPLATE_REVISION>		SHEET 2 OF 7

THIS DOCUMENT CONTAINS INFORMATION PROPRIETARY TO MAXIM USE OR DISCLOSURE WITHOUT THE WRITTEN PERMISSION OF AN OFFICER OF MAXIM IS EXPRESSLY FORBIDDEN.









REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



THIS DOCUMENT CONTAINS INFORMATION PROPRIETARY TO MAXIM USE OR DISCLOSURE WITHOUT THE WRITTEN PERMISSION OF AN OFFICER OF MAXIM IS EXPRESSLY FORBIDDEN.

PROJECT TITLE: 18-bit PRECISION DATA ACQUISITION SYSTEM			
DRAWING TITLE: USB AND FTDI CHIP			
SIZE: C	HARDWARE NUMBER: <HARDWARE_NUMBER>		DATE: <DATE>
ENGINEER: <ENGINEER>	DRAWN BY: <DRAWN_BY>		REV.: REV3
	TEMPLATE REV.: <TEMPLATE_REVISION>		SHEET 6 OF 7

