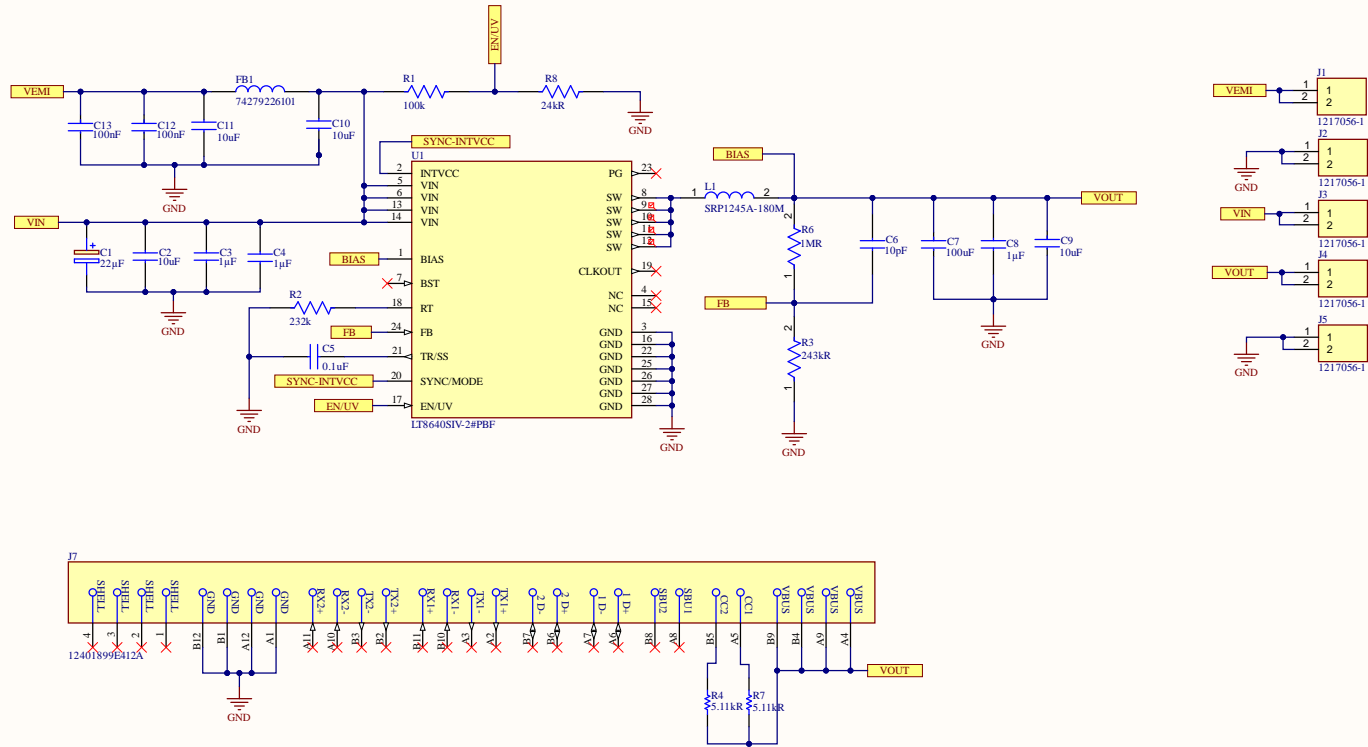
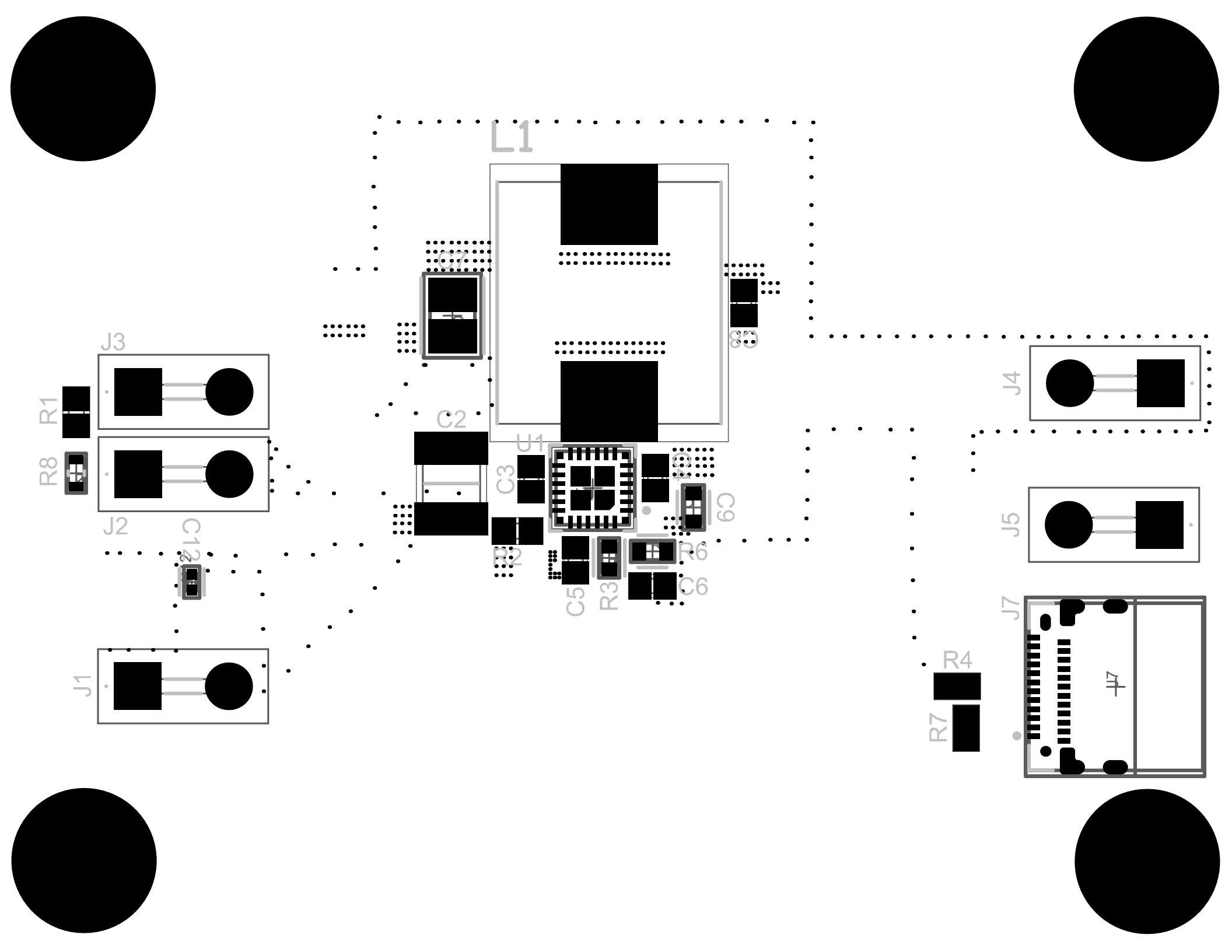


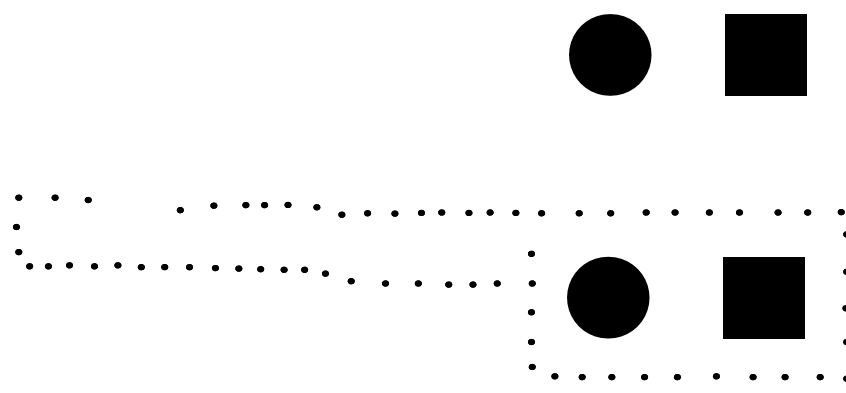
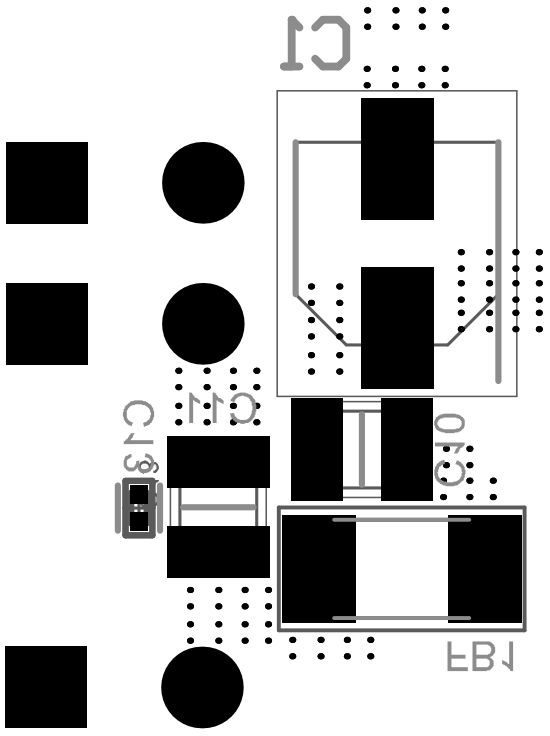
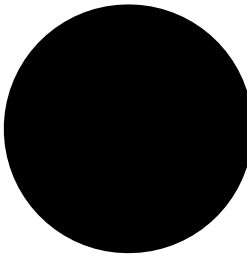
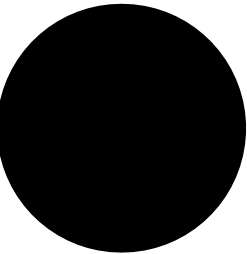
THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR HEREWITH IS THE PROPERTY OF ALTUM LIMITED AND MAY BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE RESERVED OR EXPRESS OR IMPLIED WARRANTY GIVEN.

REVISION	DESCRIPTION	DATE	APPROVED



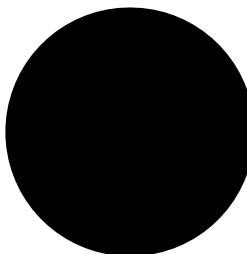
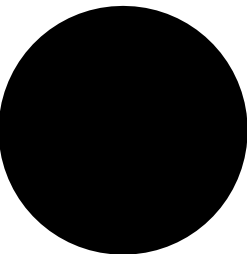
APPROVALS	DATE	PROJECT	Altium	
ENG: *	---	PROJECT REVISION:	89ab78b32471d7db7c589ad667246b570952154879ab6754a6e65	
DSN: *	---	TITLE	USB C current mode supply from 12V	
CHK: *	---	SIZE	CAGE CODE	REV
BOM: <BOM DOC NO>		B	?????	<SCH DWG NO>
ASSY DWG: <ASSY DWG NO>		SCALE:	FILE NAME	SHEET 1 OF 1
FAB DWG: <FAB DWG NO>		Sheet1.SchDoc		
PCB DWG: <PCB DWG NO>				

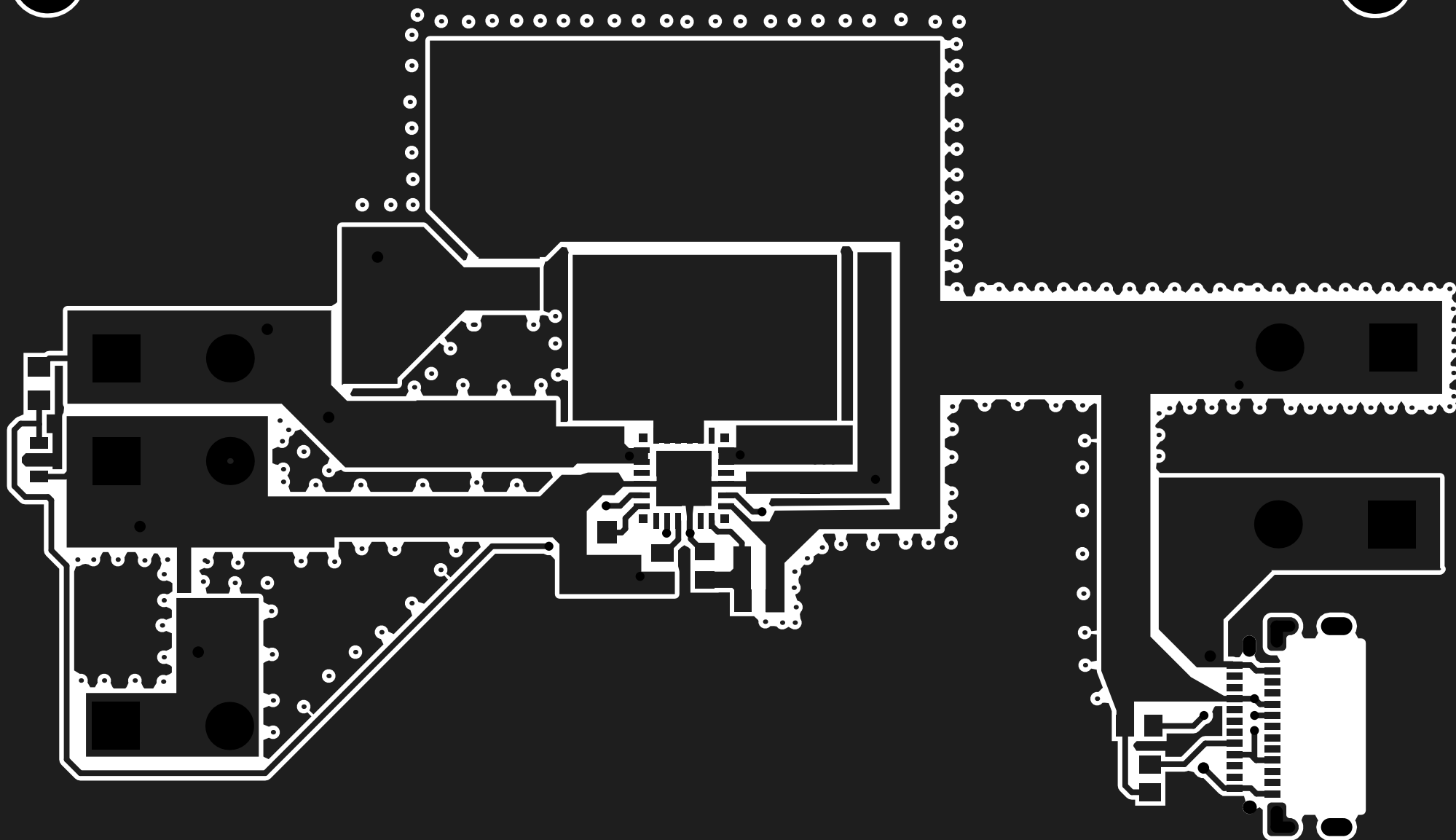


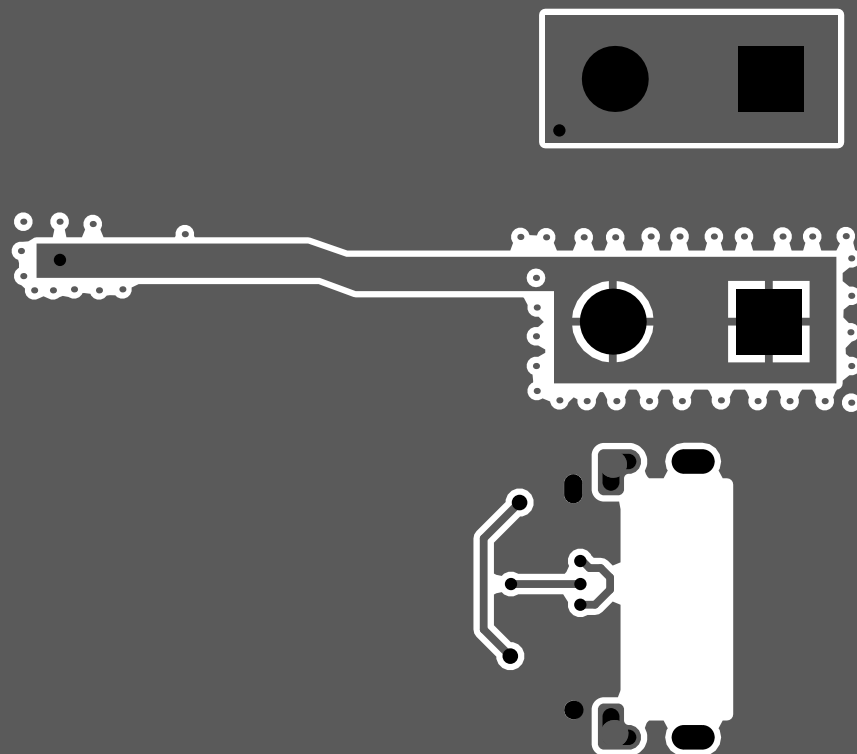
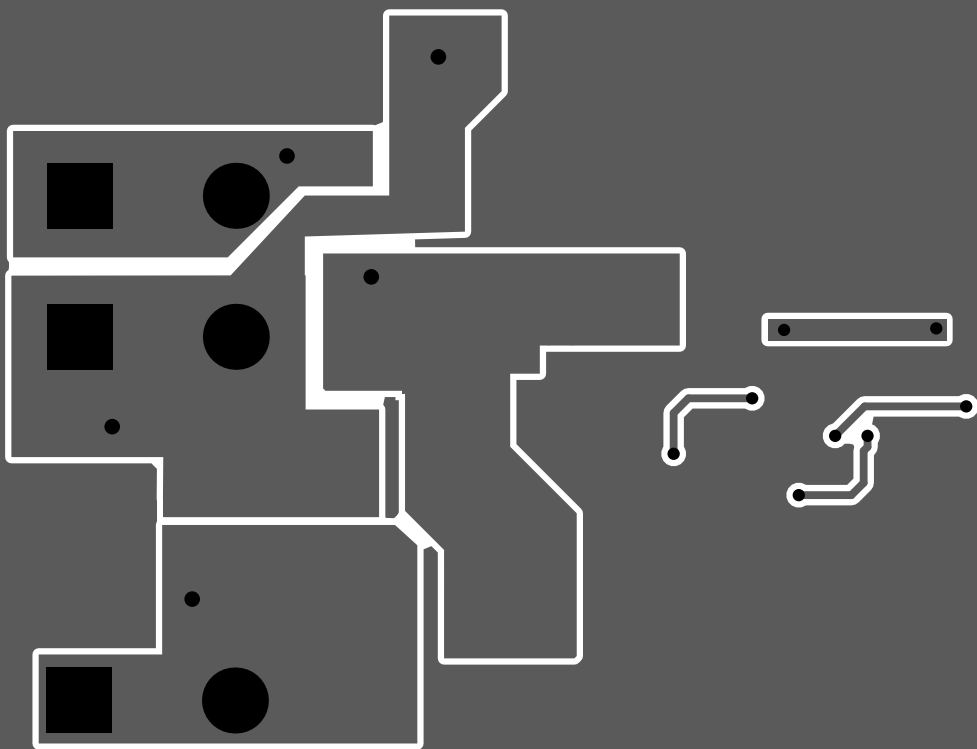


P

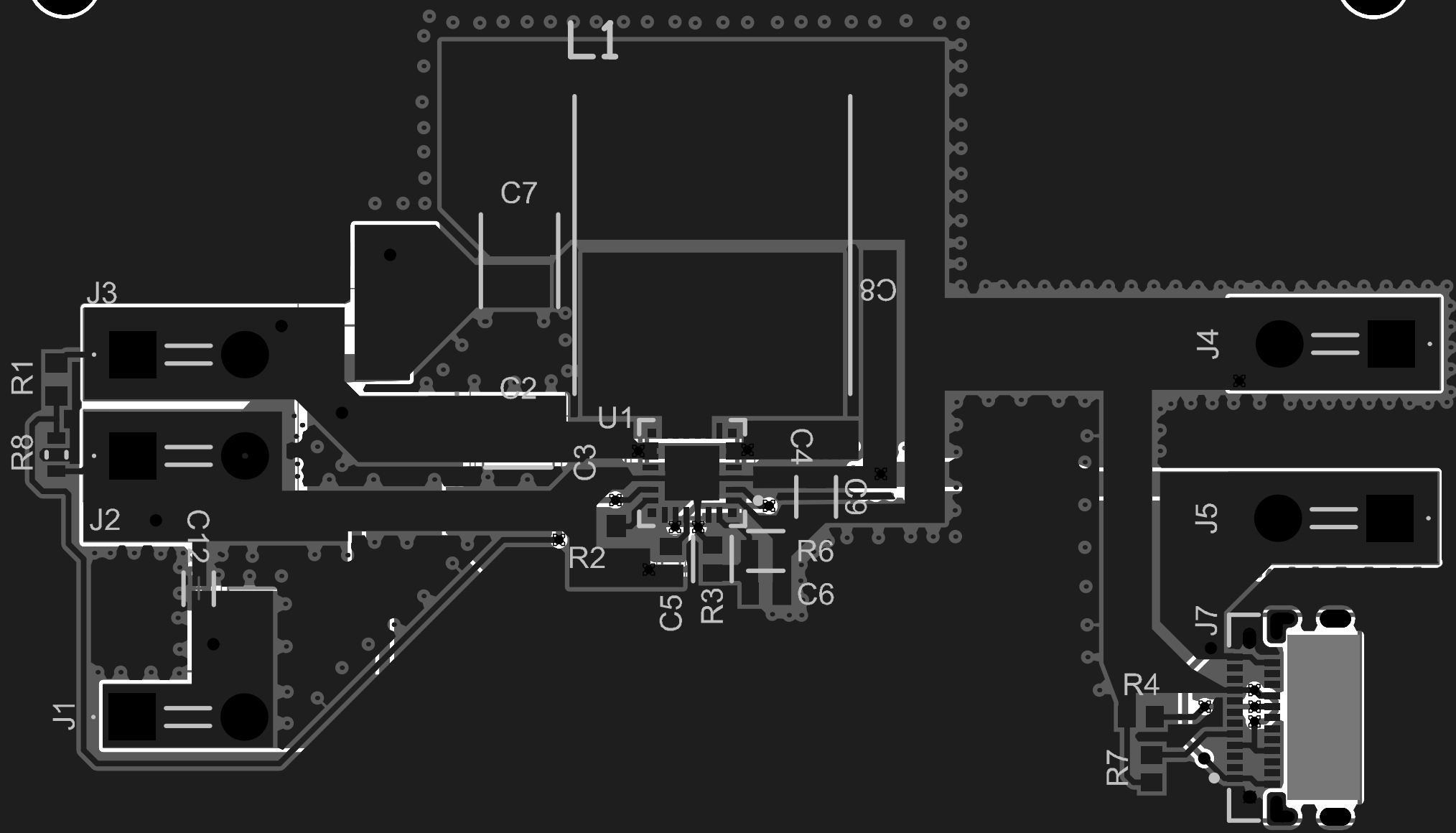
P











# Board Stack Report