Linear Structures: Applications to Cryptanalysis of Round-Reduced Keccak

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Abstract. In this paper, we analyze the security of round-reduced versions of the Keccak hash function family. Based on the work pioneered by Aumasson and Meier, and Dinur *et al.*, we formalize and develop a technique named *linear structure*, which allows linearization of the underlying permutation of Keccak for up to 3 rounds with large number of variable spaces. As a direct application, it extends the best zero-sum distinguishers by 2 rounds without increasing the complexities. We also apply linear structures to preimage attacks against Keccak. By carefully studying the properties of the underlying Sbox, we show bilinear structures and find ways to convert the information on the output bits to linear functions on input bits. These findings, combined with linear structures, lead us to preimage attacks against up to 4-round Keccak with reduced complexities. An interesting feature of such preimage attacks is low complexities for small variants. As extreme examples, we can now find preimages of 3-round `SHAKE128` with complexity 1, as well as the first practical solutions to two 3-round instances of Keccak challenge. Both zero-sum distinguishers and preimage attacks are verified by implementations. It is noted that the attacks here are still far from threatening the security of the full 24-round Keccak.

Key words: Cryptanalysis, `SHA-3`, Keccak, Preimage attacks, Zero-sum distinguishers

1 Introduction

The Keccak sponge function family [6] was designed by Bertoni *et al.* as one of the 64 proposals submitted to the `SHA-3` competition [24] in October 2008. It won in October 2012 after intense competition, and was subsequently standardized by the U.S. National Institute of Standards and Technology (NIST) as *Secure Hash Algorithm-3* [25] (`SHA-3`) in August 2015. As such, Keccak has received intensive security analysis, since the design was made public in 2008, against

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the traditional security notions such as collision, preimage, and second-preimage resistance, as well as distinguishers of the underlying permutations and securities under some message authentication code, stream cipher, and authenticated cipher modes.

Up to date, the best collision attacks are reduced up to 4 out of 24 rounds of KECCAK-224/256 with practical complexities [12, 14], and up to 5 rounds of KECCAK-256 with theoretical complexities [13], by differential attacks. Practical preimage attacks are up to 2 rounds, by the approaches of meet-in-the-middle [23] and SAT solvers [21]. Theoretical preimage attacks work up to 7/8/9 rounds for KECCAK-224/256/512 respectively with small time complexity gains over bruteforce [3, 11, 20]. There were mainly two types of distinguishers against the underlying permutation of KECCAK (named KECCAK-f), *i.e.*, zero-sum distinguishers [2, 9] and those involving high probability differentials [17, 19]. These distinguishers work for 9 rounds in [2], 8 rounds in [19] with practical complexities, and up to 15 rounds with theoretical complexities bounded by 2^{800} (birthday bound) for the 1600-bit KECCAK-f permutation. Besides these, there are also attacks in other security settings, we are not listing them all as they are less relevant with our work here.

To promote security analysis with practical complexities, the KECCAK team has been organizing the "KECCAK Crunchy Crypto Collision and Preimage Contest" [4] (we will call it KECCAK `Challenge` for short) and offering cash prizes for the winners. To make it feasible, the instances are set to be round-reduced variants of KECCAK with capacity $c = 160$ and the output truncated to 160 bits for collisions and 80 bits for preimages, so the theoretical complexities for both are 2^{80}, which is relatively small but yet beyond PC's capability. Instances have been solved for up to 4 and 2 rounds for collisions and preimages, respectively.

Our contributions. In this paper, we focus on security analysis of KECCAK with respective to two security notions, *i.e.*, distinguisher of round-reduced versions of the underlying permutation KECCAK-f, and preimage of round-reduced variants of the KECCAK hash function family. Firstly, we review the zero-sum distinguisher by Aumasson and Meier [2]. Zero-sum distinguishers finds a set of input to the permutation, whose sum is zero and the set of corresponding output sums to zero at the same time. This distinguisher makes use of the property of low algebraic degrees 2 and 3 of the Sbox and its inverse used in KECCAK-f, which is the only non-linear step of the round function. The attack starts from the middle of the permutation, and extends freely towards both forward and backward directions of the permutations. By setting up initial values of the middle starting point, one can bypass one round without increasing the algebraic degrees. Similar idea was extended to bypass one round by Dinur *et al.* [15] for key recovery attacks in keyed settings. In this paper, we formalize this idea as *linear structures* and extend the free starting rounds to 3 by combining properties of the linear layers and Sbox of the KECCAK-f round function, and generally increase the attacked rounds of the zero-sum distinguishers by Aumasson and Meier by 2 without increasing the complexities. Notably, we extend the practically attacked

rounds from 9 to 11. Furthermore, the 12-round KECCAK-f permutations can be distinguished with complexity 2^{65} or 2^{82}. This is of special interests since the 12-round KECCAK-f permutation variants are used in the CAESAR candidates KEYAK [8] and KETJE [7]. Nevertheless, we stress here that this distinguisher does not affect the security of KEYAK or KETJE. A summary of the comparisons of our results with the previous ones is shown in Table 1. Our results are verified by an implementation of the 11-round distinguisher with time and data complexity 2^{33}, with all the 1600 bits of the output summing to zero with certainty. Note that Table 1 does not include the distinguishers with complexities $\geq 2^{800}$, such as [9, 16]. In the KECCAK reference [6, Page 61], the designers mentioned that "Only structural distinguishers on f that have non-zero advantage below 2^{800} queries can possibly qualify as a threat for the security of a sponge function that uses it." This is a birthday bound with regard to the size of the permutation.

#Rounds	inv+forw	Best Known	inv+forw	Improved	inv+forw	Further
7	3+4	2^{13} [19]	3+4	2^{10}	**2+5**	2^{9}
8	3+5	2^{18} [2, 19]	3+5	2^{17}	3+5	2^{10}
9	4+5	$2^{30\,\mathrm{d}}$ [2]	4+5	2^{28}	**3+6**	2^{17}
10	4+6	$2^{60\,\mathrm{e}}$ [2]	4+6	2^{33}	4+6	2^{28}
11	5+6	$2^{60\,\mathrm{f}}$ [2]	**4+7**	2^{65}	**4+7**	2^{33}
12	5+7	2^{129} [2]	5+7	2^{82}	**4+8**	2^{65}
13	6+7	2^{244} [2]	**5+8**	2^{129}	**5+8**	2^{82}
14	6+8	2^{257} [2]	6+8	2^{244}	**5+9**	2^{129}
15	6+9	2^{513} [2]	6+9	2^{257}	-	-

Table 1: Summary of distinguishers on the 1600-bit KECCAK-f permutation, with complexities bounded by 2^{800}

The second contributions of this paper are improved preimage attacks. In contrast to the meet-in-the-middle and SAT solver techniques used previously, we adopt the techniques of linear structures and find preimages by linearizing the KECCAK round functions and converting the preimage finding problems to that of solving systems of linear equations. This technique leads to attacks on up to 4-round KECCAK with reduced complexities. The complexities of 3-round preimages are so significantly reduced that enables us to find preimages of SHAKE128 (a variant of KECCAK[$r = 1344, c = 256$] adapted by SHA-3) practically, and to solve two of 3-round preimage instances and a near-preimage with only

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d Corrected: 2^{33}. Note that the complexity 2^{30} estimated in [2] is based on the experiments made over a 25-dimensional space by the designers in [6], which shows the maximum degree over 25 variables of 4 rounds to be 15. We expect the maximum degree over 30 variables of 4 rounds to be 16, and thus we estimate the time complexity for 5 rounds to be 2^{33}.

e Corrected: 2^{65}.

f Corrected: 2^{82}.

two bits difference of 4-round preimage instance of the KECCAK `Challenge`. The summary of our preimage attacks together with the previous best ones is shown in Table 2. Note that Table 2 does not include small optimizations of exhaustive search, such as [3, 11]. In this table, by variant 128 we mean `SHAKE128`$(M, 128)$. Different with the attacks of [20] which outperform exhaustive search by a larger factor as the hash size becomes larger, our attacks outperform exhaustive search by a larger factor as the hash size (or the capacity) becomes smaller.

#Rounds	Variant	Time	Reference
2	128/224/256	2^{33}	[23]
2	128/224/256	1	Section 6.1
2	384	2^{129}	Section 6.1
2	512	2^{384}	Section 6.1
3	128	$2^{26.6}$	Section 6.2
3	128	1	Section 6.4
3	224	2^{97}	Section 6.2
3	256	2^{192}	Section 6.2
3	384	2^{322}	Section 6.3
3	512	2^{482}	Section 6.3
3	512	2^{506}	[20]
4	128	2^{106}	Section 6.4
4	224	2^{213}	Section 6.3
4	256	2^{251}	Section 6.3
4	224/256	$2^{221}/2^{252}$	[20]
4	384/512	$2^{378}/2^{506}$	[20]

**Table 2:** Summary of preimage attacks on KECCAK reduced up to 4 rounds.

Both improved zero-sum distinguishers and preimage attacks are possible thanks to the technique *linear structures*. By exploiting the properties of the Sbox used in KECCAK, we find ways to linearize both the Sbox itself and its inverse. Combining with properties of the linear layer of the KECCAK round function, we are able to find linear subspaces with large dimension by setting proper initial values. A nice property of these linear structures is that the algebraic degrees can be kept the same for up to 3 rounds, *i.e.*, output bits of 3-round KECCAK-f can be expressed as linear combinations of input bits. As a special feature of the linear structure, complexities of our attacks reduce significantly when the targets are KECCAK instances with small capacities. In such cases, the number of required constraints derived from pre-set constants is small and the degree of freedom left is relatively large, and hence leads to faster attacks. As extreme examples, we can find preimages of 3-round `SHAKE128`$(M, 128)$ and solve the 3-round KECCAK$[r = 1440, c = 160, \ell = 80]$ instance of KECCAK `Challenge` with complexity 1.

Organization. The rest of the paper is organized as follows. Section 2 gives the details of the KECCAK hash function family, followed by the properties of the Sbox in Section 3. The linear structure is introduced in Section 4. Its applications of zero-sum distinguishers and preimages attacks are presented in Section 5 and Section 6, respectively. Section 7 concludes the paper.

## 2   Definition of KECCAK

2.1   The sponge function

The KECCAK hash function follows the sponge construction, as depicted in Fig. 1. The message M is padded and split into blocks of r bits each. Beginning with an initial value (IV), the first r bits of b-bit state is XORed with the message block, followed by the application of the permutation f. This step is repeated until all message blocks are processed. Then the first r bits are outputted, r more bits can be obtained after an additional application of f, and this process is repeated until all required digest bits are obtained. The number of iterations is determined by the requested number of digest bits ℓ. Finally the output is truncated to its first ℓ bits.
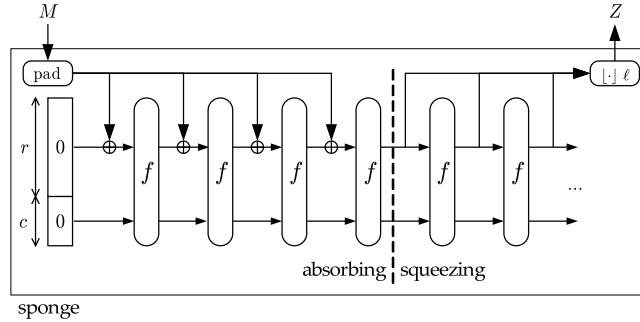
Fig. 1: The sponge function [5]

2.2   The KECCAK hash functions

To define the KECCAK hash function family, the designers give the details of the underlying permutation f, as well as parameters set of (r, c, ℓ). The IV is set to be all "0"s. The underlying permutation of the KECCAK hash function is chosen in a set of seven KECCAK-f permutations, denoted KECCAK-$f[b]$, where $b \in \{25, 50, 100, 200, 400, 800, 1600\}$ is the width of the permutation. The default version of KECCAK-f is of size $b = 1600$ bits, which can be represented as 5×5 64-bit lanes as depicted in Fig. 2, denoted as $A[x, y]$ with x for the index of column and y for the index of row. In what follows, indexes of x and y are from the set
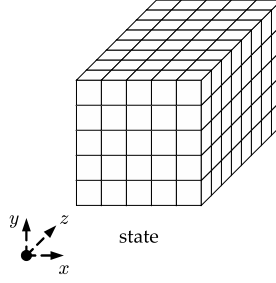
Fig. 2: The KECCAK state [6]

$\{0, 1, 2, 3, 4\}$ and they are working in modulo 5 without other specification. The underlying permutation KECCAK-f[1600] consists of 24 identical round functions up to a difference of constant addition. The round function R consists of five operations (θ goes first):

$$R = \iota \circ \chi \circ \pi \circ \rho \circ \theta.$$

θ: $A[x, y] = A[x, y] \oplus \bigoplus_{j=0}^{4}(A[x - 1, j] \oplus (A[x + 1, j] \lll 1))$, for $x, y = 0, \ldots, 4$.
ρ: $A[x, y] = A[x, y] \lll r[x, y]$, for $x, y = 0, \ldots, 4$.
π: $A[y, 2x + 3y] = A[x, y]$, for $x, y = 0, \ldots, 4$.
χ: $A[x, y] = A[x, y] \oplus ((\sim A[x + 1, y]) \& A[x + 2, y])$, for $x, y = 0, \ldots, 4$.
ι: $A[0, 0] = A[0, 0] \oplus RC$.

Here "$\oplus$" denotes for bit-wise XOR, "$\lll$" for bit rotation towards MSB of the 64-bit word, "$\sim$" for bit negation of 64-bit word, "$\&$" for bit-wise logic AND, "$r[x, y]$" for lane dependent rotation constants presented in Table 3, and "RC" for round-dependent round constants. We ignore the details of RC since it does not affect our attacks to be presented.

Without other specifications, KECCAK-f hereinafter refers to KECCAK-f[1600].

	$x=0$	$x=1$	$x=2$	$x=3$	$x=4$
$y=0$	0	1	62	28	27
$y=1$	36	44	6	55	20
$y=2$	3	10	43	25	39
$y=3$	41	45	15	21	8
$y=4$	18	2	61	56	14

Table 3: Rotation constants $r[x, y]$ in KECCAK ρ operation.

2.3 Instances of KECCAK and SHA-3

The hash function KECCAK[r, c, ℓ] refers to the instance of the KECCAK sponge function family with parameters capacity c, bitrate r and output length ℓ.

The official versions of KECCAK have $r = 1600 - c$ and $c = 2\ell$, where $\ell \in \{224, 256, 384, 512\}$, called KECCAK-224, KECCAK-256, KECCAK-384, and KECCAK-512. The padding rule of KECCAK is of the form $M10^*1$, *i.e.*, it pads a single bit "1" followed by a smallest non-negative number of "0", then a bit "1", such that the bit length of the padded message becomes multiple of r.

The SHA-3 standard takes mainly the four default instances of KECCAK with digest sizes $224, 256, 384, 512$. The only difference is the padding rule. These four SHA-3 instances pad the message with two bits "01" before applying the KECCAK padding rule, so the padded message becomes $M0110^*1$, *i.e.*, it pads M by three bits "011", followed by a smallest non-negative number of "0"s, then a bit "1", such that the padded message is of multiple of r bits. Generally, all our analysis results in this paper on KECCAK applies to SHA-3, under the same parameters (r, c, ℓ), possibly with an increment of the complexities by at most 2^2 due to the two extra padding bits.

The SHA-3 family also includes two extendable-output functions (XOFs), called SHAKE128 and SHAKE256. More exactly, these instances SHAKE128(M, ℓ) and SHAKE256(M, ℓ) are defined from KECCAK$[r = 1344, c = 256]$ and KECCAK $[r = 1088, c = 512]$ by appending a four-bit suffix "1111" to the message, for any output length ℓ. Our preimage attacks on KECCAK-256 also applies to SHAKE256$(M, 256)$. We will only consider preimage attacks on SHAKE128$(M, 128)$.

3 Properties of the Sbox χ

In this section, we discuss the properties of the Sbox χ, which will be used to construct distinguishers on KECCAK-f permutation in Section 5, and to mount preimage attacks on KECCAK in Section 6.

3.1 Setting up linear equations from the output of χ

Bilinear structure. We show in this section that given t consecutive bits out of the 5 output bits of χ, one can set up at least $t - 1$ linear equations on the 5 input bits due to the bilinear structure of the χ. Hereinafter, we may also refer to the χ operation by Sbox.

The algebraic normal form of χ mapping 5-bit $a = a_0a_1a_2a_3a_4$ into 5-bit $b = b_0b_1b_2b_3b_4$ can be written as $b_i = a_i \oplus (a_{i+1} \oplus 1) \cdot a_{i+2}$, *i.e.*,

$$b_0 = a_0 \oplus (a_1 \oplus 1) \cdot a_2, \tag{1}$$
$$b_1 = a_1 \oplus (a_2 \oplus 1) \cdot a_3, \tag{2}$$
$$b_2 = a_2 \oplus (a_3 \oplus 1) \cdot a_4, \tag{3}$$
$$b_3 = a_3 \oplus (a_4 \oplus 1) \cdot a_0, \tag{4}$$
$$b_4 = a_4 \oplus (a_0 \oplus 1) \cdot a_1. \tag{5}$$

Then, we show that given two consecutive bits of the output of χ, one linear equation can be set up on the input bits. Without loss of generality, assume that

b_0 and b_1 are known. By (2), we have

$$b_1 \cdot a_2 = (a_1 \oplus (a_2 \oplus 1) \cdot a_3) \cdot a_2 = a_1 \cdot a_2$$

and thus according to (1) we obtain

$$b_0 = a_0 \oplus (b_1 \oplus 1) \cdot a_2. \tag{6}$$

Given three consecutive bits of the output of χ, to say b_0, b_1 and b_2, an additional linear equation can be similarly set up:

$$b_1 = a_1 \oplus (b_2 \oplus 1) \cdot a_3. \tag{7}$$

Generally, the input a and output b of χ satisfy $F(a, b) = 0$ with $F(a, b) = aSb + Ta + Qb$, for some 5×5 binary matrices S, T, Q.

Given four output bits of χ, any bit of the input can be represented as a linear function on the unknown bit of the output, and one can naturally set up four linear equations on the input bits by eliminating the unknown output bit. It is clear that given all the five output bits of χ, the input bits are all determined. We summarize in Table 4 the number of linear equations on the input bits that can set up for given t consecutive bits of the output of χ.

Table 4: Number of linear equations obtained from the output of χ

#Known consecutive output bits	2	3	4	5
#Linear equations	1	2	4	5

3.2 Setting up more linear equations

As explained above, given t bits of the output of χ, for $t = 4$ or 5, one can set up t linear equations on the input of χ, and for $t < 4$, one can set up $t - 1$ linear equations. Here we present two more methods for setting up one or more extra linear equations on input bits when less than 4 bits of the output are known.

The first method is to guess the value of an input bit. We obtain two extra linear equations at cost of doubling the operations needed. For example, if a single bit b_0 of the output is known, no linear equation could be set with previous methods. However, here we can guess the input bit a_1 so that the equation $b_0 = a_0 \oplus (a_1 \oplus 1) \cdot a_2$ becomes linear. Together with the guess of a_1 itself, we obtain in total two more linear equations. The cost is that there are 2 choices of the guess, so we obtained the two extra linear equations with the cost of an increase of time complexity by a factor of 2. This is generally true when the number of known output bits is less than 4, as summarized in Setting 1.

Setting 1 *When the number of known output bits is in the range $[1, 3]$, a guess of an input bit leads to two extra linear equations on the input bits, by the cost of doubling the time complexity.*

The second method is to make use of the probabilistic equation $b_i = a_i$ which holds with probability 0.75, due to the fact that $b_i = a_i \oplus (a_{i+1} \oplus 1) \cdot a_{i+2}$ and $(a_{i+1} \oplus 1) \cdot a_{i+2}$ is 0 with probability 0.75 assuming uniformly distributed a_{i+1} and a_{i+2}, as summarized in Setting 2. This method will result in time complexity increase by a factor $0.75^{-1} = 2^{0.415}$.

Setting 2 $b_i = a_i$ *of the χ holds with probability* 0.75 *when input bit a_j's are uniformly distributed, for all $i \in \{0, \ldots, 4\}$.*

3.3 Linearizing the inverse of χ

The inverse $\chi^{-1} : b \mapsto a$ has algebraic degree 3, and its algebraic normal form can be written as

$$a_i = b_i \oplus b_{i+2} \oplus b_{i+4} \oplus b_{i+1} \cdot b_{i+2} \oplus b_{i+1} \cdot b_{i+4} \oplus b_{i+3} \cdot b_{i+4} \oplus b_{i+1} \cdot b_{i+3} \cdot b_{i+4}$$
$$= b_i \oplus (b_{i+1} \oplus 1) \cdot (b_{i+2} \oplus (b_{i+3} \oplus 1) \cdot b_{i+4}) \tag{8}$$

where $0 \le i \le 4$ and the indexes are operated on modulo 5, that is,

$$a_0 = b_0 \oplus (b_1 \oplus 1) \cdot (b_2 \oplus (b_3 \oplus 1) \cdot b_4), \tag{9}$$
$$a_1 = b_1 \oplus (b_2 \oplus 1) \cdot (b_3 \oplus (b_4 \oplus 1) \cdot b_0), \tag{10}$$
$$a_2 = b_2 \oplus (b_3 \oplus 1) \cdot (b_4 \oplus (b_0 \oplus 1) \cdot b_1), \tag{11}$$
$$a_3 = b_3 \oplus (b_4 \oplus 1) \cdot (b_0 \oplus (b_1 \oplus 1) \cdot b_2), \tag{12}$$
$$a_4 = b_4 \oplus (b_0 \oplus 1) \cdot (b_1 \oplus (b_2 \oplus 1) \cdot b_3). \tag{13}$$

It is obvious to note

Setting 3 *If there is a single unknown output bit b_j of χ and all other output bits are constants, then all input bits a_i can be expressed as linear combination of b_j.*

If we impose $b_3 = 0$ and $b_4 = 1$, then we have

$$a_0 = b_0 \oplus (b_1 \oplus 1) \cdot (b_2 \oplus 1),$$
$$a_1 = b_1,$$
$$a_2 = 1 \oplus b_2 \oplus (b_0 \oplus 1) \cdot b_1,$$
$$a_3 = 0,$$
$$a_4 = 1 \oplus (b_0 \oplus 1) \cdot b_1,$$

and thus all a_i's are linear on b_0 and b_2. That's, for $b_3 = 0$, $b_4 = 1$ and any fixed b_1, the algebraic degree of χ^{-1} becomes 1.

If we further impose $b_1 = 1$, then we have

$$a_0 = b_0, \quad a_1 = 1, \quad a_2 = b_0 \oplus b_2, \quad a_3 = 0, \quad a_4 = b_0,$$

so all inputs bits a_i's become linear combinations of b_i's. Similar property holds when $b_1 = 0$. This is summarized as:

Setting 4 *When $b_{j+3} = 0$, $b_{j+4} = 1$, and b_{j+1} is known (either 0 or 1), then all inputs bits a_i's can be written as linear combinations of b_i's, for all $j \in \{0, \ldots, 4\}$.*

4 The Linear Structures

In this section, we review the previous work, and formalize the idea of linear structure. We show linearization of KECCAK-f permutation for up to 3 rounds. Our distinguisher and preimage attacks using linear structures depend directly on the space size of the variables of these linear structures, *i.e.*, more variable bits result in lower attack complexities. We show in details how the largest space size possible could be obtained in each scenario.

4.1 Techniques for keeping 2 rounds being linear

In [15], Dinur *et al.* exploited a method for keeping the first round of KECCAK-f being linear and used it to analyze the security of keyed variants of KECCAK. Here we restate and formalize their technique. Let $A[1, i]$, $i = 0, 1, 2, 3$, be variables and $A[1, 4] = \bigoplus_{i=0}^{3} A[1, i] \oplus \alpha$ with any constant α so that variables in each column sum to a constant. Then, as shown in Fig. 3, we can see how the variables affect the internal state under the transformation of KECCAK-f round function $\mathsf{R} = \iota \circ \chi \circ \pi \circ \rho \circ \theta$. In Fig. 3 and hereinafter, the 2-tuple number "x, y" denotes the position of a lane at the initial state, and we track its position under the π function, where $0 \leq x, y \leq 4$. All bits of the lanes with orange slashes have algebraic degree 1, those of the lanes in orange have algebraic degree at most 1, and the other lanes are all constants. Note the algebraic degrees will *not* be affected by the linear operations θ, ρ, π, and ι. The only non-linear operation is the χ, and its degree is 2 or 3 in forward or backward directions, respectively. As shown in the third state in Fig. 3, each row contains a single bit of degree 1 and the other 4 bits are constants. Since the only possibility for χ to increase the algebraic degree is through two neighbouring bits due to the term $(a_{i+1} \oplus 1) \cdot a_{i+2}$, the algebraic degree of the state bits remains at most 1 after χ, *i.e.*, after one round function R. The size of free variables can be at most 4 lanes, *i.e.*, $64 \times 4 = 256$ bits.
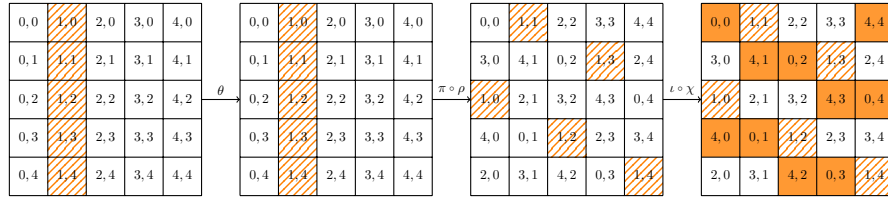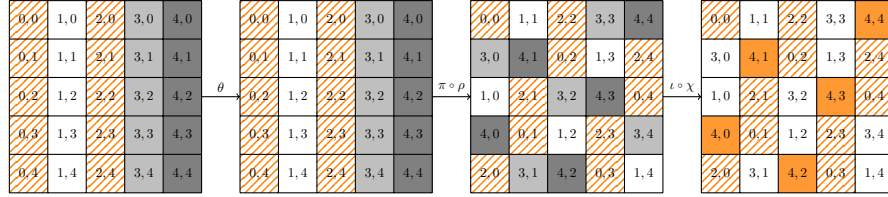

Fig. 3: Keeping the 1st forward round being linear with the degrees of freedom up to 256, with orange bits of degree at most 1, and white bits being constants.

Noting that the only nonlinear part of R is χ which operates on each 5-bit row. Since there is at most 1 variable in each row as in the first state in Fig. 3, the inverse function χ^{-1} is linear on these variables due to Setting 3. Thus, the

first inverse round R^{-1} is linear on these variables. This property was first used to construct zero-sum distinguishers on KECCAK-f in [2].

Increasing the degrees of freedom up to 512 for 2 rounds. Let $A[i, j]$ for $i = 0, 2$ and $j = 0, 1, 2, 3$ be variables and $A[i, 4] = \bigoplus_{j=0}^{3} A[i, j] \oplus \alpha_i$ with constants α_i for $i = 0, 2$. Fig. 4 shows how the variables propagate in one round R for $\alpha_0 = 0$ and $\alpha_2 = \texttt{0xff}\cdots\texttt{f}$. The bits of the lanes in gray (resp. lightgray) are set to all 1's (resp. 0's), and the bits of white lanes are set to arbitrary constants. The lanes with orange slashes or orange have algebraic degree at most 1 as above. Since there are at most two variables in each row input to χ and the variables are not adjacent, the outputs of χ are all linear on these variables. Therefore, the algebraic degree of the state bits in these variables remains 1 after the first round of KECCAK-f permutation, and the size of free variables can achieve at most $64 \times 4 \times 2 = 512$. This is also true for other constants α_i.

Fig. 4: Keeping the 1st forward round being linear with the degrees of freedom up to 512, with orange bits of degree at most 1, and gray, lightgray and white bits being values 1, 0, and arbitrary constants, respectively.

To keep the algebraic degrees to be at most one when χ^{-1} is applied (inverting one round) to the 512 variables in the first state of Fig. 4, according to Setting 4, we restrict the bits of gray lanes to be all ones and the bits of lightgray lanes to be all zeros, where the bits in gray and lightgray lanes respectively correspond to b_{i+4}'s and b_{i+3}'s in Setting 4. Note that the step ι only adds a constant to the first lane and thus it does not affect the gray and lightgray lanes. In this case, the first inverse round R^{-1} is linear on these 512 variables.

4.2 How to keep 3 rounds being linear

Based on the technique above, in this section we describe a technique for keeping an additional forward round of KECCAK-f being linear.

Let $A[i, j]$ with $i = 0, 2$ and $j = 0, 1, 2$ be variables. In what follows, we show how to impose some conditions on the input bits such that all the output bits after two rounds forward are linear. To make sure that the variables do not affect the values of the other bits after step θ of the first round, *i.e.*, keeping the sum of all columns to be zero constants, we impose the following 2×64 equations:

$$A[i, 0] \oplus A[i, 1] \oplus A[i, 2] = 0, \ i = 0, 2.$$

The values of white lanes are set in such a way that the value of the gray and lightgray lanes remained unchanged after step θ of the first round, as shown in Fig. 5. The steps ρ and π are respectively shifts of the bits in the same lanes and permutations of the positions of the lanes. After the steps χ and ι, the lane at column 0 and row 0 equals $A[0,0] \oplus A[2,2]_{\lll 43}$, the other lanes in orange remain unchanged up to constants, and the white lanes are all constants. To make sure that the variables do not propagate after step θ of the second round, we impose 3×64 more equations:

$$A[2,0]_{\lll 62} = A[0,0] \oplus A[2,2]_{\lll 43},$$
$$A[2,1]_{\lll 6} = A[0,1]_{\lll 36},$$
$$A[2,2]_{\lll 43} = A[0,2]_{\lll 3}.$$

Note that this result is still valid when constants are XORed to the above three equations. Since this linear system has in total $5 \times 64 = 320$ equations and $6 \times 64 = 384$ variables, there remains 64 degrees of freedom. As shown in Fig. 5, we can see that after the second round all the output bits are linear since no adjacent bits contain variables before step χ of the second round.
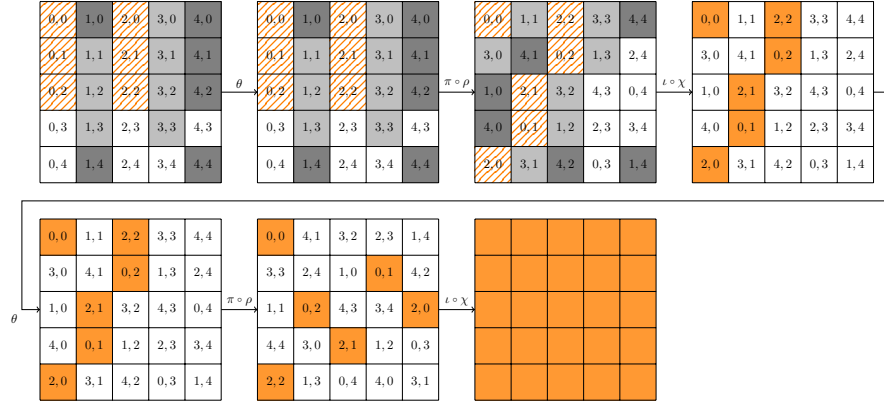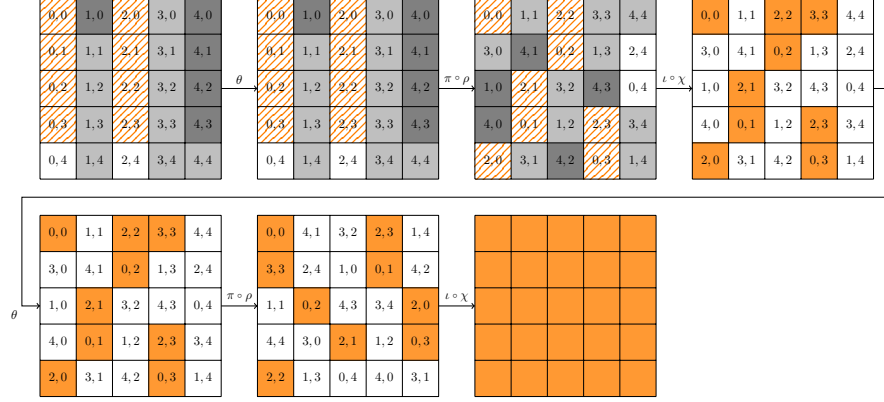

Fig. 5: Keeping the 2nd forward round being linear with degree of freedom up to 64

To ensure that the inverse function χ^{-1} is linear, we restrict the bits of lanes $A[4,j]$ with $j = 0,1,2$ to be all ones and the bits of lanes $A[3,j]$ with $j = 0,1,2$ to be all zeros as in Setting 4.

Increasing the degrees of freedom to up to 128. Similarly, we can increase the degrees of freedom from 64 to 128 by setting $A[i,j]$ with $i = 0,2$ and $j = 0,1,2,3$ be variables and imposing some conditions on the input bits as shown in Fig. 6. We build a linear system of $6 \times 64 = 384$ equations on $8 \times 64 = 512$ variables which has 128 degrees of freedom and satisfies that the output bits after

the second round are all linear. To ensure that the inverse function χ^{-1} is linear, we restrict the bits of lanes $A[4,j]$ with $j = 0, 1, 2, 3$ to be all ones and the bits of lanes $A[3,j]$ with $j = 0, 1, 2, 3$ to be all zeros.
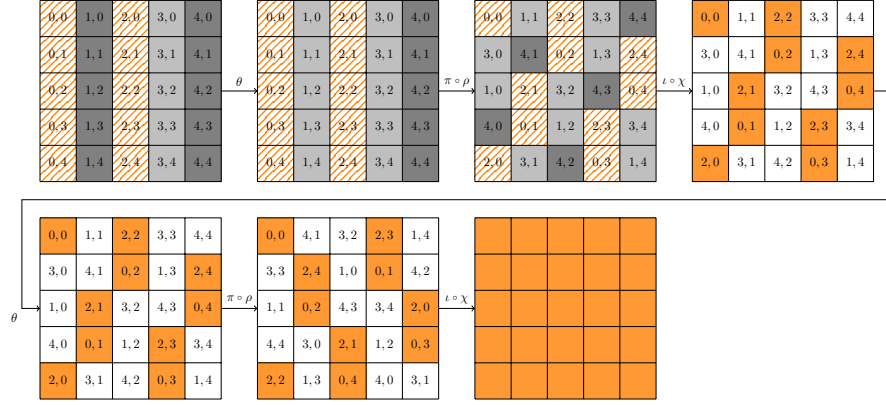

Fig. 6: Keeping the 2nd forward round being linear with the degree of freedom up to 128

Increasing the degrees of freedom to up to 194. We further extend the degrees of freedom to 194 by setting $A[i,j]$ with $i = 0, 2$ and $j = 0, 1, \cdots, 4$ be variables and imposing some conditions on the input bits as shown in Fig. 7. We build a linear system of $7 \times 64 = 448$ equations on $10 \times 64 = 640$ variables which has 194 degrees of freedom and satisfies that the output bits after the second round are all linear. Note that there are two linear equations linearly dependent on the other equations, so the degree of freedom is 194 instead of 192. To ensure that the inverse function χ^{-1} is linear, we restrict the bits of lanes $A[4,j]$ with $j = 0, 1, \cdots, 4$ to be all ones and the bits of lanes $A[3,j]$ with $j = 0, 1, \cdots, 4$ to be all zeros.

In summary, we found linear structures of KECCAK-f permutation reduced to 2 rounds with degree of freedom up to 512, and 3 rounds with degree of freedom up to 194.

5 Zero-Sum Distinguishers

A zero-sum distinguisher for a function is a method to find a set of values summing to zero such that their respective images also sum to zero. That is, it is a method to find a set S such that $\sum_{x \in S} x = 0$ and $\sum_{x \in S} f(x) = 0$ for the function f. It is well known that the d-th order derivative of a polynomial with degree at most d is a constant. For a Boolean function of algebraic degree at most d, its d-th order derivative is also a constant. Thus the outputs of a Boolean function of degree

Fig. 7: Keeping the 2nd forward round being linear with the degree of freedom up to 194

at most d sum to zero when the inputs take over a linear space of dimension at least $d+1$.

The KECCAK-f permutation is the core function of KECCAK and `SHA-3`. The known method for constructing zero-sum distinguishers on KECCAK-f permutation, exploits the fact that adding a round in KECCAK-f only doubles the degree of the algebraic expression of the output bits in terms of the input bits, and only triples the degree of the algebraic expression of the input bits in terms of the output bits. This is due to that the algebraic degree of one KECCAK-f round is 2 and the algebraic degree of one inverse round is 3. The real zero-sum distinguisher starts from some middle round of the KECCAK-f permutation, and extends n rounds forward and m rounds backward. So the algebraic degree of n forward rounds R^n is bounded by 2^n, and m backward rounds R^{-m} by 3^m. With a linear space S_M from the middle round of size at least $2^{1+\max(2^n,3^m)}$, one can be ensured that both input and output sum to zero, *i.e.*, $\sum_{x \in S_M} \mathsf{R}^n(x) = 0$ and $\sum_{x \in S_M} \mathsf{R}^{-m}(x) = 0$. The desired input space S of the $(m+n)$-round distinguisher can be obtained by $S = \{\mathsf{R}^{-m}(x) \mid x \in S_M\}$.

The attack has been extended in two different directions, finding better bounds of the algebraic degrees of R^n and R^{-m} [9, 16], and inserting rounds in the starting point in the middle [2]. Our improved zero-sum distinguisher is in line with the second approach. Aumasson and Meier showed in [2] that one round could be inserted for free. This is achieved by carefully choosing the set S_M so that the algebraic degree keeps to be 1 after one round. It becomes obvious to note the linear structures presented in Section 4 could be used here to extend the number of free rounds to *three*, *i.e.*, with linear structures as S_M (similar to the way how *initial structures* are used in MITM preimage attacks [1, 18]), the algebraic degrees of one backward round R^{-1} and two forward rounds R^2 are kept to be 1.

$$\overset{m+1}{\underset{\text{backward}}{\longleftarrow}} \Big| \overset{2+n}{\underset{\text{forward}}{\longrightarrow}}.$$

As such, with the same complexity $2^{1+\max(2^n,3^m)}$, our improved distinguisher works for $(m+n+3)$ rounds, *i.e.*, $(m+1)$ rounds backward and $(n+2)$ rounds forward. In Table 1, we summarize our results with the best combinations of m and n. Note the number of attacked rounds is limited by the size of S_M, a.k.a., the size of the linear structures. For instance, the largest space we found for 3-round linear structure is 2^{194}, so the distinguisher works for all combinations of m and n such that $2^{1+\max(2^n,3^m)} \leq 2^{194}$. When $m=4$ (5 rounds backward) and $n=7$ (9 rounds forward), as stated in the last entry of the third column of Table 1, the attack applies to $m+n+3=14$ rounds with time/data complexity $2^{1+\max(2^n,3^m)} = 2^{1+\max(2^7,3^4)} = 2^{129} \leq 2^{194}$.

As a trade-off, the size of linear structure could be larger for less rounds, *e.g.*, up to 2^{512} for 2 rounds. So the distinguisher works as below

$$\overset{\underset{\text{backward}}{\longleftarrow \quad m+1 \quad}}{\Big|} \overset{\underset{\text{forward}}{1+n \quad \longrightarrow}}{\Big|}.$$

While there is one free round less, we can afford larger complexities, *e.g.*, with $m=5$ and $n=8$, we can distinguish $m+n+2=15$ rounds with complexity $2^{1+\max(2^n,3^m)} = 2^{257}$. Results of other choices of (m,n) are listed in the second column of Table 1.

As a direct application to the 12-round KECCAK-f permutation used in the CAESAR candidate KEYAK [8], the 3-round linear structure is large enough and the choice of $(m=3, n=6)$ results in attack complexity 2^{65}. KETJE [7] uses a 12-round KECCAK-f permutation reduced to 400 bits (denoted as KECCAK-$p[400, n_r = 12]$), by reducing the length of lanes from 64 to 16 bits. When we project the zero-sum distinguisher to this small variant, the maximum sizes of linear structures are reduced to $512/4 = 128$ and $192/4 = 48$ bits respectively for 2 and 3 rounds. While the size for the 3-round linear structure is insufficient for distinguishing 12 rounds, the 128-bit 2-round linear structure makes it eligible with complexity 2^{82}. We note that though our distinguishers work for 12-round KECCAK-f, they do not result in attacks in settings of authenticated cipher against KEYAK or KETJE.

In summary our improved zero-sum distinguishers work for up to 15 rounds, and for up to 11 rounds with practical complexities.

Experiments. We have made an experiment for verifying our distinguishers on KECCAK-f permutation reduced to 7 rounds in the forward direction. We use the structure with degrees of freedom up to 64 as shown in Section 4.2. Note that all the bits of the 7-round output have algebraic degree at most $2^{7-2} = 32$ for this structure. It is sufficient to use a 33 dimensional space. In our experiment, 31 out of those 64 variables are first randomly valued and fixed, then the outputs are summed over all the possible values of the rest input variables. It turns out that all the 1600 bits of this sum are zeros.

6 Preimage Attacks

In this section, we exploit algebraic techniques to mount preimage attacks on several variants of KECCAK based on the properties of the Sbox χ and the linear structures of KECCAK-f permutation. The preimage attacks on SHA-3 are the same except that the time complexity may be at most 2^2 larger in some cases due to the two extra padding bits. In general, here we find preimages of message with length $\leq r - 2$ bits by setting the $(r-1)$-th bit of the input state to be 1 so that the padded message is one block, unless the degree of freedom is insufficient. We choose the message in such a way that the internal states of the first few rounds follow linear structures as presented in Section 4 and the χ of the last round is inverted by the methods presented in Section 3. To achieve smallest possible time complexities, we will use different linear structures, and different methods inverting the χ for each instance of KECCAK. Note, the first $r - 1$ bits of the input to KECCAK-f can be chosen freely by choosing the proper message bit values. However, the last $c = b - r$ bits could not be chosen since there is no addition of message bits, so we can only choose "variables" of linear structures from the first $r - 1$ bits, and this is why we must use different linear structures for different instances. In what follows, we present the preimage attacks by showing the choice of linear structures, ways to invert the Sbox, followed by a complexity analysis of each instance attacked. The basic idea of our attacks is to set up and solve linear equations. The complexity in this section is measured by the number of times for solving the linear system of equations.

6.1 Preimage attacks on 2-round KECCAK
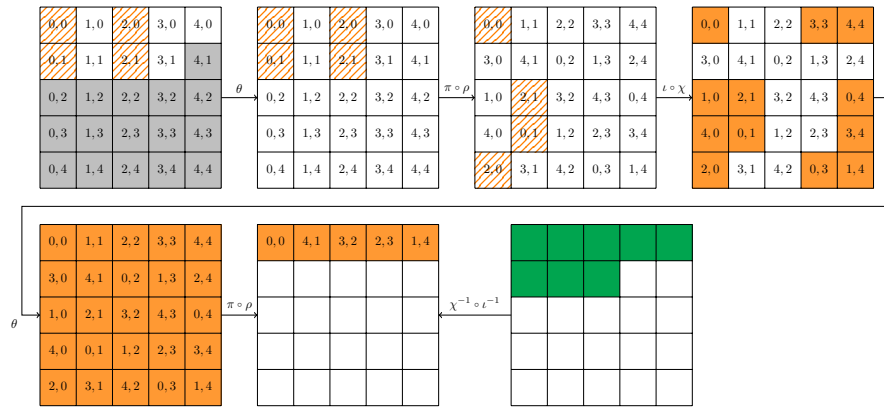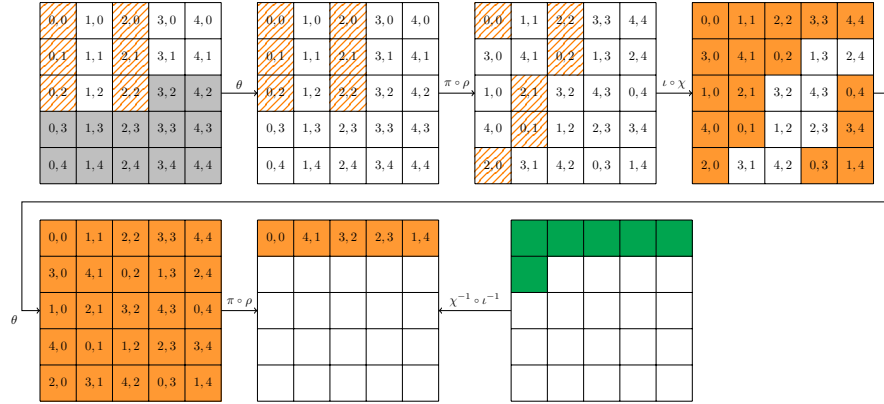

Fig. 8: Preimage attack on 2-round KECCAK-512

First we discuss the preimage attacks on KECCAK reduced to 2 rounds. They follow 1-round linear structures, plus 1-round inversion of the Sbox. These attacks

adopt some similar ideas of meet-in-the-middle [23], while they exploit the linear structures of KECCAK. For 2-round KECCAK-512, we execute the attack as follows (depicted in Fig. 8):

1. Invert the first 320 bits of a given hash value h through $\chi^{-1} \circ \iota^{-1}$. Note these bits form the full output of the 64 Sboxes in the first row, so the corresponding input bits can be fully determined.
2. Randomly guess the values of the lanes in white of the state input to the first round, as shown in Fig. 8, where the 1024 bits of the lanes in lightgray are set to all zeros and the last bit of $A[3, 1]$ is set to 1 such that the state input to the first round satisfies the padding rule;
3. For each guess, we set $A[0, 1] = A[0, 0] + \alpha_0$ and $A[2, 1] = A[2, 0] + \alpha_2$ with random constants α_0, α_2, build a linear system between $A[0, 0]$, $A[2, 0]$ and the recovered 320 input bits of the χ in the second round, then solve this system and check whether the resulted hash value is correct.

Since $A[0, 0]$ and $A[2, 0]$ have 128 bits, so we have a complexity gain over bruteforce of 2^{128}, *i.e.*, $2^{512-128} = 2^{384}$ for 2-round KECCAK-512 preimage attack. Note the degree of freedom in our setting is sufficient to find a preimage eventually. There are 128 bits from $A[0, 0]$ and $A[2, 0]$, 319 bits from white lanes, and 128 bits from α_0 and α_2, which sums to 575 bits, larger than the required 512 bits.

Fig. 9: Preimage attack on 2-round KECCAK-384

For the 2-round KECCAK-384, the attack is similar to that for KECCAK-512, except that we can construct linear structure from $r = 1600 - 2 \times 384 - 1 = 831$ bits instead of 575 bits for KECCAK-512. We can obtain a linear structure of 256-bit variables from $(A[0, 0], A[0, 1], A[2, 0], A[2, 1])$ with $A[0, 2] = A[0, 0] \oplus A[0, 1] \oplus \alpha_0$ and $A[2, 2] = A[2, 0] \oplus A[2, 1] \oplus \alpha_2$, hence a linear system of 256-bit equations, as shown in Fig. 9. For generating a message satisfying the padding rule, we just need a solution with the last bit of $A[2, 2]$ being 1. Therefore, the time complexity of this attack is $2^{384-256+1} = 2^{129}$.

Noting that we can obtain 4 linear equations on the input bits given 4 output bits of the 5-bit Sbox χ. We can also apply the above preimage attack to 2-round KECCAK-256, by solving the system of linear equations just once, *i.e.*, with time complexity 1. As a feature of sponge functions, all other variants with digest size less than 256 bits could be attacked in exactly the same way by randomly presetting the extra digest bits not outputted.

6.2 Preimage attacks on 3-round KECCAK

Next, we show preimage attacks on several instances of KECCAK reduced to 3 rounds.

Preimage attacks on 3-round SHAKE128. SHAKE128(M, ℓ) is an instance of SHA-3 standard defined from KECCAK[$r = 1344, c = 256$], with unlimited output length ℓ. We focus on the preimage attack on SHAKE128(M, 128), denoted by SHAKE128 hereinafter for simplicity.
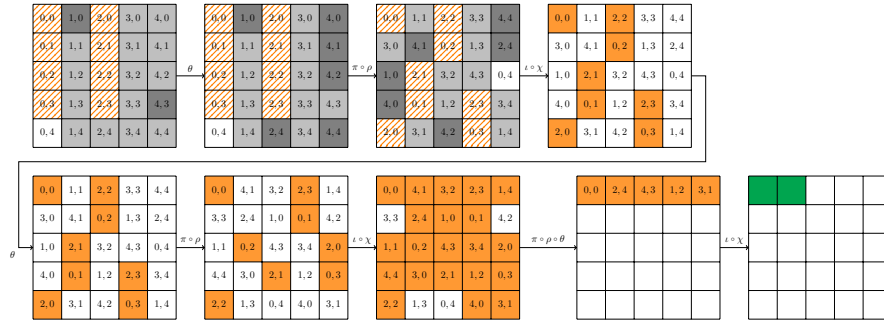

Fig. 10: Preimage attack on 3-round SHAKE128

Similar to that in Section 4.2, we set $A[i, j]$ with $i = 0, 2$ and $j = 0, 1, 2, 3$ being variables, and impose some conditions on the input bits such that all the output bits after two rounds are linear, as shown in Fig. 10. $A[0, 4]$ is set to any constant such that M is a legal message. The lanes in gray and lightgray are set to all ones and all zeros. To make sure that all the output bits after two rounds are linear, we require:

$$A[0,0] \oplus A[0,1] \oplus A[0,2] \oplus A[0,3] = A[0,4] \oplus \texttt{0xff}\cdots\texttt{f},$$
$$A[2,0] \oplus A[2,1] \oplus A[2,2] \oplus A[2,3] = \texttt{0xff}\cdots\texttt{f},$$
$$A[2,0]_{\lll 62} = A[0,0] \oplus A[2,2]_{\lll 43},$$
$$A[2,1]_{\lll 6} = A[0,1]_{\lll 36} \oplus A[2,3]_{\lll 15},$$
$$A[2,2]_{\lll 43} = A[0,2]_{\lll 3},$$
$$A[2,3]_{\lll 15} = A[0,3]_{\lll 41} \oplus A[2,0]_{\lll 62}.$$

All these 6×64 linear equations are linearly independent and thus have 2^{128} solutions. We expect that there is one solution matching the given 128-bit hash value. Since $\pi \circ \rho \circ \theta$ is linear, the bits input to χ of the last round are all linear on the variables. For SHAKE128, the first two output bits of each 5 bits of the 64 Sboxes χ in the first row of the last round are known. According to the properties of χ as shown in Table 4, we can set up 1 linear equation for each Sbox, hence 64 linear equations in total between the input bits to the Sboxes of the last round and hash value. There are two methods to obtain extra 64 linear equations, as shown in Section 3.1, including guess-and-determine technique in Setting 1 and probabilistic linearization in Setting 2. For the former, we guess 32 bits input to χ of the last round and obtain 64 more linear equations, which will find the correct solution in 2^{32}. For the latter, we exploit the probabilistic equations $b_i = a_i$'s each of which holds with probability 0.75. Since we have 64 probabilistic equations, the total probability of this system is $0.75^{64} = 2^{-26.6}$. We can expect a correct solution from $2^{26.6}$ such systems which can be obtained by changing the values of $A[0, 4]$. Thus the complexity of this attack is $2^{26.6}$.

Preimage attacks on 3-round KECCAK $[r = 1440, c = 160, \ell = 80]$. Similar techniques as presented previously allow us to find solutions for the 3-round preimage challenge with width 1600 in the KECCAK Challenge [4]. As shown in Fig. 11, we set the lanes with orange slashes of the first state to be variables. The 31st bit of $A[2, 4]$ is set to 1 for ensuring that the state input to the first round complies with the padding. Finally, we get 161 degrees of freedom such that the bits input to χ of the last round are all linear. The sketch of the processing is shown in Fig. 11. According to the properties of χ as presented in Section 3.1, we can set up 16 linear equations between the bits input to the last χ and hash value. We can obtain extra 2×64 linear equations by guessing 64 bits input to the last χ. Now, we build a linear system of $16 + 2 \times 64 = 144$ equations on 161 variables. Therefore, we immediately get correct solutions for any given hash value by solving this system. A solution for the 3-round preimage challenge with width 1600 is listed as below, where the message has length 1438 and each 64-bit word is expressed in hexadecimal.
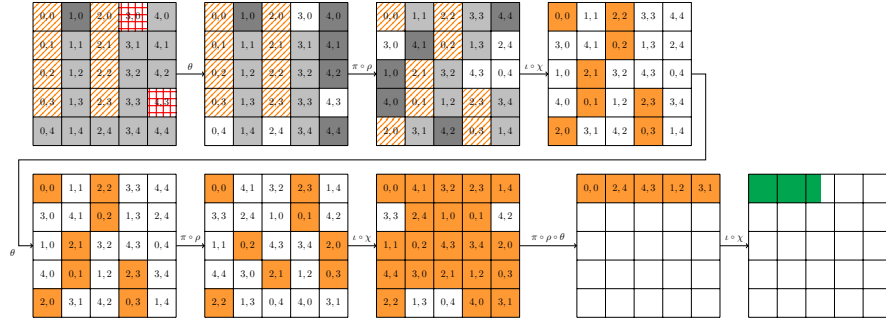
Challenge:
```
e7cfc02846a32506 756c
```

Preimage:
```
01e0bc766796d36f ffffffffffffffff bd25fc21a299814e 0000000000000000 0000000000000000
cc85265f6f0e696a ffffffffffffffff 3a6f339c0eb075b9 0000000000000000 0000000000000000
d22ac7903b459dc2 ffffffffffffffff 903a19e9986a2ac7 0000000000000000 0000000000000000
539674b5f5e23187 ffffffffffffffff 1770d654e35ec89e 0000000000000000 0000000000000000
b326d6f339c0e9bf ffffffffffffffff d71d16ae
```

Preimage attacks on 3-round KECCAK $[r = 640, c = 160, \ell = 80]$. Similar techniques also allow us to find solutions for the 3-round preimage challenge with width 800. The sketch of the attack is shown in Fig. 12. To keep two rounds
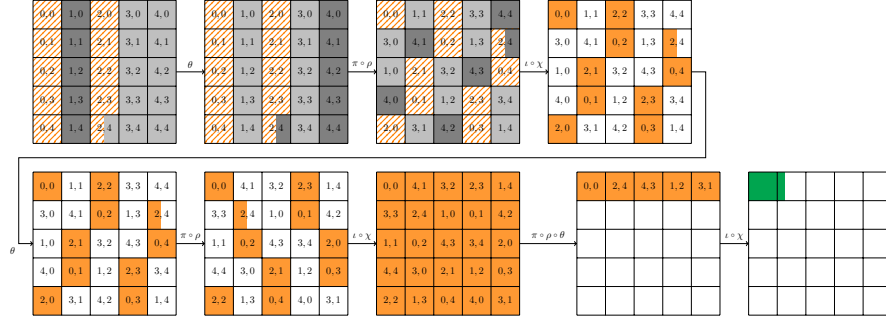
Fig. 11: Preimage attack on 3-round KECCAK$[r = 1440, c = 160, \ell = 80]$

being linear, the six lanes with orange slashes of input state are expressed by 64 variables for any fixed values of auxiliary variables, and the two lanes with red grid, $A[3,0]$ and $A[4,3]$, are represented by 32 auxiliary variables. We set up 64 linear equations on 64 variables for a given 80-bit hash value by guessing 8 bits of the variables, and expect a correct preimage for 2^{16} tries. The time complexity of this attack is 2^{24}. As a matter of fact, the time complexity can be further cut down to 2^7 by applying a similar attack as described in Section 6.4. A solution for the 3-round preimage challenge with width 800 is listed as below, where the message has length 638.

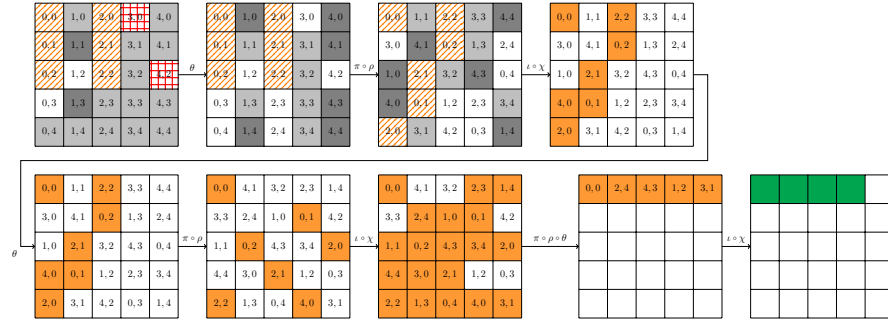**Fig. 12:** Preimage attack on 3-round KECCAK$[r = 640, c = 160]$

Challenge:

`0e668099c5b57b00 9302`

Preimage:

`ffffffff1097e68a 069e5c9097c2a342 9128124400000000 3bc3a3a300000000 0000000000000000`

`0000000056ace9cb 00000000cb56ace9 2ba3ccb200000000 990fc4d300000000 ff2c346d00000000`

Preimage attacks on 3-round KECCAK-224 **and** KECCAK-256**.** Since the rates r of KECCAK-224 and KECCAK-256 are much smaller than that of SHAKE128, there are less choices of constant part when keeping two rounds being linear with as many degrees of freedom as possible. For KECCAK-256, we use 384 variables $A[i,j]$, $i = 0, 2, j = 0, 1, 2$, out of which there will be 64 independent variables after forcing the sum of variables in column $0, 2$ of the input to θ in the first round, and in column $0, 1, 2$ of the input to the θ in the second round to be constants as depicted in Fig. 13, *i.e.*, the size of this linear structure is 2^{64}. However, it is insufficient to match a 256-bit hash value by 64-bit variables. To get enough choices for the state input to the first round, we set the constant part by using 128 auxiliary variables $A[3,0]$ and $A[4,2]$ such that the linear structure remains linear after two rounds for any fixed values of auxiliary variables. As depicted in Fig. 13, we required that the gray and lightgray lanes of the state after step θ of the first round are respectively ones and zeros. To achieve this, we first fix the values of $A[0,3]$ and $A[3,0]$, and then set up 192 linear equations, $\bigoplus_{j=0}^{4}(A[i-1,j] \oplus (A[i+1,j] \lll 1)) = \mathtt{0xff\cdots f}, i = 1, 4$ and $\bigoplus_{j=0}^{4}(A[i-1,j] \oplus (A[i+1,j] \lll 1)) = 0, i = 3$, which implies that $A[4,2]$ is determined by $A[3,0]$. To make sure that the variables do not affect the other bits after step θ of the second round, we impose 192 more equations according to the value of $A[1,2]$. Finally, the six lanes with orange slashes of input state can be expressed by 64 variables for any fixed values of auxiliary variables, and the two lanes with red grid can be represented by 64 auxiliary variables. As usual, we can set up 64 linear equations on these 64 variables for a given 256-bit hash value. Since there are 2^{64} choices for variable lanes, 2^{64} choices for auxiliary variable lanes, and 2^{128} choices for constant lanes, we have 2^{256} choices for the state input to the first round, and we expect a correct solution. The time complexity of this attack is 2^{192}.

Fig. 13: Preimage attack on 3-round KECCAK-256

The preimage attack on KECCAK-224 is similar, as shown in Fig. 14. To keep two rounds being linear, the eight lanes with orange slashes of input state are expressed by 128 variables for any fixed values of auxiliary variables, and the

four lanes with red grid are represented by 64 auxiliary variables. We set up 128 linear equations on 128 variables for a given 224-bit hash value (half solutions correspond to legal messages), and expect a correct preimage for 2^{97} tries. The time complexity of this attack is 2^{97}.
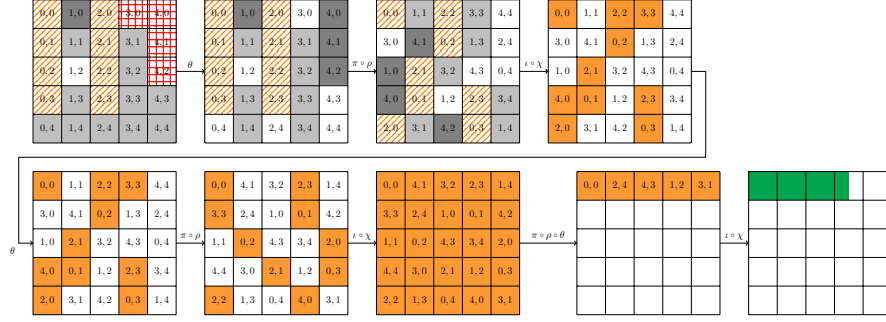

Fig. 14: Preimage attack on 3-round KECCAK-224

6.3 Preimage attacks on 3-round KECCAK-384/512 and 4-round KECCAK-224/256

For 3-round KECCAK-512, on one hand, we have 128 variables such that the bits input to step χ of the second round are all linear, as depicted in Fig. 8; on the other hand, we can directly inverse 320 bits through $\chi^{-1} \circ \iota^{-1}$ from a given hash value, each bit of which is a sum of 11 bits of the output of the second round. Since $\pi \circ \rho$ just permutate the positions of the bits and ι just add a constant to the first lane, they do not increase the nonlinear terms, and thus we neglect these steps in the last one and a half rounds.

$$M \xrightarrow[\text{1.5 rounds}]{\pi \circ \rho \circ \theta \circ \mathsf{R}} A \xrightarrow{\iota \circ \chi} B \xrightarrow{\theta} C \xrightarrow{\pi \circ \rho} | \xleftarrow{\chi^{-1} \circ \iota^{-1}} h.$$

The expressions of θ and χ are given as follows,

$$
\begin{aligned}
\chi &: \ B[x][y][z] = A[x][y][z] \oplus (A[x+1][y][z] \oplus 1) \cdot A[x+2][y][z], \\
\theta &: \ C[x][y][z] = B[x][y][z] \oplus \bigoplus_{y'=0}^{4} B[x-1][y'][z] \oplus \bigoplus_{y'=0}^{4} B[x+1][y'][z-1].
\end{aligned}
\tag{14}
$$

Since the bits input to step χ of the second round are all linear, each output bit of the second round is quadratic and the quadratic part is a product of two linear combinations. Note that the quadratic parts of $B[x][y][z]$ and $B[x-1][y][z]$ share a common factor $A[x+1][y][z]$ according to (14). We linearize $C[x][y][z]$ by guessing 10 bits input to step χ. That is, we obtain $11 = 1 + 10$ linear equations

and match 1 bit of the hash value. As such, we can match $\lfloor\frac{128}{11}\rfloor = 11$ bits of the hash value since we have 128 variables. The time complexity of this preimage attack is 2^{501}.

For 3-round KECCAK-384, we set the last bit of $A[2,2]$ to be 1 and have 255 variables such that the bits input to step χ of the second round are all linear, as depicted in Fig. 9, and thus the time complexity of the preimage attack is $2^{384-\lfloor\frac{255}{11}\rfloor} = 2^{361}$.

For KECCAK-224/256, we cannot inverse the hash value through χ^{-1} as KECCAK-384/512, but we can set up the equations such as $a_0 = b_0$ for $b_1 = 1$ according to (6). Since we have 127 and 64 variables such that the bits input to step χ of the third round are all linear, as depicted in Fig. 14 and Fig. 13, the time complexities for 4-round KECCAK-224/256 are respectively 2^{213} and 2^{251}.

Improved preimage attacks on 3-round KECCAK-384 **and** KECCAK-512.
In the above attacks, we assume that all the guessed linear combinations are linearly independent. It is possible to cut down the time complexity if elaborately choose linearly dependent ones, since there will be more degrees of freedom for guessing more linear combinations to match more bits of the hash value. For 3-round KECCAK-384/512, we can further improve the attacks by this method. Since we can inverse 320 bits of through $\chi^{-1} \circ \iota^{-1}$ from a given hash value, we can choose the bits which share a sum of one column (according to the property of θ) or common linear parts in quadratic terms (according to the property of χ).

By (14), $B[x-1][y][z]$ and $B[x][y][z]$ are linear after guessing the value of $A[x+1][y][z]$ for $0 \le y \le 4$. It is also true that $B[x+1][y][z-1]$ and $B[x+2][y][z-1]$ are linear after guessing the value of $A[x+3][y][z-1]$ for $0 \le y \le 4$. This means that after guessing the above 10 bits input to step χ, we not only linearize $C[x][y][z]$, but also obtain an extra equation:

$$C[x+1][y+1][z] = B[x+1][y+1][z] \oplus \bigoplus_{y'=0}^{4} B[x][y'][z] \oplus \bigoplus_{y'=0}^{4} B[x+2][y'][z-1],$$

the quadratic part of which only appears in $B[x+1][y+1][z]$. Thus we can set up 2 extra linear equations and match one more bit of the hash value by guessing one more bit. Totally we set up 13 linear equations and match two bits of the given hash value.

Then we consider another two equations:

$$C[x+2][y+2][z-1] = B[x+2][y+2][z-1] \oplus \bigoplus_{y'=0}^{4} B[x+1][y'][z-1] \oplus \bigoplus_{y'=0}^{4} B[x+3][y'][z-2],$$

$$C[x+3][y+3][z-1] = B[x+3][y+3][z-1] \oplus \bigoplus_{y'=0}^{4} B[x+2][y'][z-1] \oplus \bigoplus_{y'=0}^{4} B[x+4][y'][z-2].$$

Again, we can set up another 8 linear equations and match two more bits of the hash value by guessing 6 more bits.

Generally, we can match $2\lfloor\frac{t-5}{8}\rfloor$ bits of a given hash value if we have t variables. For 3-round KECCAK-384/512, we have 255 and 128 variables, and thus match 62 and 30 bits respectively. Therefore, the time complexities of this improved preimage attack are respectively 2^{322} and 2^{482} for 3-round KECCAK-384/512.

6.4 Improved preimage attacks on `SHAKE128`

The idea presented in Section 6.3 also applies to `SHAKE128`. In this section we extend it to improve the preimage attacks on `SHAKE128`.

Instead of linearizing 2 rounds forward, we linearize 2 rounds by combining one round forward and one round backward as discussed in Section 4.1, and we have 512 variables such that these two rounds are linear. To make sure that the state input to the first round corresponds to a legal message, we set up 262 linear equations such that the last 256 bits are all zeros and the following last 6 bits are all ones. Then there remains 250 degrees of freedom such that the bits input to step χ of the third round are all linear.

For 3-round `SHAKE128`, we set up 64 linear equations between these 250 variables and a given hash value as the same way done in Section 6.2, and then obtain extra 2×64 linear equations by guessing 64 bits input to step χ of the third round. Each solution of this linear system corresponds to a preimage of the given hash value. Therefore, the time complexity of this attack is 1.

For 4-round `SHAKE128`, given a 128-bit hash value, we expect 32 zeros and 32 ones among its last 64 bits (b_1's), and thus we can set up a linear system, which matches 22 bits (b_0's) of the hash value, by guessing 220 bits input to step χ of the third round. This attack gives a correct preimage in 2^{106}.

6.5 Preimage attacks on 4-round $\textsc{Keccak}[r = 1440, c = 160, \ell = 80]$

A similar attack as proposed in Section 6.4 also applies to $\textsc{Keccak}[r = 1440, c = 160, \ell = 80]$. In stead, we use two rounds forward and one round backward for linearization. As shown in Section 4.2, we have 194 degrees of freedom for such 3-round linear structure. To make sure that the state input to the first round corresponds to a legal message, we set up 161 linear equations such that the last 161 bits are fixed. Then there remains 33 degrees of freedom such that the bits input to step χ of the fourth round are all linear. Given an 80-bit hash value of 4-round $\textsc{Keccak}[r = 1440, c = 160, \ell = 80]$, we can set up 16 linear equations by (6), and set up 17 probabilistic equations using $b_i = a_i$. This attack gives a correct two-block preimage in $2^{47+17 \times 0.42} \approx 2^{54}$. We estimate that the computations of the whole attack need approximately 2^{20} CPU core hours. We run this attack in less than 2^{10} CPU core hours, and find a 78-bit matched preimage of length 2874 for the 4-round $\textsc{Keccak}$ preimage challenge with width 1600.

Message:

```
bc739847dd59b8f6 21e6f9016ae9292d 44c2f9f008f175fc fb1a9d7d2f5af0d9 c709f78dfa830460
0000000000000000 0000000000000000 0000000000000000 0000000000000000 0000000000000000
0000000000000000 0000000000000000 0000000000000000 0000000000000000 0000000000000000
0000000000000000 0000000000000000 0000000000000000 0000000000000000 0000000000000000
0000000000000000 0000000000000000 00000000
34d781770fae25d9 4bcdf7304704b1a0 aeb1cc6a3d9a4b9f 879b5b095e744910 09096232b744ac44
63faab93d1b6a3f5 7aca93b5c0c2afa0 f1b2772194934266 41e5a573d5efc16f 34e0e077bfb4ce43
48bb5cb11aa15738 3ecb466e4aa6fec3 4e3e5449626d5e2d ccec6be24c92d63b fb652d66cc6a4621
```

```
356d6bfdd56b1afb d9da9b8c0e366cd3 034ad6fdd9caa885 236ade6960c8edaf 03d6d60e45aeb00e
b8132036d4e20f33 8e4a29bbbd2c1cb8 8549b303
```

Output:
```
 7d aa d8 07 b0 50 6c 9c 02 76
```
Challenge:
```
 7d aa d8 07 f8 50 6c 9c 02 76
```
Difference:
```
 -- -- -- -- 48 -- -- -- -- --
```

7   Conclusions

In conclusion, we have described the linear structures of KECCAK-f and exploited them to analyze the security of KECCAK, including zero-sum distinguishers on KECCAK-f permutation and preimage attacks on KECCAK. Our distinguishers work on KECCAK-f reduced to up to 15 rounds, and are practical for up to 11 rounds. These results improve the previously best known distinguishers by two more rounds with the same complexities. Our preimage attacks work on all variants of KECCAK reduced to up to 4 rounds except for 4-round KECCAK-384/512, much faster than the exhaustive search. Specially, in terms of practical preimage attacks, we could find the preimage by solving a small linear system just once for 2-round KECCAK-224/256 and 3-round SHAKE128. With these techniques, we have found preimages for 3-round KECCAK Challenge with widths 1600 and 800, and a 78-bit matched preimage for 4-round KECCAK Challenge with width 1600. It will be interesting to see applications of linear structures to other KECCAK-like ciphers or functions.

Acknowledgement

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