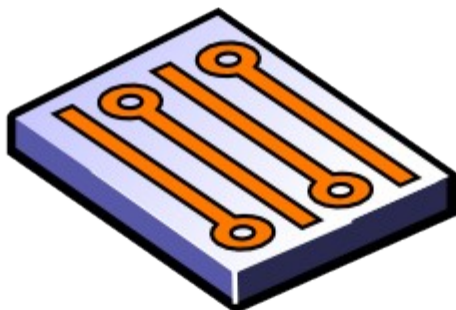


# KICAD

# PCBNEW



# LINUX & WINDOWS

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Version

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August 2011

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## 1 - Presentation

### Headings:

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### 1.1 - Description

PCBNEW is a powerful printed circuit board program, available for both the LINUX and WINDOWS operating systems.

It is used in association with the schematic capture software program EESCHEMA, which provides the **Netlist** file - this describes the electrical connections of the PCB to design.

A second program CVPCB is used to assign each component in the Netlist produced by EESCHEMA, to a module that is used by PCBNEW. This can be done either interactively or automatically using equivalence files.

PCBNEW manages libraries of modules. Each module is a drawing of the physical component including its **footprint** - the layout of pads providing connections to the component. The required modules are automatically loaded during the reading of the **Netlist** produced by CVPCB.

PCBNEW integrates, automatically and immediately, any circuit modification, by removal of any erroneous tracks, addition of the new components, or by modifying any value (and under certain conditions any reference) of the old or new modules, according to the electrical connections appearing in the scheme.

PCBNEW provides a rats nest display, a hairline connecting the pads of modules which are connected on the schematic. These connections move dynamically as track and module movements are made.

PCBNEW has an active **Design Rules Check** (DRC) which automatically indicates any error of track layout in real time.

PCBNEW can automatically generate a copper plane, with or without **thermal breaks** on the pads.

PCBNEW has a simple but effective **autorouter** to assist in the production of the circuit.

An Export/Import in **SPECCTRA dsn** format allows to use advanced autorouters.

PCBNEW provides options specifically for the production of **ultra high frequency** circuits (such as pads of trapezoidal and complex form, automatic layout of coils on the printed circuit...).

PCBNEW displays the elements (tracks, pads, texts, drawings...) as actual size and according to personal preferences:

- display in full or outline
- display of the track/pad clearance...

### 1.2 - Principal Design features

PCBNEW has an internal resolution of 1/10000 inch.

PCBNEW works on 16 layers of copper, plus 12 technical layers (silk screen, solder mask, component adhesive, solder paste, drawings and comments...) and manages in real time the hairline indication (rats nest) of missing tracks.

The display of the PCB elements (tracks, pads, text, drawings...) can be customised:

- In full or outline.
- With or without track clearance.
- by hiding certain elements (copper layers, technical layers, zones of copper, modules...), which is useful for high density multi-layer circuits

For the complex circuits, the display of layers, zones, components can be removed in a selective way for a better legibility of the screen.

Modules can be rotated to **any angle**, with a step of 0,1 degree.

Pads can be round, rectangular, oval or trapezoidal (the latter is necessary for the production of ultra high frequency circuits). In addition several basic pads can be grouped.

Both the size of each pad, and the layers where they appear, can be adjusted.

The drilling of holes can be offset.

PCBNEW can automatically generate copper planes, with automatic generation of thermal breaks around the pads concerned.

The Module Editor can be accessed from the PCBNEW toolbar. The Editor allows creation or modification of a module from the PCB or a library and then saved to either. A module saved to the PCB can be subsequently saved to a library. In addition all modules on the PCB can be saved to a library by creating a **footprint archive**.

PCBNEW generates in an extremely simple way all the documents necessary:

## Pcbnew

- Fabrication outputs:
  - Files for Photoplotters in GERBER RS274X format
  - Files for drilling in EXCELLON format
- Plot files in HPGL, SVG and DXF format
- Plot and drilling maps in POSTSCRIPT format
- Local Printout.

### 1.3 - Remark

PCBNEW requires a **3 button mouse**. The 3rd button is mandatory.

Finally it should be noted that the diagrammatic tool Eeschema and CVPCB are needed to create the required netlists.

## 2 - Installation

### Headings:

#### [2 - Installation](#)

##### [2.1 - Installation of the software](#)

##### [2.2 - Modifying the default configuration](#)

### 2.1 - Installation of the software

The installation procedure is described in the **kicad** documentation.

### 2.2 - Modifying the default configuration

A default configuration file: **kicad.pro** is provided in **kicad/share/template**. It is used as the initial configuration for all new projects.

This configuration file can be modified, generally to change the list of libraries.

To do this:

- Launch pcbnew using kicad or directly (something like c:\kicad\bin\pcbnew.exe).  
(Linux: run **/usr/local/kicad/bin/kicad** or **/usr/local/kicad/bin/pcbnew** if binaries are in /usr/local/kicad/bin).
- Select Preferences - Libs and Dir.
- Edit as required.
- **Save the modified configuration** (Save Cfg) to **kicad/share/template/kicad.pro**.



## 3 - General Operations

### Headings:

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### 3.1 - Access to the operations

One reaches the various operations by using:

- the menu bar (top of screen).
- the icons at the top of the screen (general orders)
- the icons on the right of the screen (specific orders or **"tools"**)
- the icons on the left of the screen (Display Options)
- the mouse buttons (provides menu options).

**In particular:**

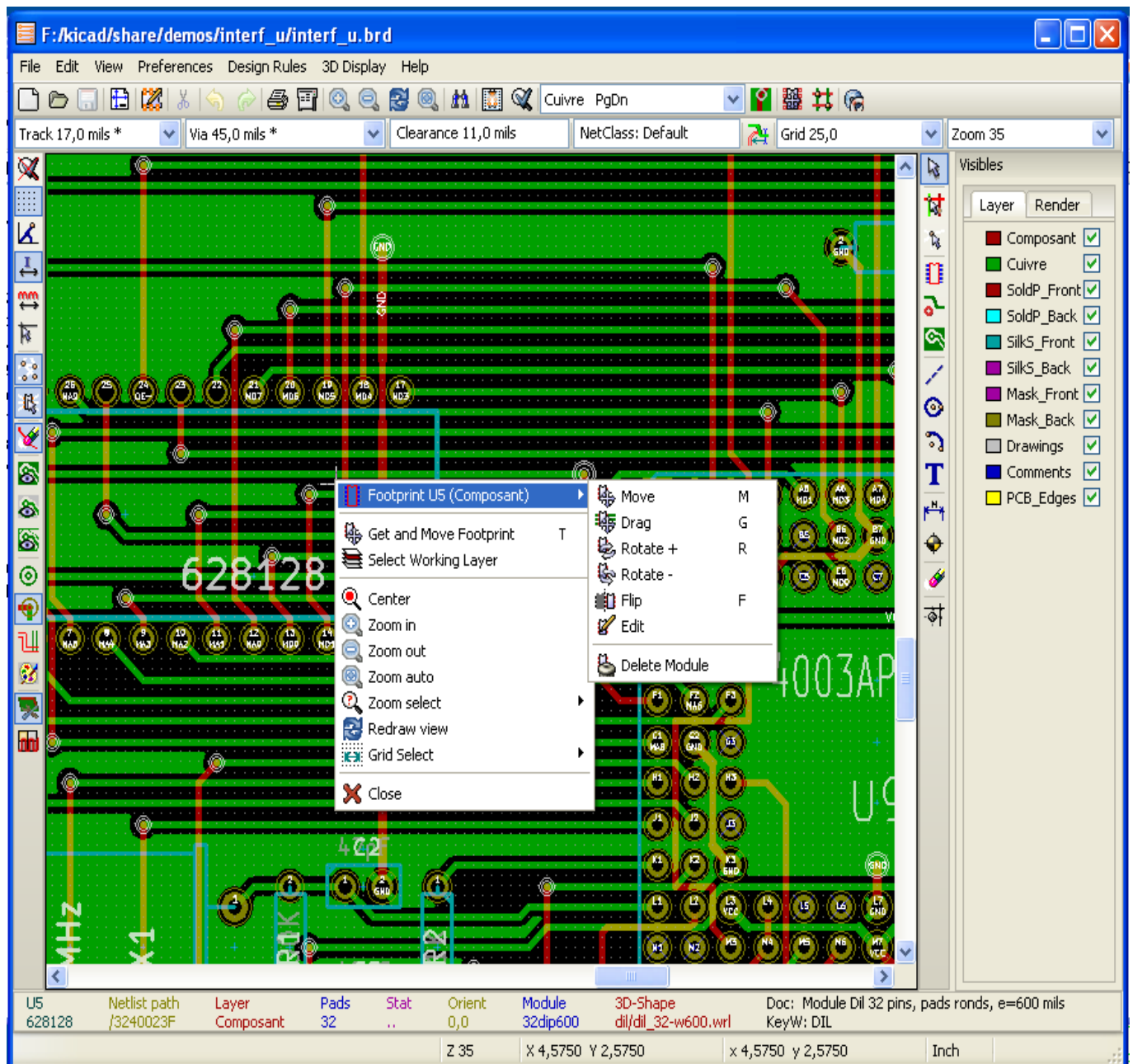
*The right-hand mouse button reveals a POP Up Menu the contents of which depend on the element under the cursor (Zoom, grid and editing the element).*

- the keyboard (Function keys **F1, F2, F3, F4, Shift, Delete, +, - Page Up, Page Down** and **"space"** bar).

**In particular:**

*the "Escape" key generally cancels an operation in progress.*

The screenshot below illustrates some of the possible accesses to the operations:



## 3.2 - Commands with the Mouse

### 3.2.1 - Basic commands

#### - Left Button:

- Single click: displays the characteristics of the module or text under the cursor to the lower status bar.
- Double click: displays the editor (if the element is editable) of the element under the cursor.

#### - Centre Button/Wheel:

- Rapid Zoom and some commands in layer manager. Consequently a 2 button mouse is unusable. Hold down the centre button and draw a rectangle to zoom to the described area. And rotation of a mouse wheel zooms in and out.

#### - Right Button:

- Displays a Pop-up Menu

### 3.2.2 - Operations on blocks

Operations to move, invert (mirror), copy, rotate and delete a block are all available on the pop-up menu. In addition the view can zoom to the area described by the block.

The framework of the block is traced by moving the mouse whilst holding down the left mouse button. The operation is carried out on releasing the button.

By holding down one of the keys "Shift" or "Ctrl", or both keys "Shift and Ctrl" together, whilst the block is drawn the operation invert, rotate or delete is automatically selected as shown in the table below:

*Summarised orders:*

Left mouse button held down	Trace framework to move block
Shift + Left mouse button held down	Trace framework for invert block
Ctrl + Left mouse button held down	Trace framework for rotating block 90°
Shift+Ctrl + Left mouse button held down	Trace framework to delete the block
Centre mouse button held down	Trace framework to zoom to block

When moving a block:

- Move block to new position and operate left mouse button to place the elements.
- To cancel the operation use the right mouse button and select Cancel Block from the menu (or press Esc key).

Alternatively if no key is pressed when drawing the block use the right mouse button to display the pop-up menu and select the required operation.

For each block operation a selection window enables the action to be limited to only some elements.

### 3.3 - Selection of grid size

The cursor during layout of elements moves on a grid, the display of the grid can be turned on or off using the icon on the left hand side toolbar.

Any of the pre-defined grid sizes, or a User Defined grid, can be chosen using the pop-up window, or the drop-down selector on the toolbar at the top of the screen. The size of the User Defined grid is set using the menu bar option Dimensions - User Grid Size.

### 3.4 - Adjustment of the ZOOM

To change the "ZOOM":

- Open the POP-Up window (using the right mouse button) and to select the desired zoom.
- Or use the function keys:  
**F1:** Enlarge (zoom in)  
**F2:** Reduce (zoom out)  
**F3:** Redraw the display  
**F4:** Centre view at the current cursor position
- Or rotate the mouse wheel.
- Or hold down the middle mouse button and draw a rectangle to zoom to the described area.

### 3.5 - Displaying the Cursor Coordinates

The cursor co-ordinates are displayed in inches (inch or ") or millimetres (mm) as selected using the 'I' or 'mm' icons on the left hand side toolbar.

Whichever unit is selected PCBNEW always works to a precision of 1/10,000 of inch.

The status bar at the bottom of the screen gives:

- The current zoom setting.
- The absolute position of the cursor.
- The relative position of the cursor. **Note the relative co-ordinates (x,y) can be set to 0,0 at any position by pressing the space bar.** The cursor position is then displayed relative to this new datum.

In addition the relative position of the cursor can be displayed using its polar co-ordinates (ray + angle). This can be turned on and off using the icon on the left hand side toolbar.



### 3.6 - Quick Commands using the keyboard (“Hot Keys”)

Many commands are accessible directly with the keyboard.

Selection can be either upper or lower case.

Most hot keys are shown in menus.

Some hotkeys that do not appears:

- Key **Delete** (or Del): Deletes a Module or track  
*(only if the Module tool or the track tool is active)*
- Key **V**, if the track tool is active Switches working layer or place via, if a track is in progress.
- Key **+** and **-**: Active layer = next or previous layer.
- Key **« ? »** to display the list off all hot keys.
- Key **« space »** pour reset relative coordinates.

### 3.7 - Operation on blocks

Operations to move, invert (mirror), copy, rotate and delete a block are all available on the pop-up menu. In addition the view can zoom to that described by the block.

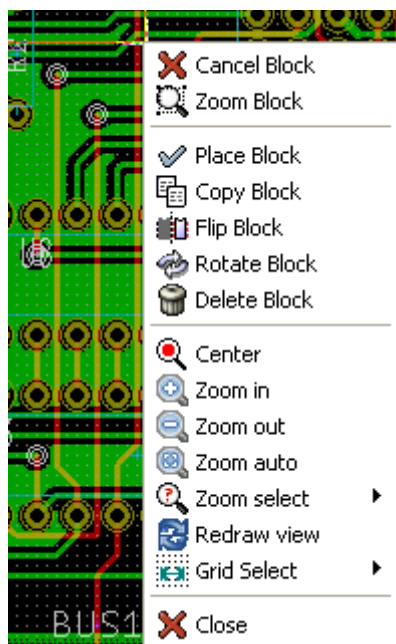
The framework of the block is traced by moving the mouse whilst holding down the left mouse button. The operation is carried out on releasing the button.

By holding down one of the keys “Shift” or “Ctrl” or both “Shfit and Ctrl” together or “Alt”, whilst the block is drawn the operation invert, rotate,delete or copy is automatically selected as shown in the table below:

Left mouse button held down	move block
Shift + Left mouse button held down	invert (mirror) block
Ctrl + Left mouse button held down	rotate block 90°
Shft+Ctrl + Left mouse button held down	delete the block
Alt + Left mouse button held down	copy the block

If move block is initially selected (no keys held down), one of the alternative options can be chosen by displaying the pop-up menu using the right mouse button.

Any of the commands above can be cancelled via the same pop-up menu or pressing the Escape key (Esc).



### 3.8 - Units used in dialogs:

Units used to display dimensions values are:

- inch
- mm



according to the selection made by icon in left toolbar

However one can enter the unit used to define a value, when entering a new value.

Accepted units are:

1in	(1 inch)
1"	(idem)
25th	(25 thou)
25mi	(25 mils, same as thou)
6mm	(6 mm, obviously)

The rules are:

- Spaces between the number and the unit are accepted.
- Only the first two letters are significant.

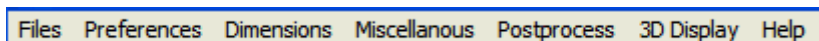
#### Remark:

In countries using an other decimal separator than the period (.), one can also use the period (.) as a decimal point, substituting it with the correct locale character.

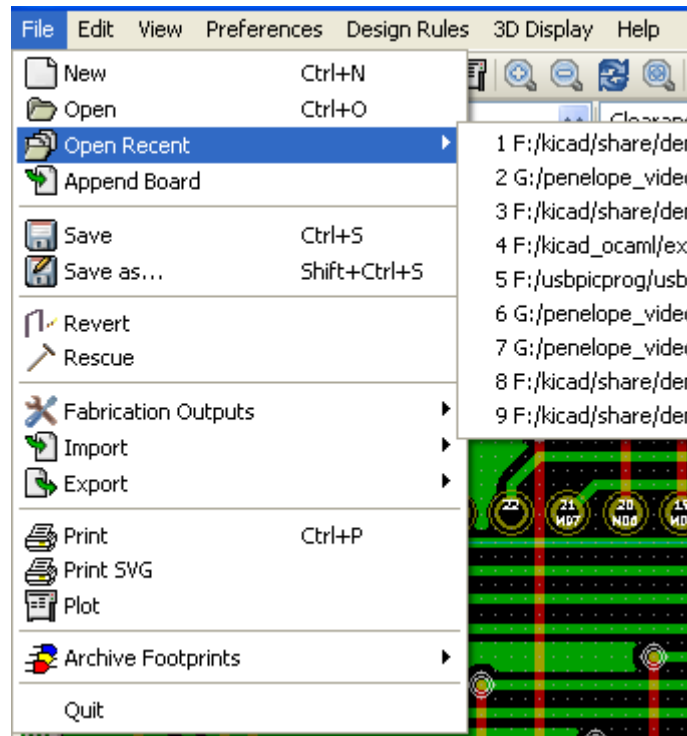
So 1,5 and 1.5 are the same in French.

### 3.9 - Menu Bar Options

The menu bar provides access to the files (loading and saving), configuration options, printing, plotting and the help files.

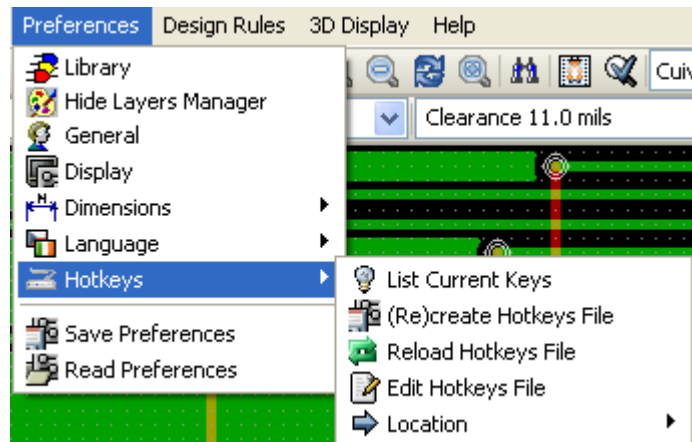


### 3.9.1 - Files



Allows the loading and saving of the printed circuits files, as well as printing and plotting the circuit board. Also enables the export (with the format GenCAD 1.4) of the circuit for use with automatic testers.

### 3.9.2 - Preferences

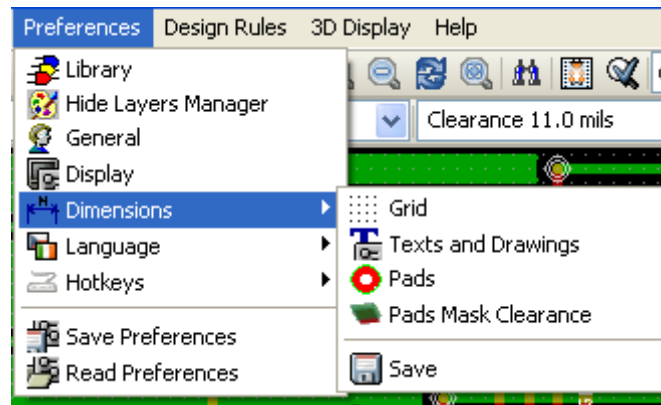


Allows:

- Selection of the module libraries.
- Hide/Show the Layers manager( colors selection for displaying layers and other elements. Also enables the display of elements to be turned on and off.)
- Management of general options (units, etc.).
- The management of other display options.
- Creation, edition (and reread) of the hot keys file.

### 3.9.3 - Preferences/Dimensions:

An important sub menu.



Allows adjustment of:

- User grid size.
- Size of texts and the line width for drawings.
- Dimensions and characteristic of pads.
- Setting the global values for solder mask and solder paste layers

### 3.9.4 - Design Rules

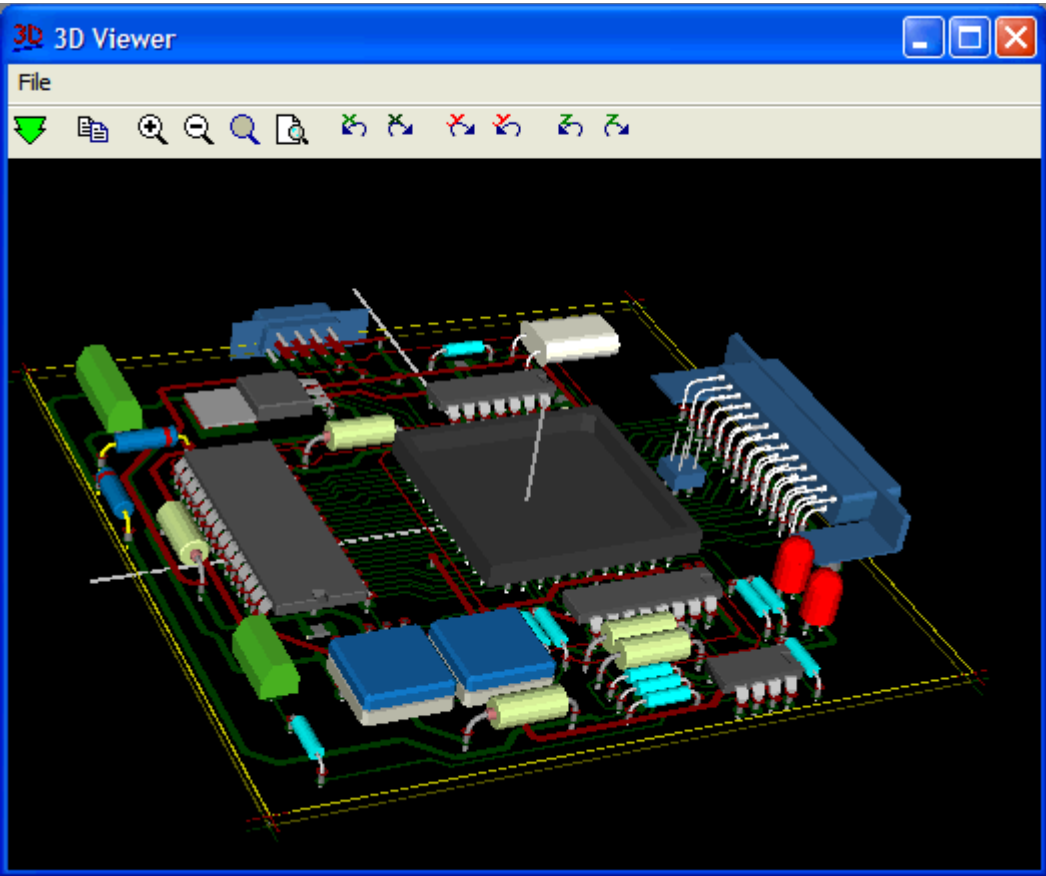


Provides access to 2 dialogs:

- Setting the Design rules (tracks and vias sizes, clearances).
- Setting layers (Number, enabled and layers names)

### 3.9.5 - 3D Display

Brings up 3D viewer to display the circuit board in 3 dimensions. e.g.

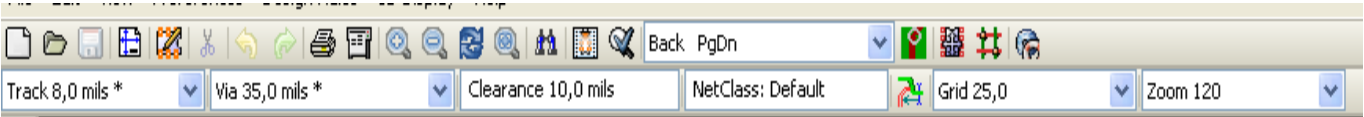











3.9.6 - Help

Provides access to the help file and version information (Pcbnew About).









3.10 - Commands using icons on the upper toolbar

This toolbar gives access to the principal functions of PCBNEW.


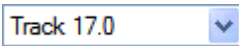
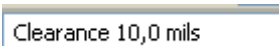
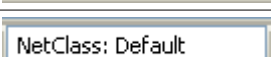


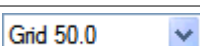
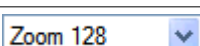


	Creation of a new printed circuit.
	Opening of an old printed circuit.
	Save printed circuit.
	Selection of the page size and modification of the file properties.
	Opens module editor ( <b>Modedit</b> ) to display/edit library or pcb modules.
	Delete the elements selected during a move block operation.
	Undo Redo last commands (10 levels)
	Display print menu.
	Display plot menu.



















	Zoom in and Zoom out (relative to the centre of screen).
	Redraw the screen and Auto Zoom.
	Find module or text.
	Netlist options (selection, reading, testing and compiling).
	DRC (Design Rule Check): Automatic check of the tracks.
	<i>Footprint mode</i> : when active this enables module options in the pop-up window.
	<i>Routing mode</i> : when active this enables routing options in the pop-up window
	Direct access to the web router FreeRoute

Auxiliary toolbar:


	Selection of the work layer.
	Selection a thickness of track already in use.
	Current track and vias clearance (That is set in the Design rules dialog)
	Display the current selected netclass (That is set in the Design rules dialog) A netclass defines the current tracks and vias parameters.
	Automatic track width: if enabled when creating a new track, when starting on an existing track, the width of the new track is set to the width of the existing track.
	Selection of a dimension of via already in use.
	Selection of the grid size.
	Selection of the zoom.


















### 3.11 - Commands using icons on the right hand toolbar

	<p>This toolbar gives access to the <b>tools</b> for:</p> <ul style="list-style-type: none"> <li>- Placement of modules, tracks, zones of copper, texts...</li> <li>- Net Highlighting.</li> <li>- Creating notes, graphic elements....</li> <li>- Deleting elements.</li> </ul>
---	--

	Cancel the tool in progress.
	Highlight net selected by clicking on a track or pad.
	Display local ratsnest (Pad or Module).
	Add a module from a library.
	Placement of tracks and vias.
	Placement of zones (copper planes).
	Draw Lines on technical layers (i.e. not a copper layer).
	Draw Circles on technical layers (i.e. not a copper layer).
	Draw Arcs on technical layers (i.e. not a copper layer).
	Placement of text.
	Draw Dimensions on technical layers (i.e. not the copper layer).
	Draw Alignment Marks (appearing on all layers).
	Delete element pointed to by the cursor (see note below) Note: when Deleting if several superimposed elements are pointed to priority is given to the smallest (in the decreasing set of priorities tracks, text, module). the function "Undelete" of the upper toolbar allows the cancellation of the last item deleted.
	Offset adjust for drilling and place files.
	Grid origin. (grid offset). Useful mainly for edition and placement of footprints. Can also be set in Dimensions/Grid menu.

### 3.12 - Commands using icons on the left hand toolbar

	<p>This left hand toolbar provides display and control options</p>
--	--

	Turns DRC (Design Rule Checking) on/off. Caution: when DRC is off incorrect connections can be made.
	Grid display on/off (Note: a small grid may not be displayable).
	Polar display of the relative co-ordinates on the status bar on/off.
	Display/entry of co-ordinates in inches or millimeters.
	Change cursor display.
	Display general rats nest (incomplete connections between modules).
	Display module rats nest dynamically as it is moved.
	Enable/Disable automatic deletion of a track when it is redrawn.
  	<p>Display mode for copper zones.</p> <p> = Shows all (outlines + filled areas)</p> <p> = Shows outlines only (no filled areas)</p> <p> = Shows all outlines ( zone outlines + filled areas outlines ) Filling itself is not shown</p>
	Display of pads in sketch mode on/off.
	Display of tracks and vias in sketch mode on/off.
	High contrast display mode on/off. In this mode the active layer is displayed normally, all the other layers are displayed in gray. Useful for working on multi-layer circuits.

	Hide/Show the Layers manager
	Access to microwaves tools. Under development





3.13 - POP Up Windows and fast editing of elements

A click of the right mouse button reveals a Pop-Up Window the contents of which depend on the element pointed at by the cursor.  
This gives immediate access to:

- Changing the display (centre display on cursor, zoom in or out or selecting the zoom).
  - Setting the grid size.
  - Additionally a right click on an element enables editing of the most usually modified element parameters.
- The views below show the pop-up windows:

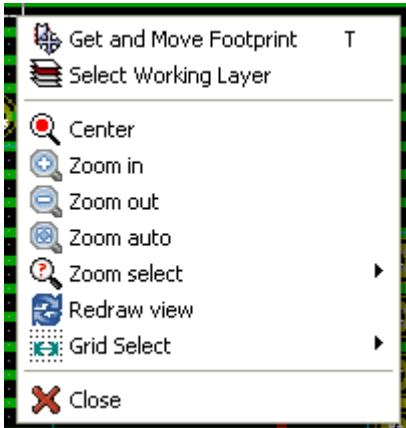
3.13.1 - Modes:

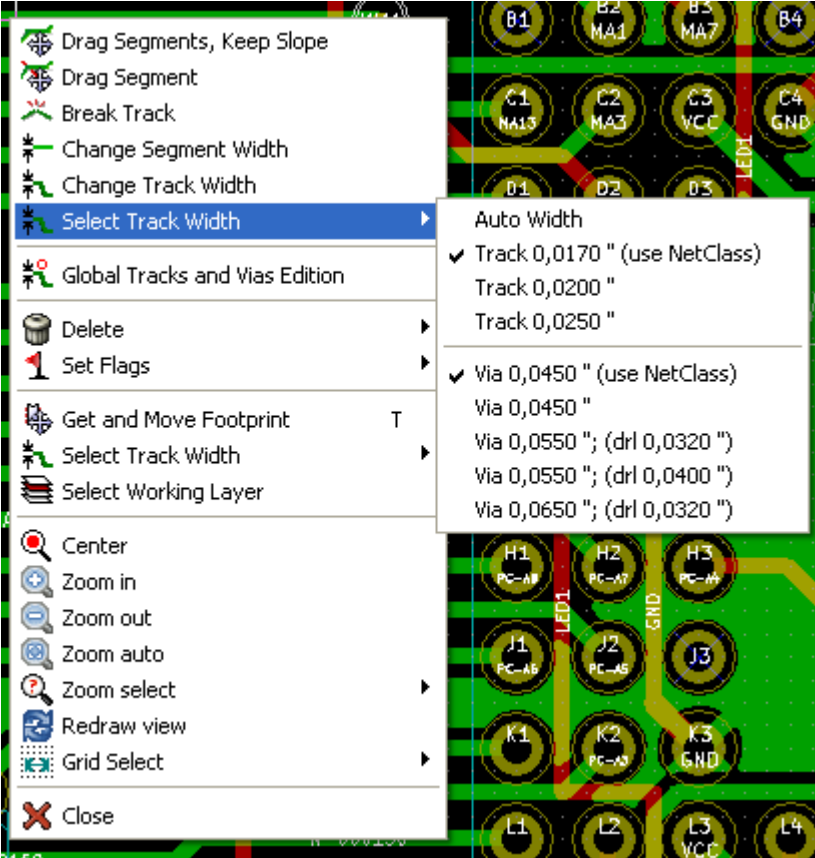

There are 3 modes when using pop up menus.

 et  disabled	Normal mode
 enabled	Footprint mode
 enabled	Tracks mode

In pop up menus, these modes add or remove some specific commands.

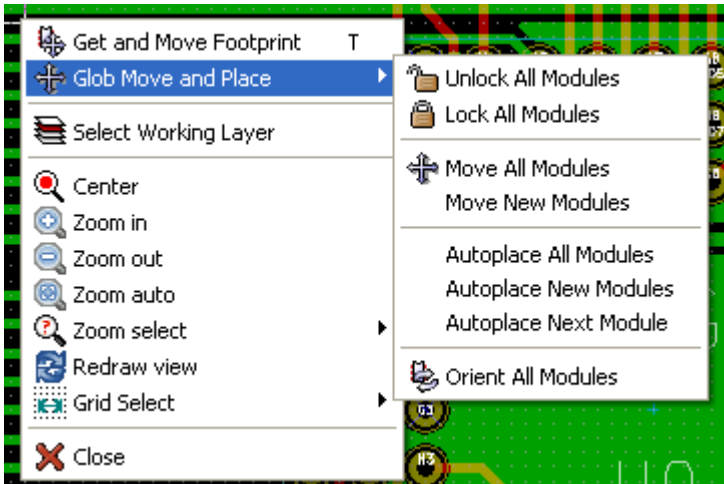
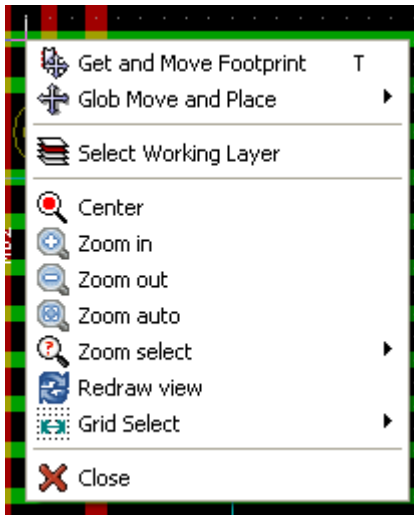
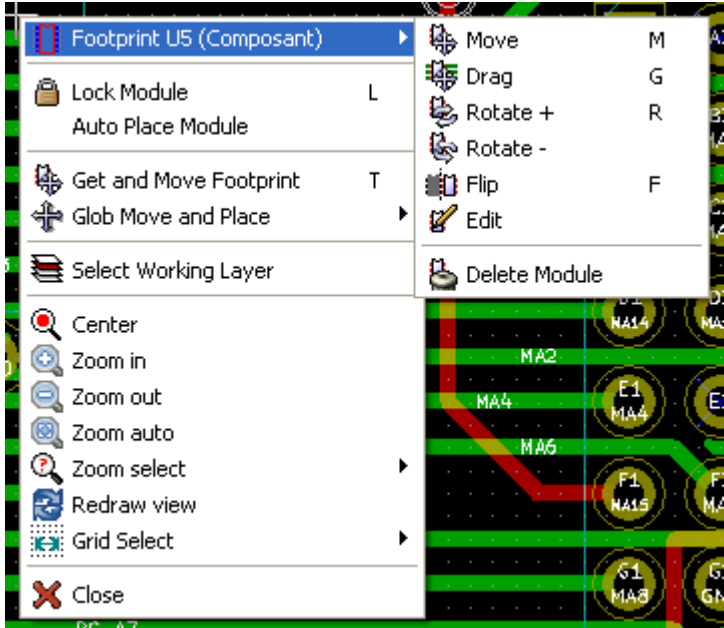
3.13.2 - Normal mode:

	Pop Up with no selected item.
---	-------------------------------

	<p>Pop Up on a track.</p>
	<p>Pop up on a footprint.</p>

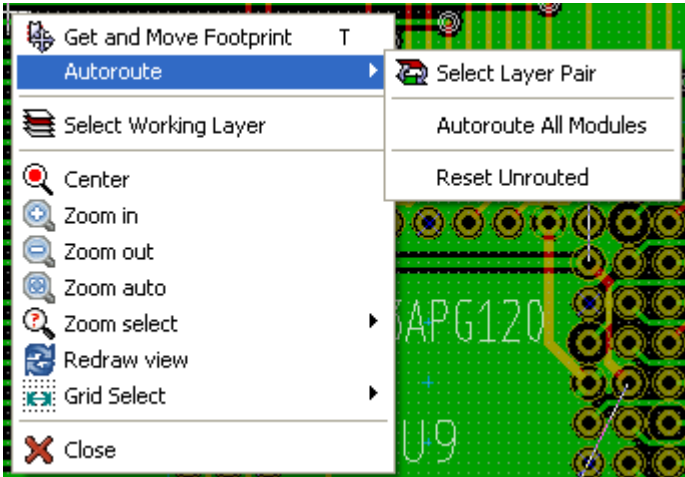
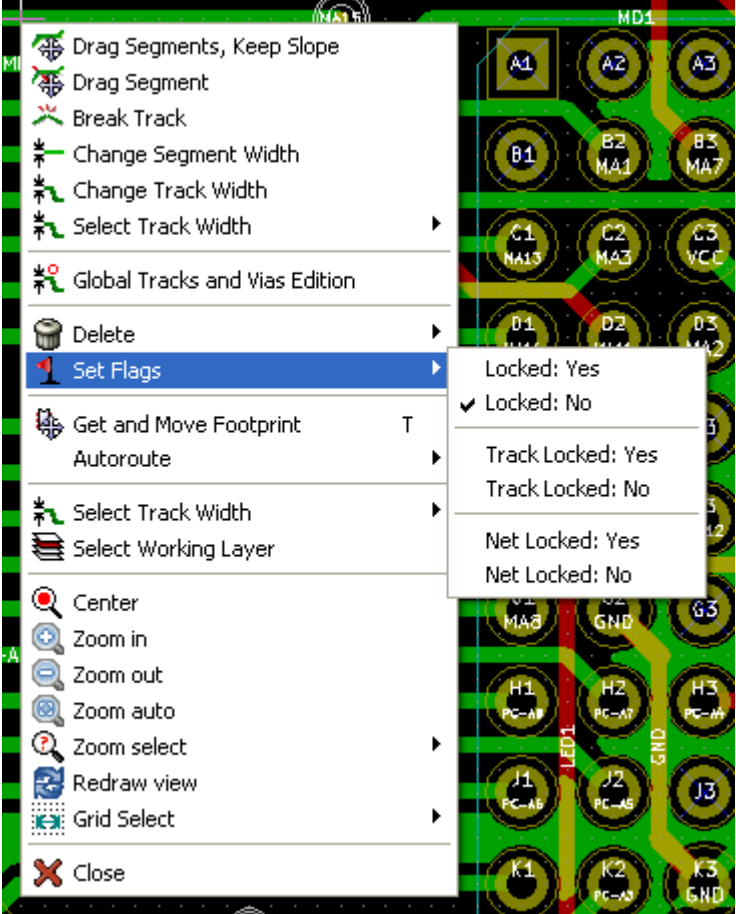
3.13.3 - Footprint mode

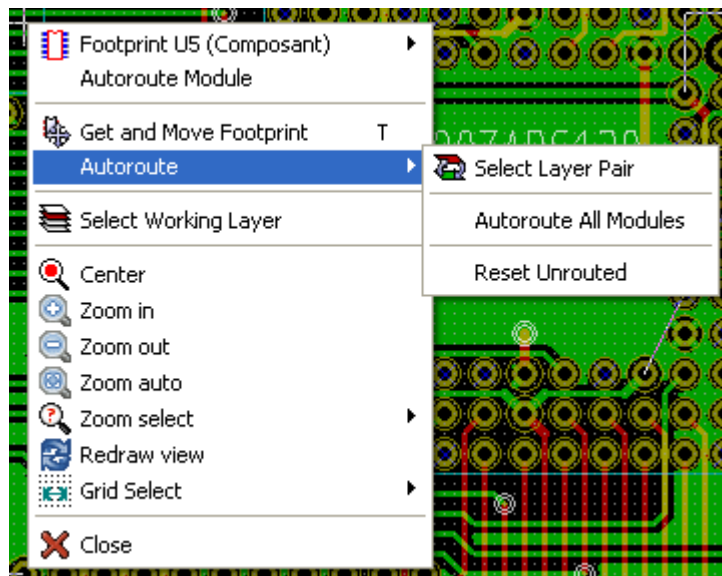
Same cases in *Footprint mode* (  on)

	<p>Pop Up with no selected item.</p>
	<p>Pop Up on a track.</p>
	<p>Pop up on a footprint.</p>

### 3.13.4 - Tracks mode:

Same cases in **Tracks mode** (  on)

	<p>Pop Up with no selected item.</p>
	<p>Pop Up on a track.</p>



Pop up on a footprint.



## 4 - Schematic Implementation

### Headings:

#### [4 - Schematic Implementation](#)

##### [4.1 - Linking the Schematic to the Printed Circuit](#)

##### [4.2 - Procedure for Creating the Printed Circuit](#)

##### [4.3 - Procedure for Updating the Printed Circuit](#)

##### [4.4 - Reading netlist - Loading footprints - Options:](#)

###### [4.4.1 - Dialog box:](#)

###### [4.4.2 - Options:](#)

###### [4.4.3 - Loading new footprints:](#)

### 4.1 - Linking the Schematic to the Printed Circuit

The schematic is linked to PCBNEW by means of the Netlist file, which is normally generated by the schematic program used.

Note: PCBNEW accepts Netlist files with the **Eeschema** or **ORCAD PCB 2** formats.

The Netlist file initially generated is usually incomplete as there has been no assignment of the modules that correspond to the various components used in the schematic. Consequently an intermediate stage is necessary, the generation of the file of association components/modules.

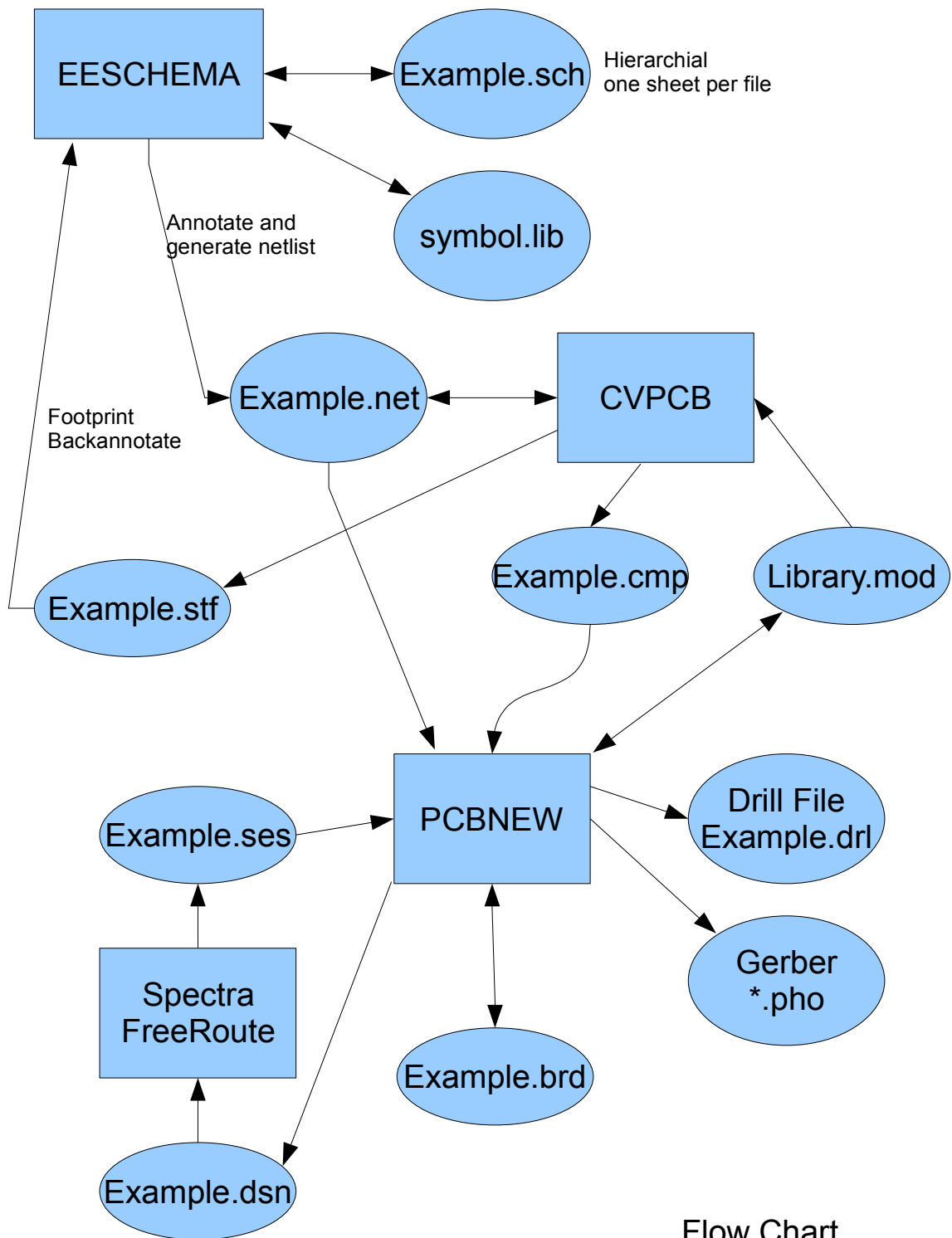
CVPCB is used to create this file which is named \*.CMP.

CVPCB also updates the Netlist file using this information.

CVPCB can also output a "stuff file" \*.STF which can be back annotated into the schematic file as the F2 field for each component, saving the task of re-assigning module footprints in each schematic edit pass. In Eeschema coping a component will also copy the footprint assignment and set the reference designator as unassigned for later auto-incremental-annotation.

PCBNEW reads the modified Netlist file \*.NET and, if it exists, the file \*.CMP.

In the event of a module being changed directly in PCBNEW the \*.CMP file is automatically updated so avoiding the requirement to run CVPCB again.



Flow Chart

## 4.2 - Procedure for Creating the Printed Circuit

After having created the required schematic:

- Generate the netlist using Eeschema.
- Assign each components in the Netlist produced by Eeschema to the corresponding module used on the printed circuit using CVPCB.
- Launch PCBNEW and read the modified Netlist (this will also read the file with the module selections).

PCBNEW will then load automatically all the modules.

The modules can now be placed manually or automatically on the board and the tracks routed.

## 4.3 - Procedure for Updating the Printed Circuit


If the schematic is modified, the following steps must be repeated:

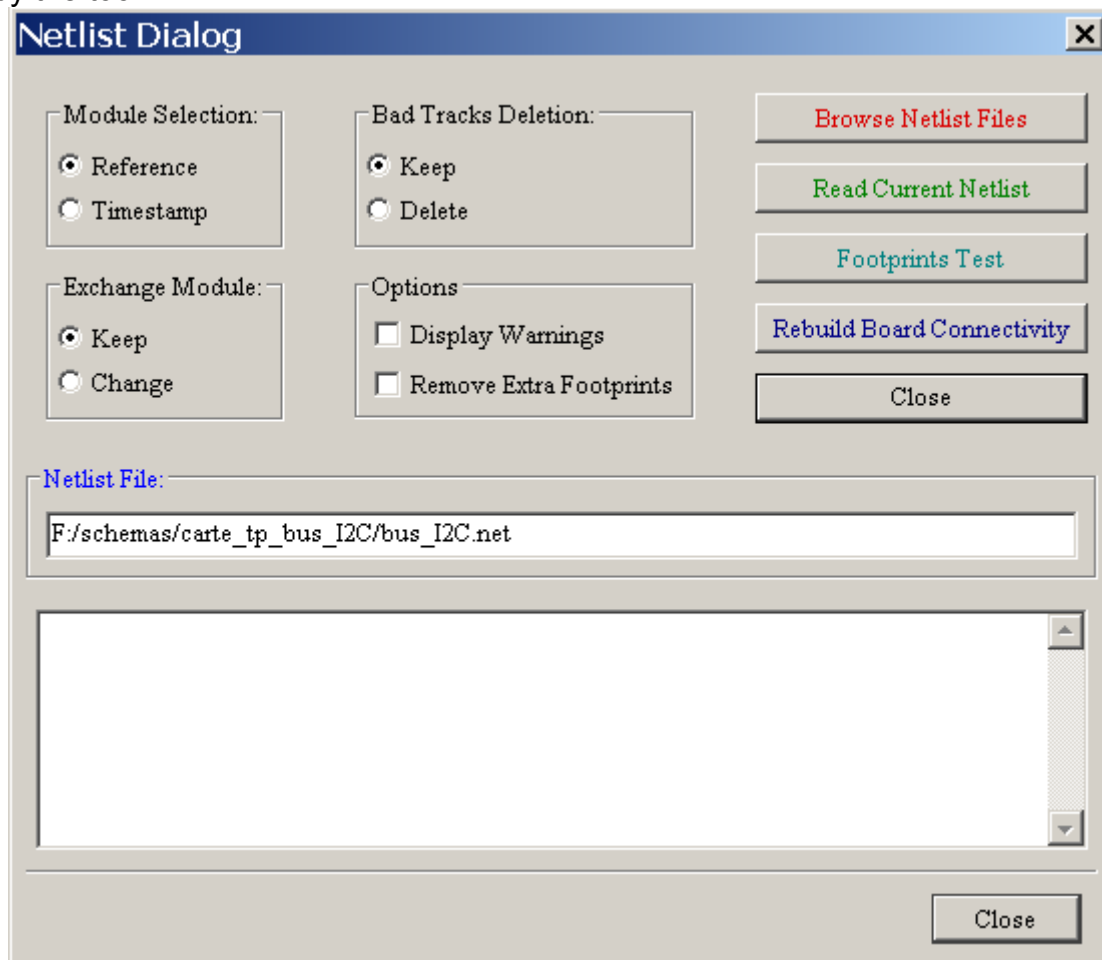
- Generate a new netlist using Eeschema.
- If the changes to the schematic involve new components, the corresponding modules must be assigned using CVPCB.
- Launch PCBNEW and re-read the modified Netlist (this will also re-read the file with the module selections).

PCBNEW will then load automatically any new modules, add the new connections and remove redundant connections.

## 4.4 - Reading netlist - Loading footprints - Options:

### 4.4.1 - Dialog box:

Access by the tool: 

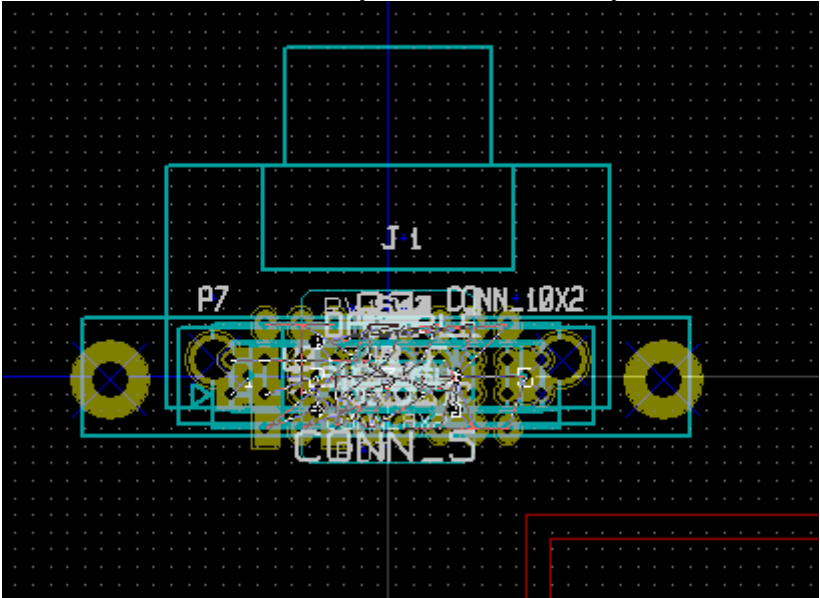


4.4.2 - Options:

Exchange Module:	If a footprint has changed in the netlist: keep old footprint or change to the new one.
Bad Tracks Deletion	Keep all existing tracks, or delete erroneous tracks
Options : (on/off)	Display all messages (or not) Remove footprints which are on board but not in netlist. Footprint with attribute "Locked" will be not removed

4.4.3 - Loading new footprints:

When new footprints are found in netlist, they are automatically loaded:

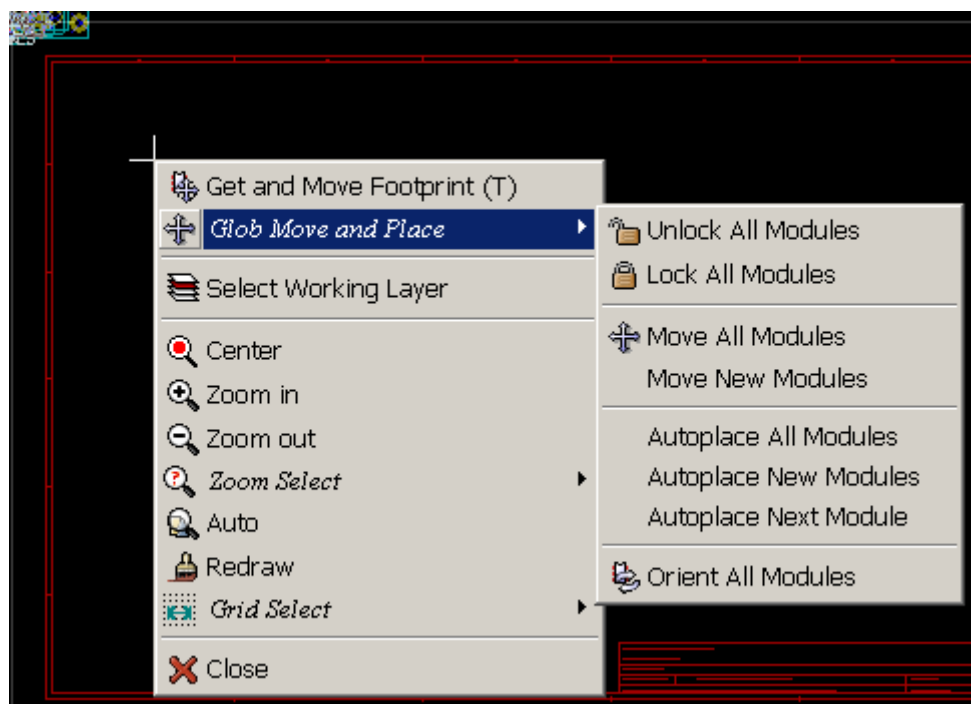


They are stacked at coordinate 0,0.  
One can move footprints one by one.  
But a better way is to automatically move (unstack) them:

- Activate footprint mode"

	activate the "footprint mode"
	footprint mode activated

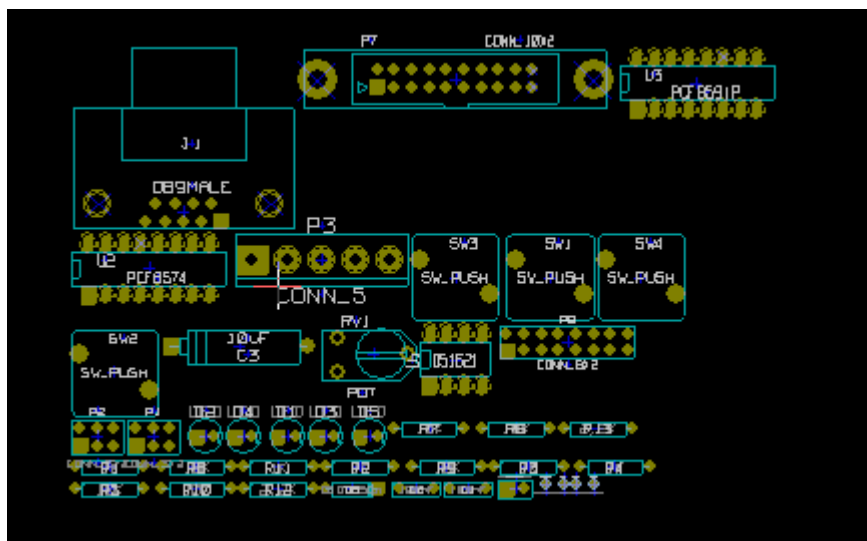
Move the mouse cursor to a suitable (free) area, and activate the right button:



**Use:**

- **Move New Modules** if there is already a board with existing footprints.
- or
- **Move All Modules**, the first time (when creating a board)

Here is result:



## 5 - Setting and displaying the working layers

### Headings:

- [5 - Setting and displaying the working layers](#)
  - [5.1 - Layers of copper](#)
    - [5.1.1 - General information:](#)
    - [5.1.2 - Selection of the number of layers:](#)
  - [5.2 - Copper layers:](#)
  - [5.3 - Auxiliary technical layers](#)
    - [5.3.1 - Paired layers:](#)
    - [5.3.2 - Layers for general use:](#)
    - [5.3.3 - Special layer:](#)
  - [5.4 - Selection of the Active Layer:](#)
    - [5.4.1 - Selection using the Layer manager:](#)
    - [5.4.2 - Selection using the upper toolbar:](#)
    - [5.4.3 - Selection Using the Pop-Up Window:](#)
  - [5.5 - Selection of the Layers for Vias:](#)
  - [5.6 - Using the High Contrast mode:](#)
    - [5.6.1 - Copper layers in high contrast mode:](#)
    - [5.6.2 - Technical layers:](#)

PCBNEW works on 28 different layers:

- 16 layers of copper (or of routing of tracks)
- 12 auxiliary technical layers.

One must set the number of copper layer, and (if needed) their name and attribute.  
One can also disable unused technical layers for this board.

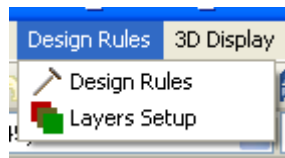
### 5.1 - Layers of copper

#### 5.1.1 - General information:

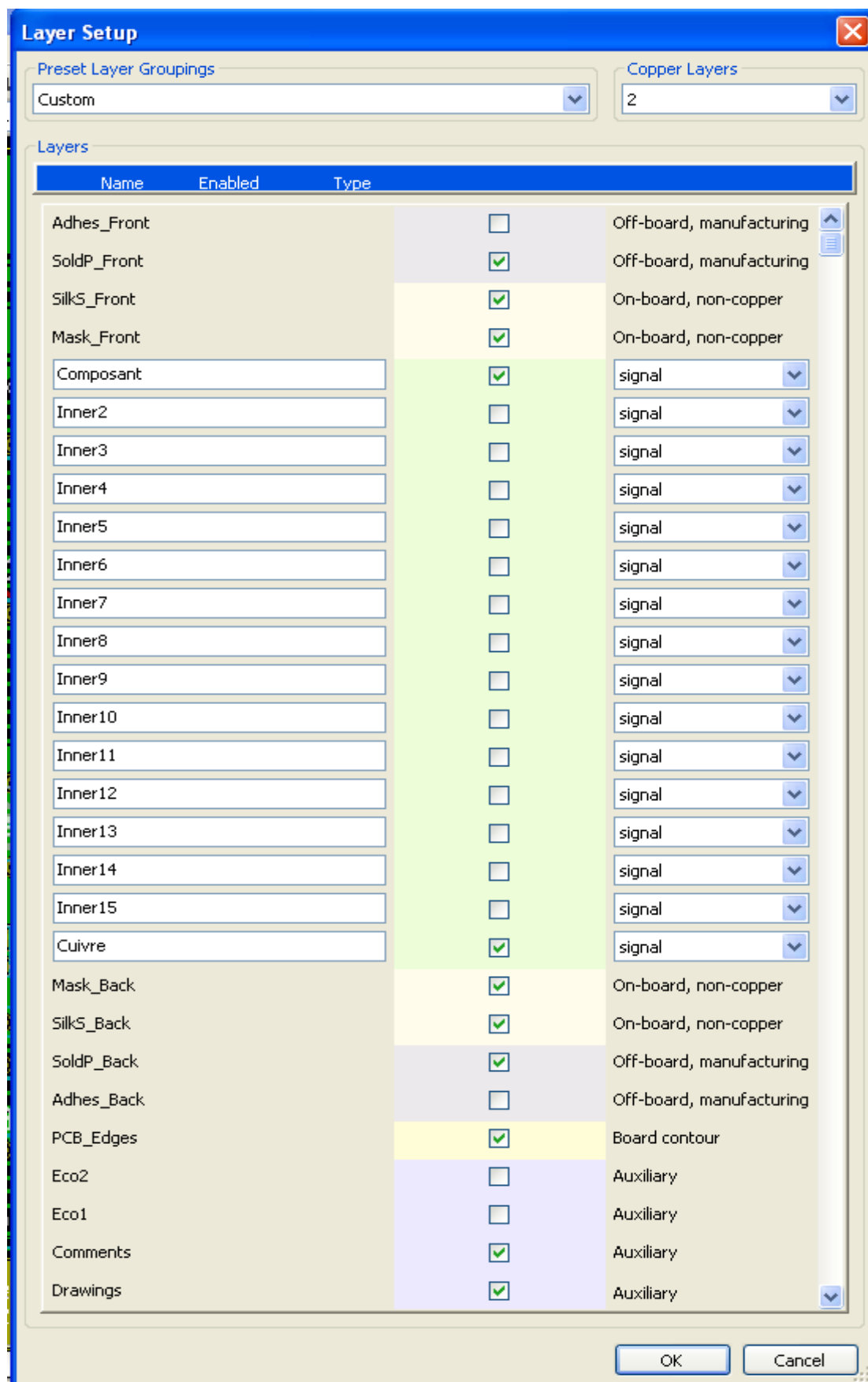
They are the usual layers of work, used by the automatic router, on which tracks can be placed. Layer 1 is the copper (solder) layer. Layer 16 is the component layer. The other layers are the internal layers (L2 to L15).

#### 5.1.2 - Selection of the number of layers:

To aid navigation between layers, it is necessary to select the number of working layers.  
To do this from the menu bar select **Preferences / Layers Setup**.

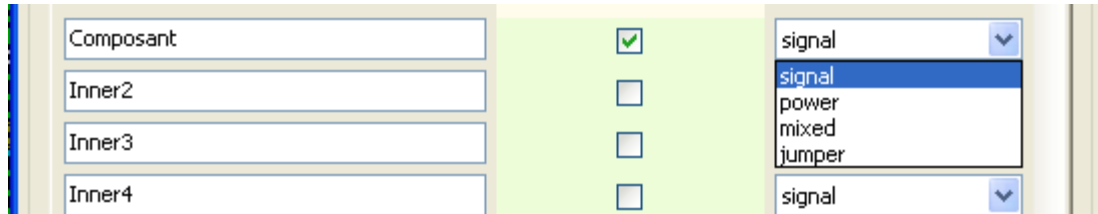


Then select the required number of layers (2 to 16).



## 5.2 - Copper layers:

Name of copper layers are editable. Copper layers have attributes useful when using the external router

**FreeRouter.**

### 5.3 - Auxiliary technical layers

Some are associated in pairs, others not. When they appear as a pair this affects the behaviour of modules. The elements making up a module (pads, drawing and text) appearing on a layer (solder or component), appear on the other complementary layer when the module is inverted (mirrored).  
The technical layers are:

#### 5.3.1 - Paired layers:

- **The Adhesives layers (Copper and Component):**  
These are used in the application of adhesive to stick SMD components to the circuit board, generally before wave soldering.
- **The Solder Paste layers paste SMD (Copper and Component):**  
Used to produce a masks to allow solder paste to be placed on the pads of surface mount components, generally before reflow soldering. In theory only surface mount pads occupy these layers.
- **The Silk Screen layers (Copper and Component):**  
They are the layers where the drawings of the components appear.
- **The Solder Mask layers (Copper and Component):**  
These define the solder masks. Normally all the pads appear on one or the other of these layers (or both for through pads) to prevent the varnish covering the pads.

#### 5.3.2 - Layers for general use:

- **Comments**
- **E.C.O. 1**
- **E.C.O. 2**
- **Drawings**

These layers are for any use. They can be used for text such as instructions for assembly or wiring, or construction drawings, to be used to create a file for assembly or machining.

#### 5.3.3 - Special layer:

Layer **Edges PCB**:

this layer is reserved for the drawing of circuit board outline. Any element (graphic, texts...) placed on this layer appears on all the other layers.

**Use this layer only to draw board outlines.**

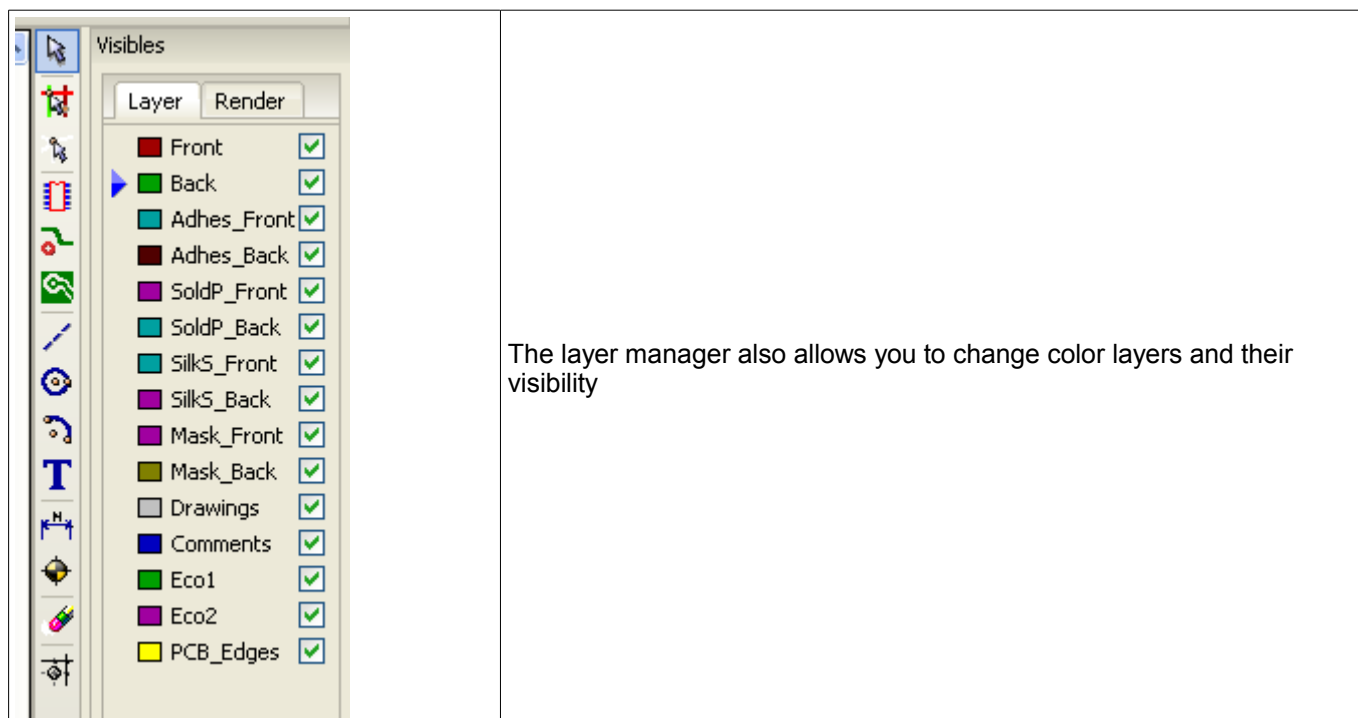
### 5.4 - Selection of the Active Layer:

The selection of the active working layer can be done in several ways:

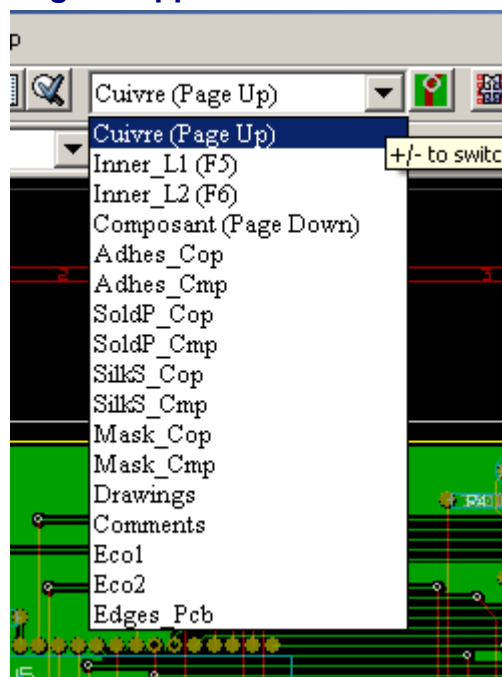
- Using the right toolbar (Layer manager).
- Using the upper toolbar.
- With the Pop-Up window (activated with the right mouse button).
- Using the + and – keys (works on copper layers only).
- By hot keys.

#### 5.4.1 - Selection using the Layer manager:



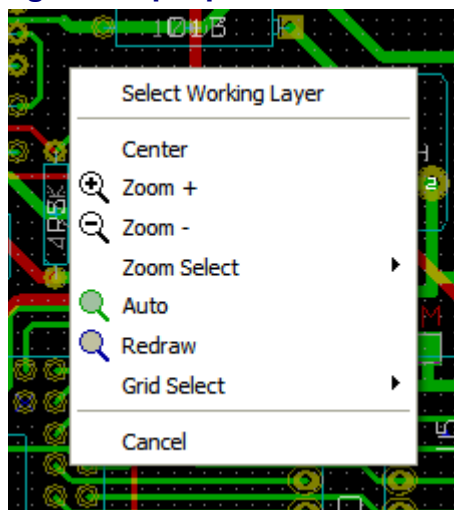


#### 5.4.2 - Selection using the upper toolbar:

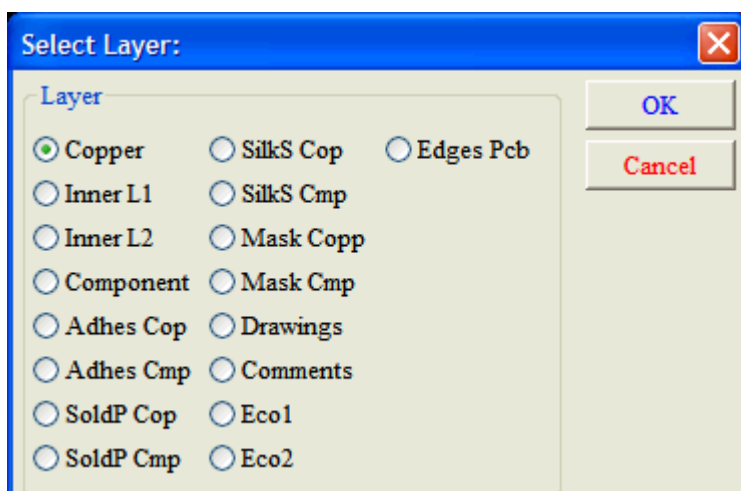


This directly selects the working layer.  
Hot keys to select the working layer are displayed.

### 5.4.3 - Selection Using the Pop-Up Window:

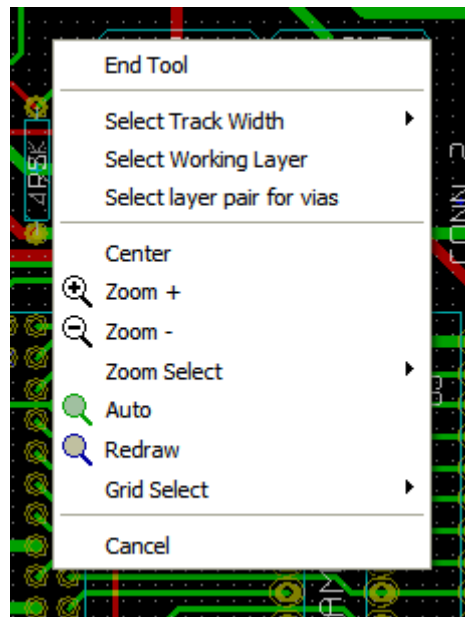


The Pop-up window opens a menu window - which provides choice of the working layer:

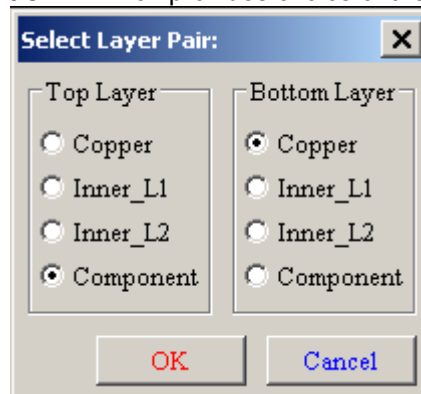


### 5.5 - Selection of the Layers for Vias:

If the **Add Tracks and Vias** icon is selected on the right hand toolbar, the Pop-Up window provides the option to change the layer pair used for vias:




This selection opens a menu window - which provides choice of the layers used for vias.



When a via is placed the working (active) layer is automatically switched to the alternate layer of the layer pair used for the vias.

One can also switch to an other active layer by hot keys, and if a track is in progress, a via will be inserted.

## 5.6 - Using the High Contrast mode:

This mode is entered when the tool  (left toolbar ) is activated.

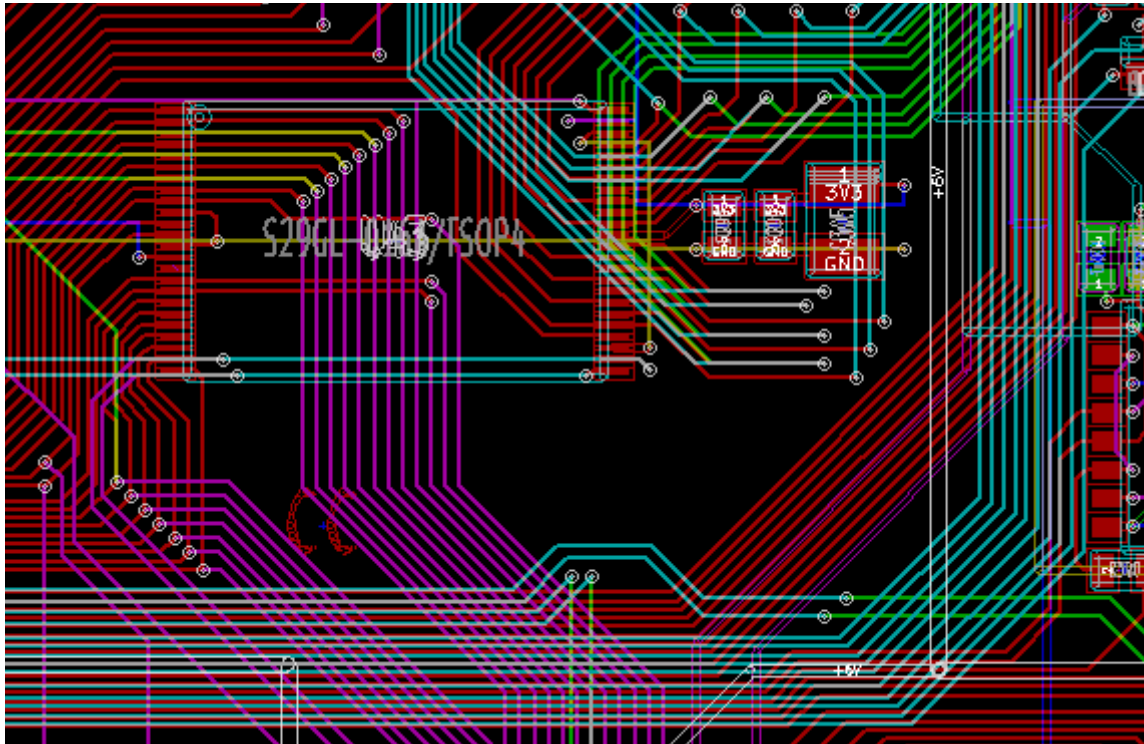
When using this mode, the active layer is displayed like in the normal mode, but all other layers are displayed in gray color.

There are two useful cases:

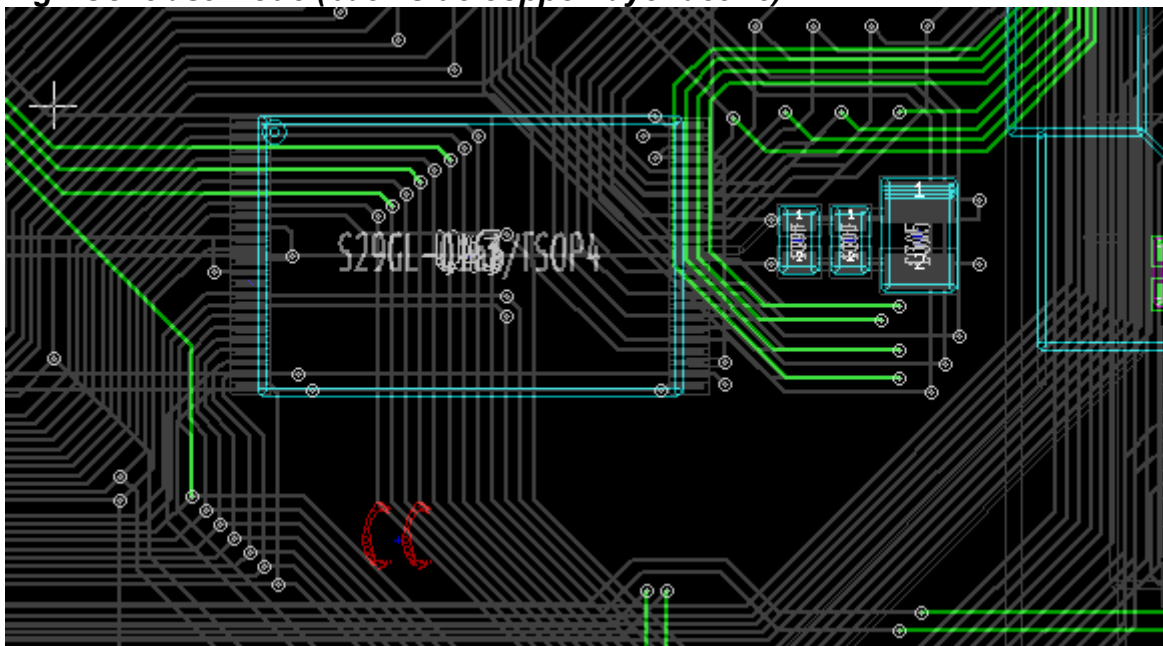
### 5.6.1 - Copper layers in high contrast mode:

When a board uses more than four layers, this option allows the design to be seen more easily with the active copper layer:

- ***Normal mode normal (back side copper layer active)***



- **High Contrast mode (back side copper layer active):**

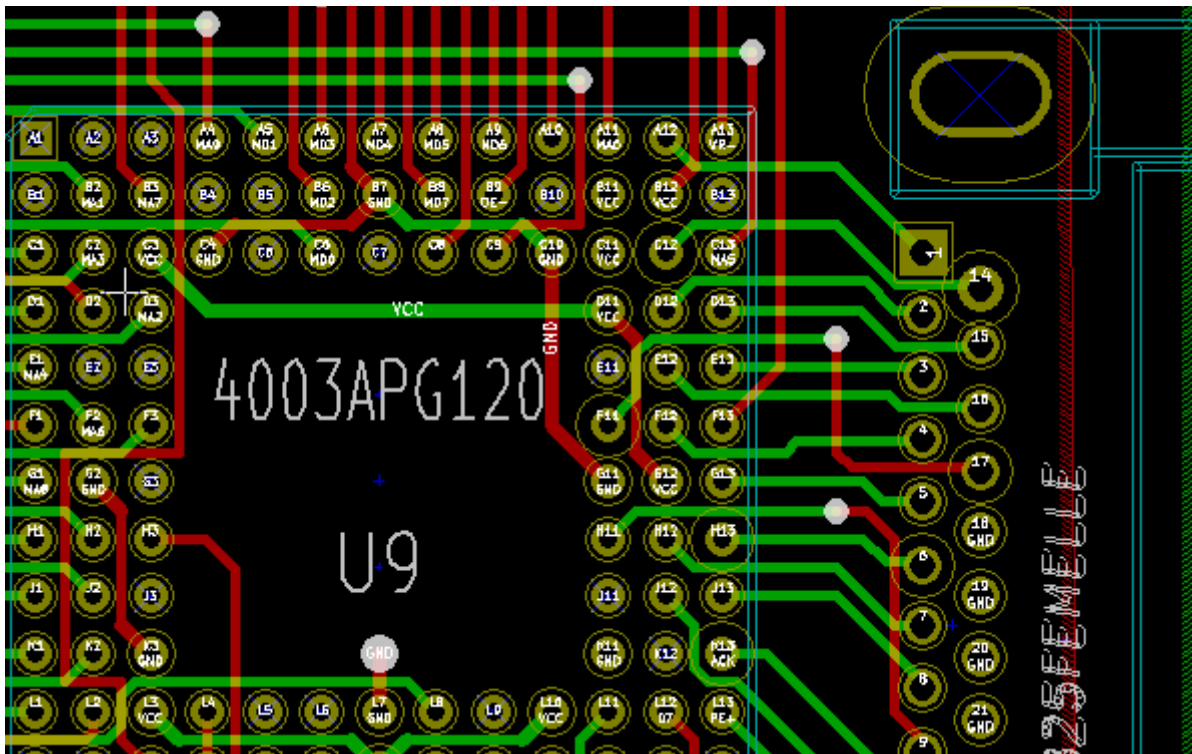


### 5.6.2 - Technical layers:

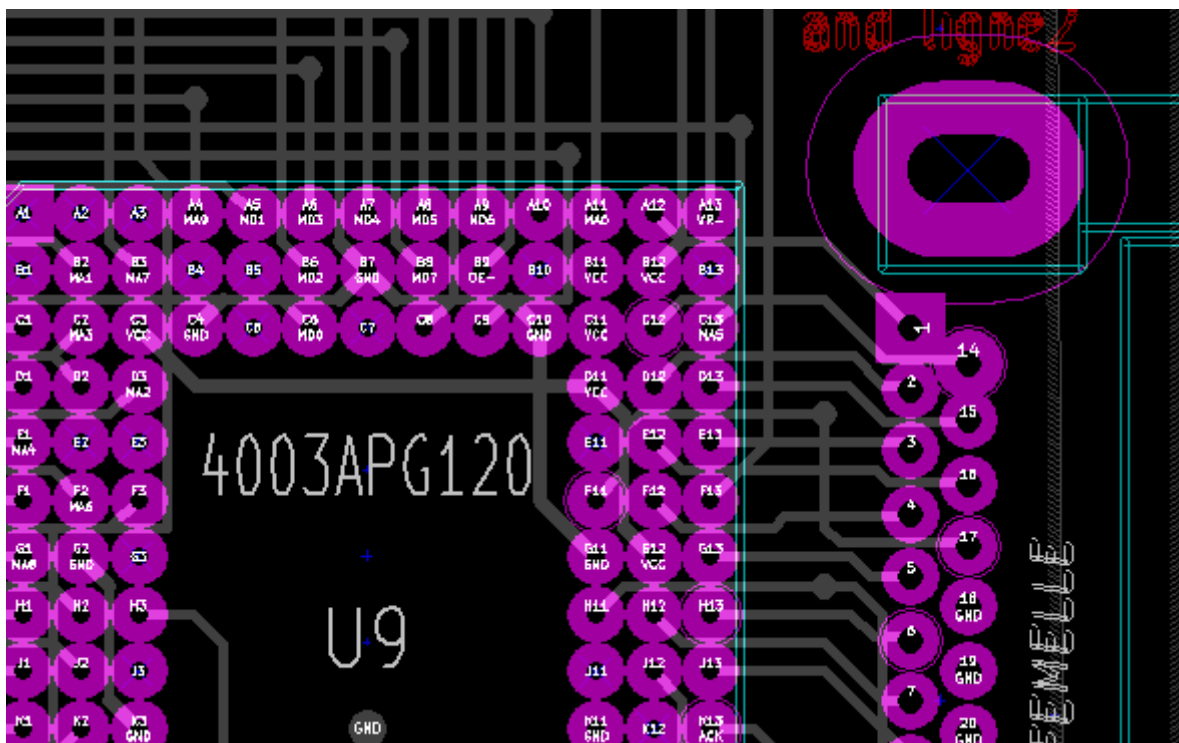
The other case is when it is necessary to examine solder paste layers and solder mask layers, that are usually not displayed.

Masks on pads are displayed if this mode is active:

- **Normal mode (front side solder mask layer active):**



- **High Contrast mode (front side solder mask layer active):**



This layer is now displayed, and pads sizes on this layer can be checked.

## 6 - Creation/correction of a board

### Headings:

#### [6 - Creation/correction of a board](#)

##### [6.1 - Creating a board](#)

##### [6.1.1 - Drawing the board outline](#)

##### [6.1.2 - Reading the netlist generated from the schematic](#)

##### [6.2 - Correcting a board](#)

##### [6.2.1 - Steps to follow:](#)

##### [6.2.2 - Deleting incorrect tracks:](#)

##### [6.2.3 - Deleted components:](#)

##### [6.2.4 - Modified modules:](#)

##### [6.2.5 - Advanced options - selection using time stamps:](#)

##### [6.3 - Direct exchange for footprints already placed on board:](#)

### 6.1 - Creating a board

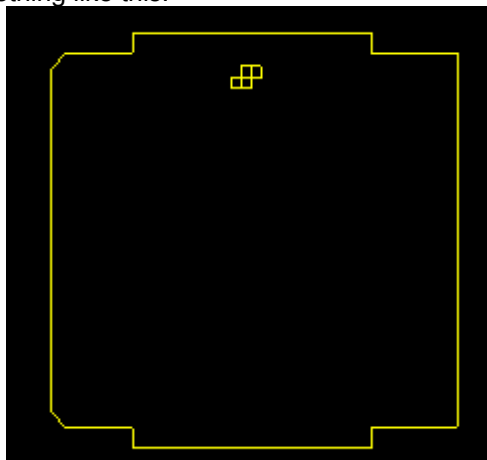
#### 6.1.1 - Drawing the board outline

It is usually a good idea to define the outline of the board first. The outline is drawn as a sequence of line segments. Select 'Edges pcb' as the active layer and use the 'Add graphic line or polygon' tool to trace the edge, clicking at the position of each vertex and double-clicking to finish the outline. Boards usually have very precise dimensions, so it may be necessary to use the displayed cursor coordinates while tracing the outline. Remember that the relative coordinates can be zeroed at any time using the space bar, and that the display units can also be toggled using 'Alt-U'. Relative coordinates enable very precise dimensions to be drawn. It is possible to draw a circular (or arc) outline:

1. Select the 'Add graphic circle' or 'Add graphic arc' tool
2. Click to fix the circle centre
3. Adjust the radius by moving the mouse
4. Finish by clicking again.

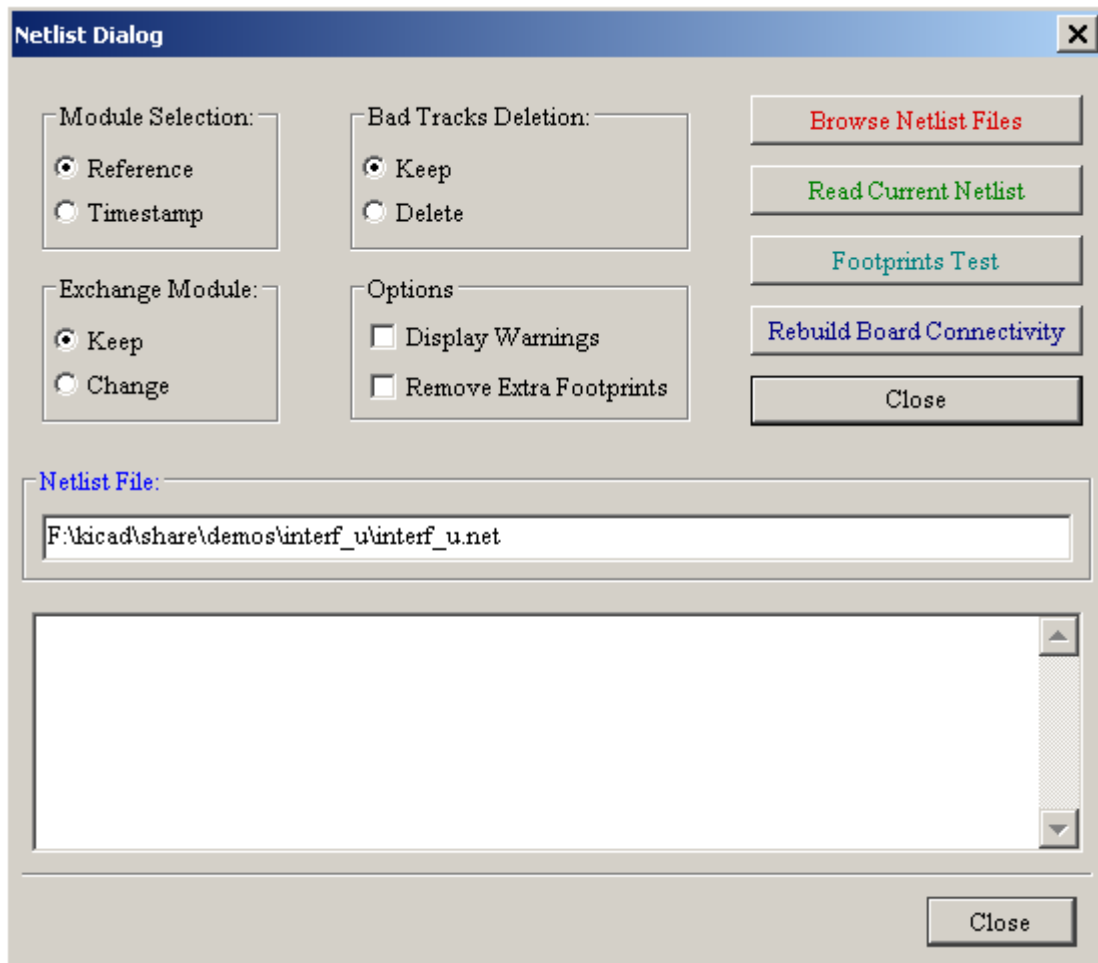
Note that the width of the outline can be adjusted, in the **Parameters** menu (recommended width = 150 in 1/10 mils) or via the **Options**, but this will not be visible unless the graphics are displayed in other than outline mode.

The resulting outline might look something like this:

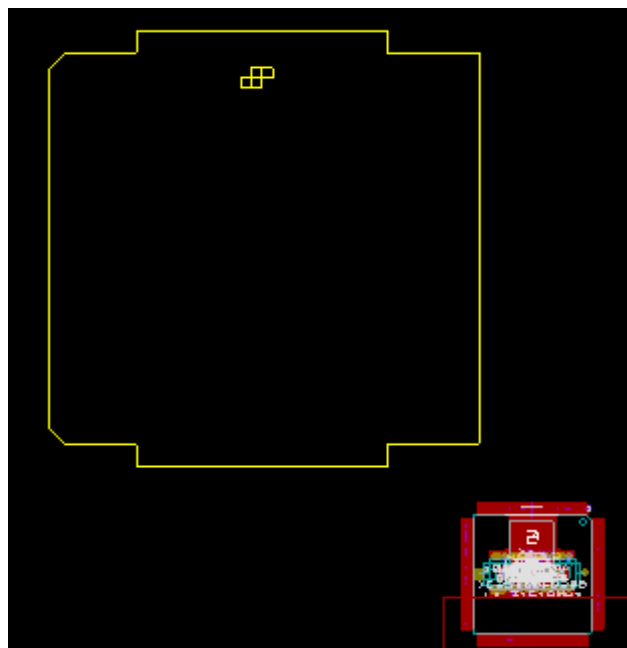


#### 6.1.2 - Reading the netlist generated from the schematic

Activate the  icon to display the netlist dialog window:

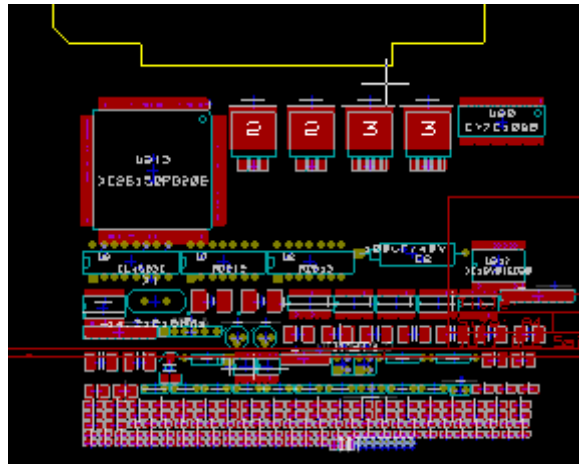


If the name (path) of the netlist in the window title is incorrect, use the 'Select' button to browse to the desired netlist. Then 'Read' the netlist. Any modules not already loaded will appear, superimposed one upon another (we shall see below how to move them automatically).



If none of the modules have been placed, all of the modules will appear on the board in the

same place, making them difficult to recognize. It is possible to arrange them automatically (using the command 'Global Place/Move module' accessed via the right mouse button). Here is the result of such automatic arrangement:



**Important note:**

If a board is modified by replacing an existing module with a new one (for example changing a 1/8W resistance to 1/2W) in CVPCB, it will be necessary to delete the existing component before PCBNEW will load the replacement module. However, if a module is to be replaced by an existing module, this is easier to do using the module dialog accessed by clicking the right mouse button over the module in question.

## 6.2 - Correcting a board

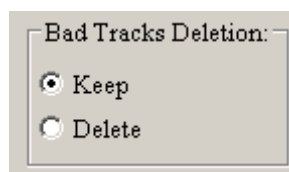
It is very often necessary to correct a board following the corresponding change in the schematic.

### 6.2.1 - Steps to follow:

1. Create a new netlist from the modified schematic.
2. If new components have been added, link these to their corresponding modules in *cvpcb*.
3. Read the new netlist in *Pcbnew*.

### 6.2.2 - Deleting incorrect tracks:

*Pcbnew* is able to delete automatically tracks that have become incorrect as a result of modifications. To do this, check the 'Delete' option in the 'Bad tracks deletion' box of the netlist dialog:



However, it is often quicker to modify such tracks by hand (the DRC function allows their identification).

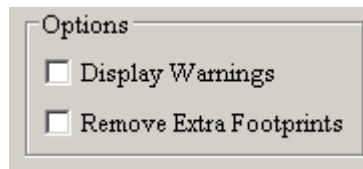
### 6.2.3 - Deleted components:

Pcbnew can delete modules corresponding to components that have been removed from the schematic.

This is optional.

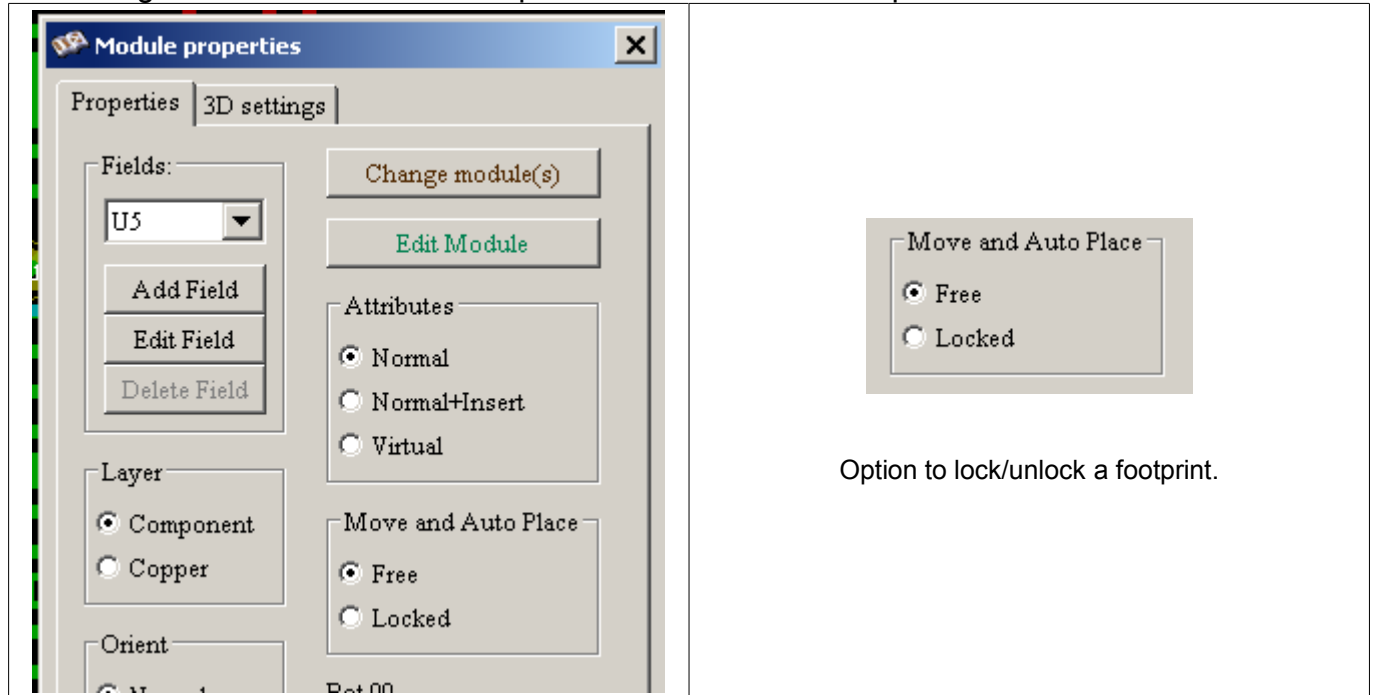
This is necessary because there are often modules (holes for fixation screws, for instance) that are added to the PCB that never appear in the schematic.





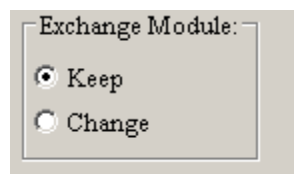
If option **Remove Extra Footprints** is checked, a footprint corresponding to a component not found in netlist will be deleted, unless they have the option "**Locked**" active.

This is a good idea to activate this option for "mechanical" footprints.



#### 6.2.4 - Modified modules:

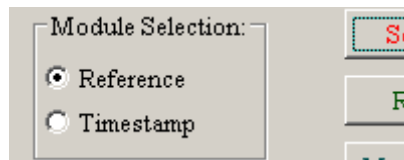
If a module is modified in the netlist (using *Cvpcb*), but the module has already been placed, it will not be modified by Pcbnew, unless the corresponding option of the 'Exchange module' box of the netlist dialog is checked:



Changing a module (replacing a resistance with one of a different size, for instance) can be effected directly by editing the module.

#### 6.2.5 - Advanced options - selection using time stamps:

Sometimes the notation of the schematic is changed, without any material changes in the circuit (this would concern the references - like R5, U4...). The PCB is therefore unchanged (except possibly for the silkscreen markings). Nevertheless, internally, components and modules are represented by their reference. In this situation, the 'Timestamp' option of the netlist dialog may be selected before rereading the netlist:



With this option, Pcbnew no longer identifies modules by their reference, but by their time stamp instead. The time stamp is automatically generated by Eeschema (it is the time and date when the component was placed in the schematic).

Great care should be exercised when using this option (save the file first!)

This is because the technique is complicated in the case of components containing multiple parts (e.g. a 7400 has 4 parts and one case). In this situation, the time stamp is not uniquely defined (for the 7400 there would be up to four – one for each part). Nevertheless, the time stamp option usually resolves re annotation problems.

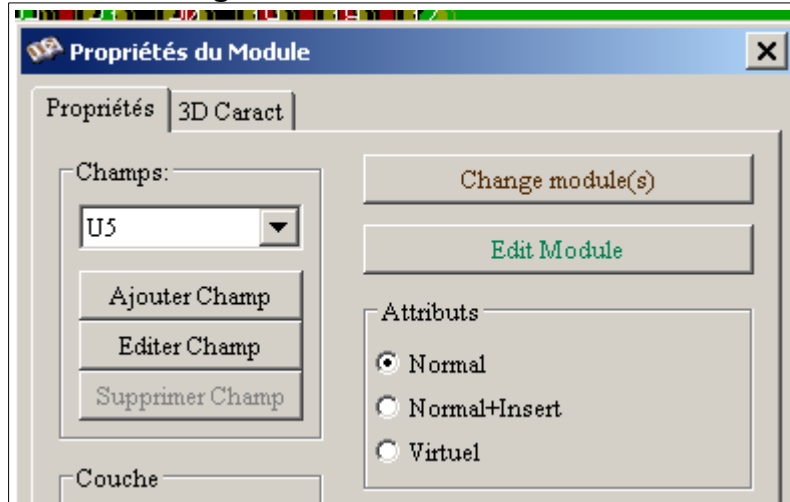
### 6.3 - Direct exchange for footprints already placed on board:

Changing a footprint ( or some identical footprints) to an other footprint is very usefull.

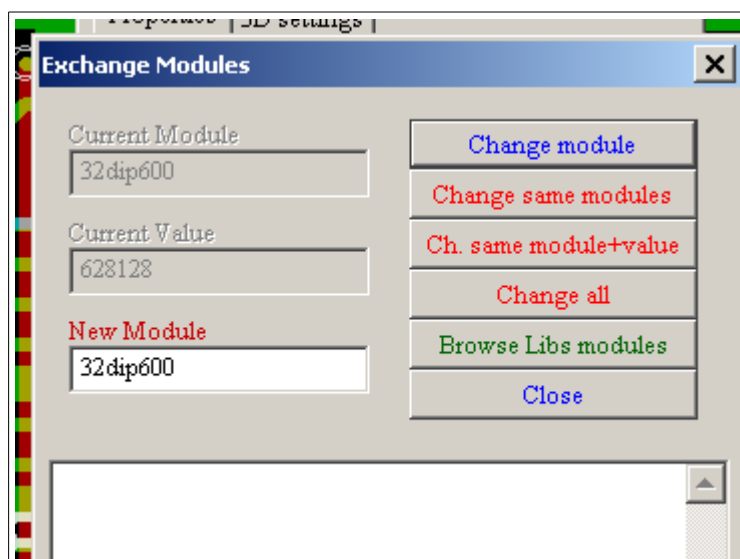
This is very easy:

Click on a footprint to open the Edit dialog box.

Activate **Change Modules**.



access to **Change Modules**



Options for footprints exchange:

One must choose a new footprint name and use:

- **Change Module** for the current footprint

## Pcbnew

- **Change same modules** for all footprints like the current footprint.
- **Change same module+value** for all footprints like the current footprint, restricted to components which have the same value.

Note:

- **Change all** reload all footprints on board.

## 7 - Placement of the modules

### Headings:

#### [7 - Placement of the modules](#)

##### [7.1 - Assisting the Placement](#)

##### [7.2 - Manual placement](#)

##### [7.3 - General Reorientation of the modules](#)

##### [7.4 - Automatic Module Distribution](#)

##### [7.5 - Automatic placement of the modules](#)


##### [7.5.1 - Characteristics of the automatic placer](#)

##### [7.5.2 - Preparation](#)

##### [7.5.3 - Interactive Autoplacement](#)

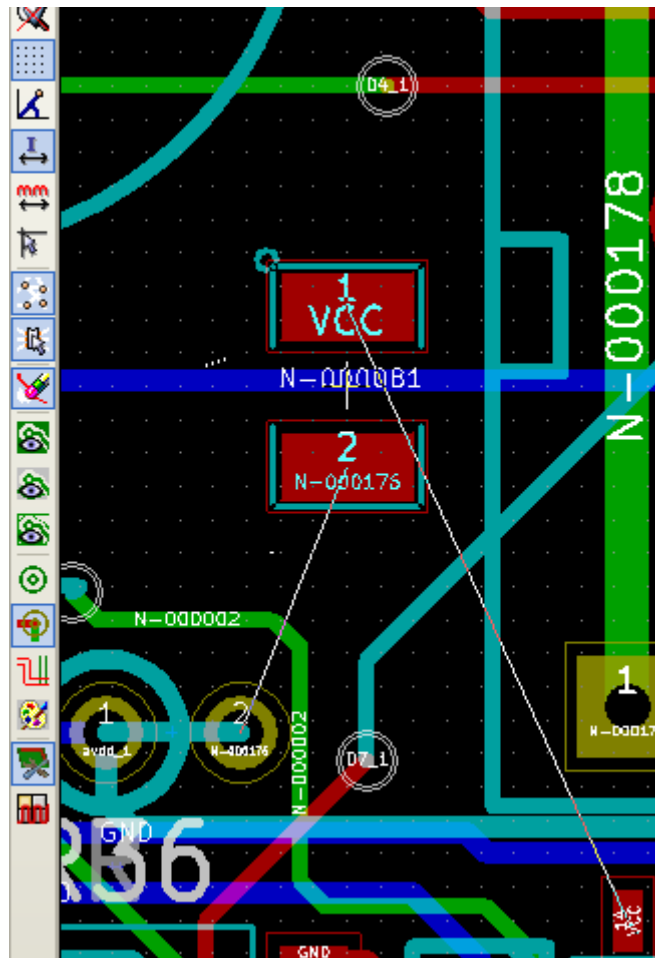
##### [7.5.4 - Note](#)

### 7.1 - Assisting the Placement

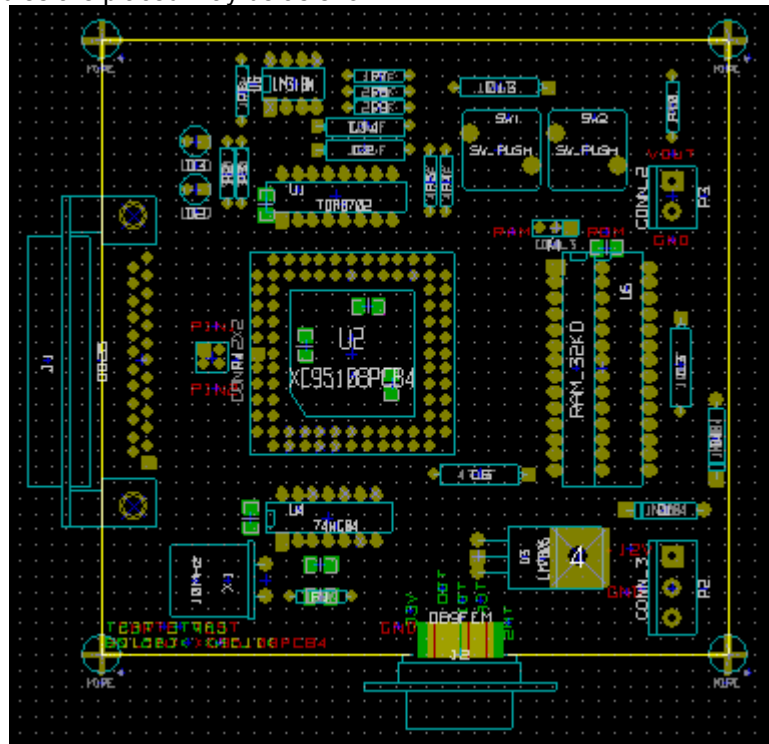
Whilst moving modules the module ratsnest (the net connections) can be displayed to assist the placement. To enable this the icon  of the left toolbar must be activated.

### 7.2 - Manual placement

Select the module with the right mouse button then chose the **Move** command from the menu. Move the module to the required position and place it with the left mouse button. If required the selected module can also be rotated, inverted or edited. Select Cancel from the menu (or press the Esc key) to abort.



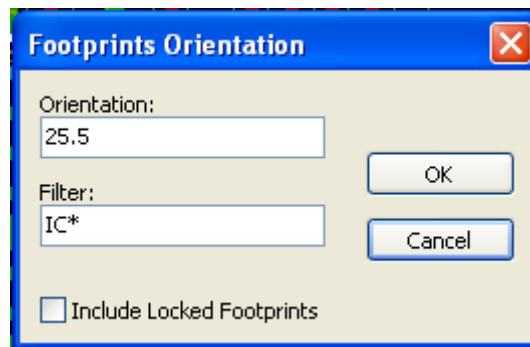
Here you can see the display of the module ratsnest during a move.  
The circuit once all the modules are placed may be as shown:



### 7.3 - General Reorientation of the modules

Initially all modules inherit the same orientation that they had in the library (normally 0).

If an alternative orientation is required for an individual module, or all modules (for example all vertical) use the menu option **AutoPlace/Orient All Modules**. This orientation can be selective (for example to relate only to the modules whose reference starts with "IC").




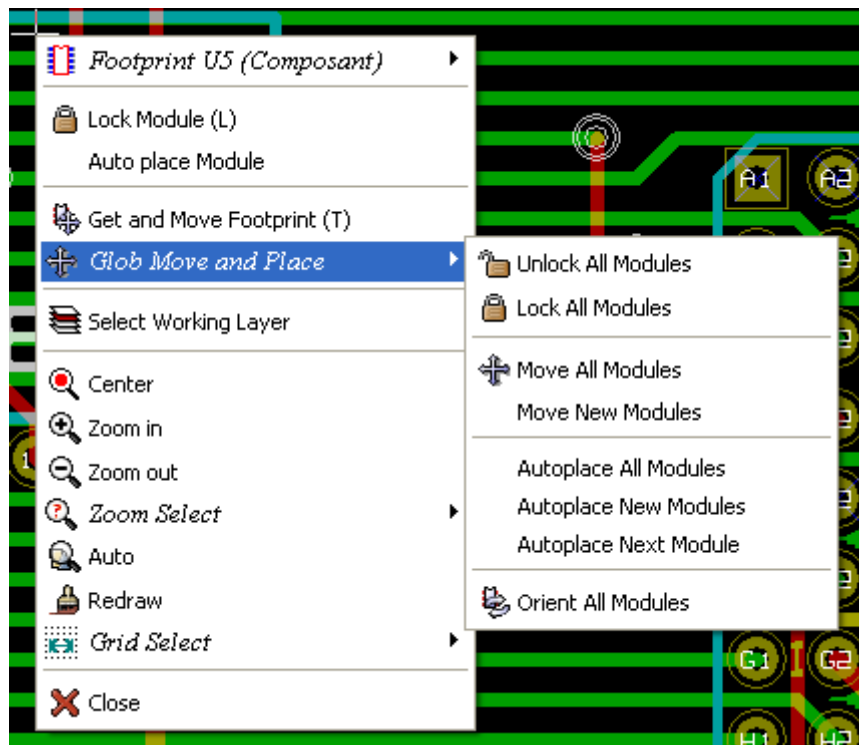
### 7.4 - Automatic Module Distribution

Note:

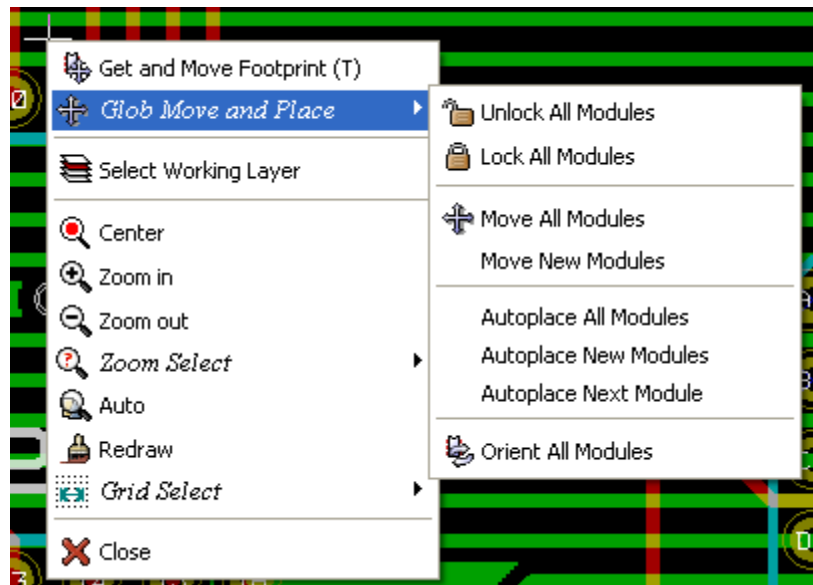
Generally modules can only be moved if they have not been "**Fixed**". This attribute can be turned on and off from the pop-up window (click right mouse button over module) whilst in Module Mode, or through the Edit Module Menu.

As stated in the last chapter, new modules loaded during the reading of the netlist appear piled up at a single location on the board. PCBNEW allows an automatic distribution of the modules to make manual selection and placement easier.

Select the option "**Module Mode**" (Icon  on the upper toolbar).  
The PopUp window activated by the right mouse button becomes:  
If there is a module under the cursor:



If there is nothing under the cursor:



In both cases the following commands are available:

- **Move all Modules** allows the automatic distribution of all the modules not Fixed. This is generally used after the first reading of a netlist.
- **Move new Modules** allows the automatic distribution of the modules which have not been placed already within the PCB outline. This commands requires that an outline of the board has been drawn to determine which modules can be automatically distributed.

## 7.5 - Automatic placement of the modules

### 7.5.1 - Characteristics of the automatic placer

The automatic module of placement allows the placement of the modules onto the 2 faces of the circuit board (however switching a module onto the copper layer is not automatic).

It also seeks the best orientation (0, 90, - 90, 180 degrees) of the module.

The placement is made according to an optimization algorithm, which seeks to minimize the length of the ratsnest, and which seeks to create space between the larger modules with with many pads. The order of placement is


optimized to initially place these larger modules with many pads.


### 7.5.2 - Preparation

PCBNEW can thus place the modules automatically, however it is necessary to guide this placement, because no software can guess what the user wants to achieve.

Before an automatic placement is carried out one must:

- Create the outline of the board (It can be complex, but it must be closed if the form is not rectangular).
- Manually place the components whose positions are imposed (Connectors, clamp holes...).
- Similarly certain SMD modules and critical components (large modules for example) must be on a specific side or position on the board and this must be done manually.
- Having completed any manual placement these modules must be "Fixed" to prevent them being moved.

With the Module Mode icon  selected right click on the module and pick "Fix Module" on the Pop-up menu. This can also be done through the Edit/Module Pop-up menu.

- Automatic placement can then be carried out. With the Module Mode icon  selected, right click and select Glob(al) Move and Place – then Autoplace All Modules.

During automatic placement, if required, PCBNEW can optimize the orientation of the modules. However rotation will only be attempted if this has been authorized for the module (see Edit Module Options).

Usually resistances and non-polarized capacitors are authorized for 180 degrees rotation. Some modules (small transistors for example) can be authorized for +/- 90 and 180 degrees rotation.

For each module one slider authorizes 90 degree Rot(ation) and a second slider authorizes 180 degree Rot(ation). A setting of 0 prevents rotation, a setting of 10 authorizes it, and an intermediate value indicates a preference for/against rotation.

The rotation authorization can be done by editing the module once it is placed on the board. However it is preferable to set the required options to the module in the library as these settings will then be inherited each time the module is used..

### 7.5.3 - Interactive Autoplacement

It may be necessary during automatic placement to stop (press Esc key) and manually re-position a module. Using the command **Autoplace Next Module** will restart the autoplacement from the point at which it was stopped.

The command **Autoplace new modules** allows the automatic placement of the modules which have not been placed already within the PCB outline. It will not move those within the PCB outline even if they are not 'fixed'.

The command **Autoplace Module** makes it possible to re-place the module pointed to by the mouse, even if its 'fixed' attribute is active.

### 7.5.4 - Note

PCBNEW automatically determines the possible zone of placement of the modules by respecting the shape of the board outline, which is not necessarily rectangular (It can be round, or have cutouts...).

If the board is not rectangular, the outline must be closed, so that PCBNEW can determine what is inside and what is outside the outline. In the same way, if there are internal cutouts, their outline will have to be closed.

PCBNEW calculates the possible zone of placement of the modules using the outline of the board, then passes each module in turn over this area in order to determine the optimum position at which to place it.

## 8 - Setting the routing parameters

### Headings:

#### [8 - Setting the routing parameters](#)

##### [8.1 - Current settings:](#)

###### [8.1.1 - Access to main dialog:](#)

###### [8.1.2 - Current settings:](#)

##### [8.2 - General Options.](#)

##### [8.3 - Netclasses:](#)

###### [8.3.1 - Setting routing parameters](#)

###### [8.3.2 - Netclass editor:](#)

###### [8.3.3 - Global Design Rules.](#)

###### [8.3.4 - Via Parameters.](#)

###### [8.3.5 - Track Parameters.](#)

###### [8.3.6 - Specific sizes:](#)

##### [8.4 - Examples and typical dimensions](#)

###### [8.4.1 - Track width](#)

###### [8.4.2 - Insulation \(clearance\)](#)

###### [8.4.3 - Examples:](#)

###### [8.4.3.1 - 'Rustic'](#)

###### [8.4.3.2 - 'Standard'](#)

##### [8.5 - Manual routing](#)

###### [8.5.1 - Help when creating tracks:](#)

###### [8.5.2 - Creating tracks:](#)

###### [8.5.3 - Moving and dragging tracks](#)

###### [8.5.4 - Via Insertion](#)

##### [8.6 - Select/edit the track width and via size](#)

###### [8.6.1 - Using the horizontal toolbar:](#)

###### [8.6.2 - Using the pop-up menu:](#)

##### [8.7 - Track edition and correction:](#)

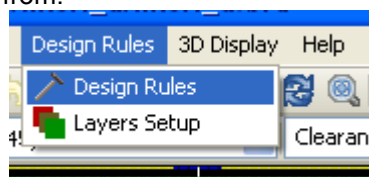
###### [8.7.1 - Change a track:](#)

##### [8.8 - Global Changes:](#)

### 8.1 - Current settings:

#### 8.1.1 - Access to main dialog:

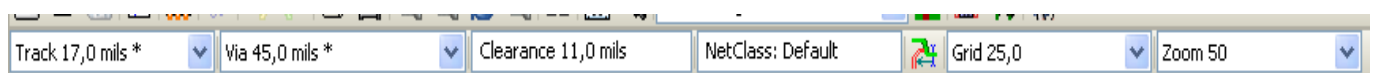
Most important parameters are accessed from:



and are set in the **Design Rules** dialog.

#### 8.1.2 - Current settings:

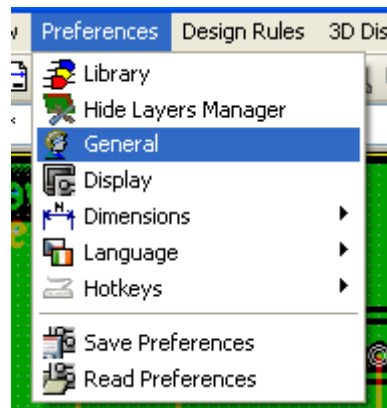
Current settings are displayed on the toolbar:



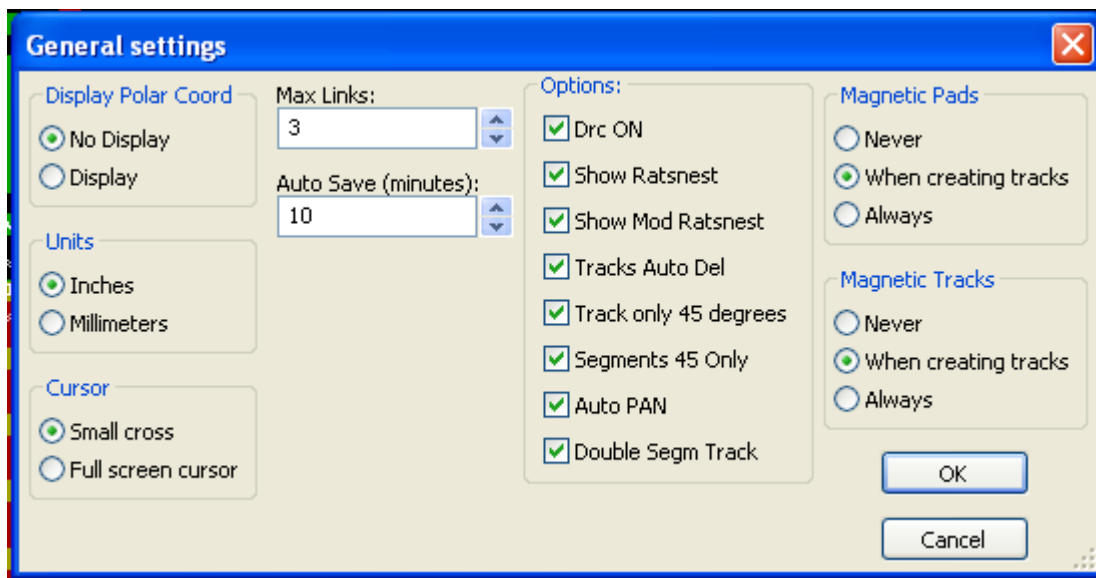
### 8.2 - General Options.

General options are acceded from:





The opened dialog is:



For tracks creation, parameters are:

- Tracks 45 Only: Directions allowed for track segments are 0, 45 or 90 degrees
- Double Segm Track: When creating tracks, 2 segments will be displayed.
- Tracks Auto Del: When recreating tracks, the old one will be automatically delete if it is redundant.
- Magnetic Pads: The graphic cursor goes to pad, center when entering the pad area.
- Magnetic Tracks: The graphic cursor goes to track axis.

### 8.3 - Netclasses:

Pcbnew allows you to define routing parameters for each net.

In fact, parameters are defined for a group of nets.

- A group of nets is called a **Netclass**.
- There is always a netclass called **default**.
- Users can add some others Netclasses.

A netclass specify:

- The width of tracks and via diameters and drills.
- The clearance between pads and tracks (or vias)

When routing, Pcbnew selects automatically the netclass corresponding to the net of the track to create or edit, and therefore the routing parameters.

#### 8.3.1 - Setting routing parameters

The choice is made in the menu: **Design Rules ->Design Rules** .

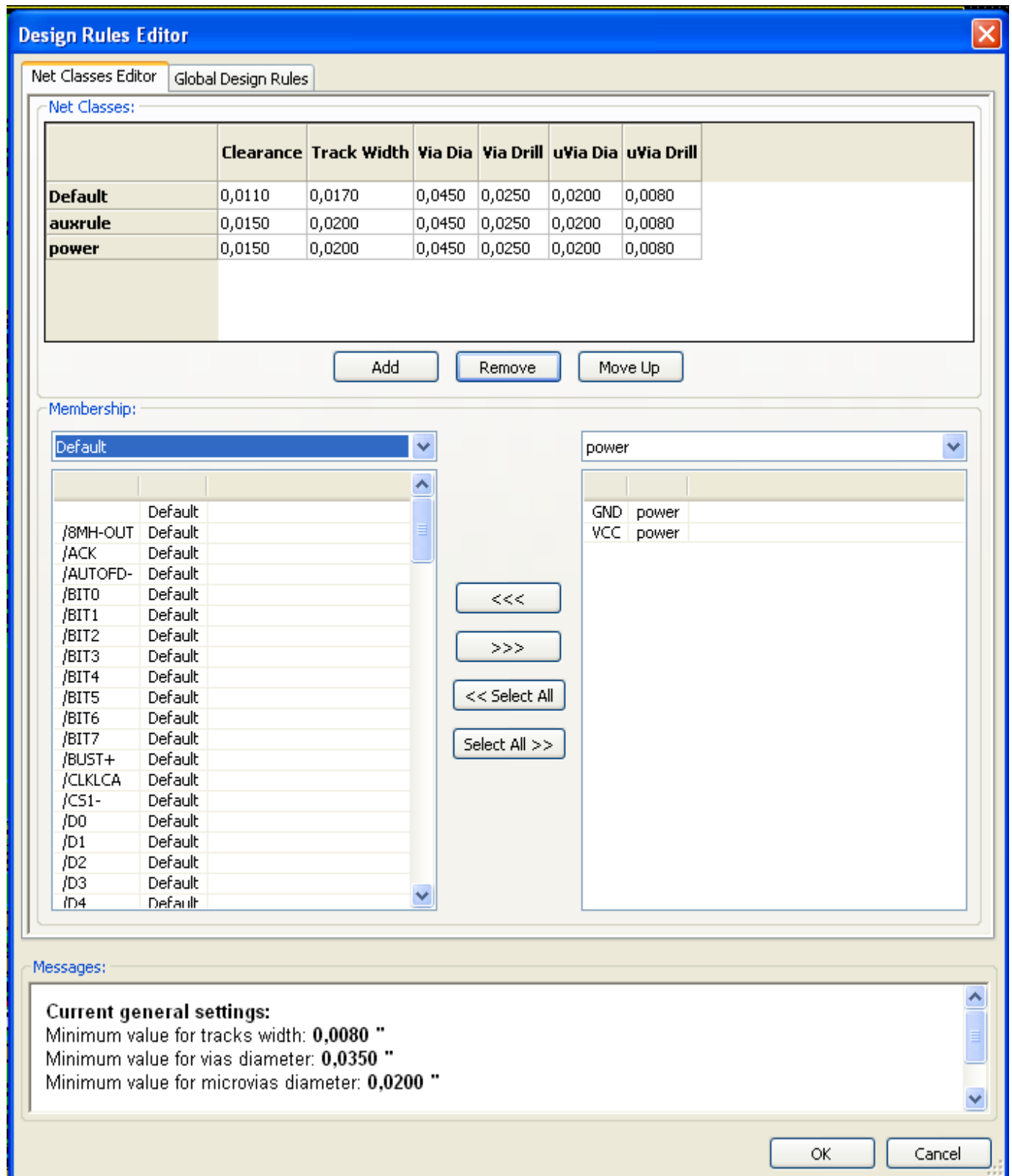
#### 8.3.2 - Netclass editor:

The Netclass editor allows you :

- To add or delete Netclasses.

## Pcbnew

- To set routing parameters values : Clearance, track width, via sizes
- To group nets in netclasses.



### 8.3.3 - Global Design Rules.

They are:

- Via type
- Enabling/disabling micro-via use.
- Minimum clearance (minimum distance between tracks, vias and pads).
- Minimum tracks and vias sizes.

A DRC error is set when a value less than a minimum value specified here is encountered

The second dialog panel is:

**Design Rules Editor**

Net Classes Editor | **Global Design Rules**

**Via Options:**

Default Via Type

☒ Through via

☐ Blind or buried via

Min via diameter ("): 0,0350

Min via drill dia ("): 0,0190

**Micro Via Options:**

Micro Vias:

☒ Do not allow micro vias

☐ Allow micro vias

Min micro via drill dia ("): 0,0200

Min micro via drill dia ("): 0,0070

**Minimum Allowed Values:**

Min track width ("): 0,0080

Specific via diameters and track widths, which can be used to replace default Netclass values on demand, for arbitrary via or track segments.

**Custom Via Sizes:**

Drill value: a blank or 0 => default Netclass value

	Diameter	Drill
<b>Via 1</b>	0,0450	
<b>Via 2</b>	0,0550	0,0320
<b>Via 3</b>	0,0550	0,0400
<b>Via 4</b>	0,0650	0,0320
<b>Via 5</b>		
<b>Via 6</b>		
<b>Via 7</b>		

**Custom Track Widths:**

	Width
<b>Track 1</b>	0,0200
<b>Track 2</b>	0,0250
<b>Track 3</b>	
<b>Track 4</b>	
<b>Track 5</b>	
<b>Track 6</b>	
<b>Track 7</b>	

**Messages:**

**Current general settings:**

Minimum value for tracks width: **0,0080 "**

Minimum value for vias diameter: **0,0350 "**

Minimum value for microvias diameter: **0,0200 "**

OK Cancel

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This dialog also allows to enter a “stock” of tracks and vias sizes.

When routing, one can select one of these values to create a track or via, instead of using the default netclasses values ones.

Useful in critical cases when a small track segment must have a specific size.

### 8.3.4 - Via Parameters.

Pcbnew handles 3 types of vias:

- The through via (usual vias).
- Blind or buried vias,.
- Micro Vias, like buried vias but restricted to an external layer to its nearest neighbor. They are intended to connect BGA to the nearest inner layer. the diameter is usually very small and they are drilled by laser.

By default, all vias have the same drill value.

This dialog specify the smallest acceptable values for vias parameters.

On board, a via smaller than specified here generates a DRC error.

### 8.3.5 - Track Parameters.

Specify the minimum acceptable track width.

On board, a track width smaller than specified here generates a DRC error.

### 8.3.6 - Specific sizes:

Specific via diameters and track widths, which can be used to replace default Netclass values on demand, for arbitrary via or track segments.

Custom Via Sizes:

Drill value: a blank or 0 => default Netclass value

	Diameter	Drill
Via 1	0,0450	
Via 2	0,0550	0,0320
Via 3	0,0550	0,0400
Via 4	0,0650	0,0320
Via 5		
Via 6		
Via 7		

Custom Track Widths:

	Width
Track 1	0,0200
Track 2	0,0250
Track 3	
Track 4	
Track 5	
Track 6	
Track 7	

One can enter a stock of extra tracks and/or vias sizes.

While routing a track, these values can be used on demand instead of the values from the current netclass values.

## 8.4 - Examples and typical dimensions

### 8.4.1 - Track width

Use the largest possible value and conform to the minimum sizes given here:

Units	CLASS 1	CLASS 2	CLASS 3	CLASS 4	CLASS 5
mm	0,8	0,5	0,4	0,25	0,15
mils	31	20	16	10	6

### 8.4.2 - Insulation (clearance)

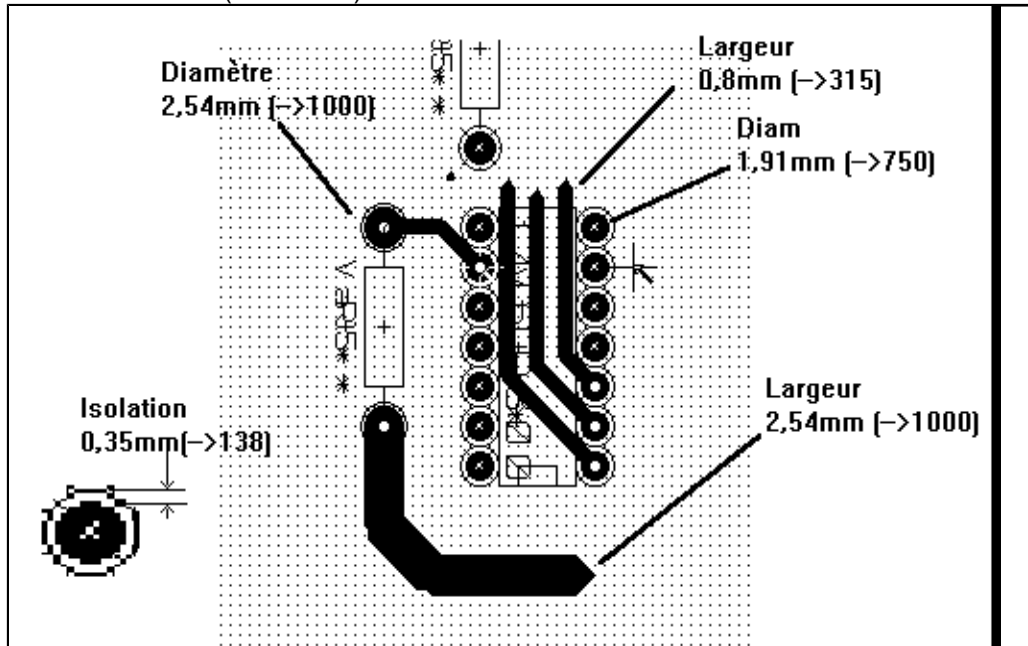
Unité	CLASS 1	CLASS 2	CLASSE3	CLASS 4	CLASS 5
mm	0,70	0,5	0,35	0,23	0,15
mils	27	20	14	9	6

Usually, the minimum clearance is very similar to the minimum track width.

### 8.4.3 - Examples:

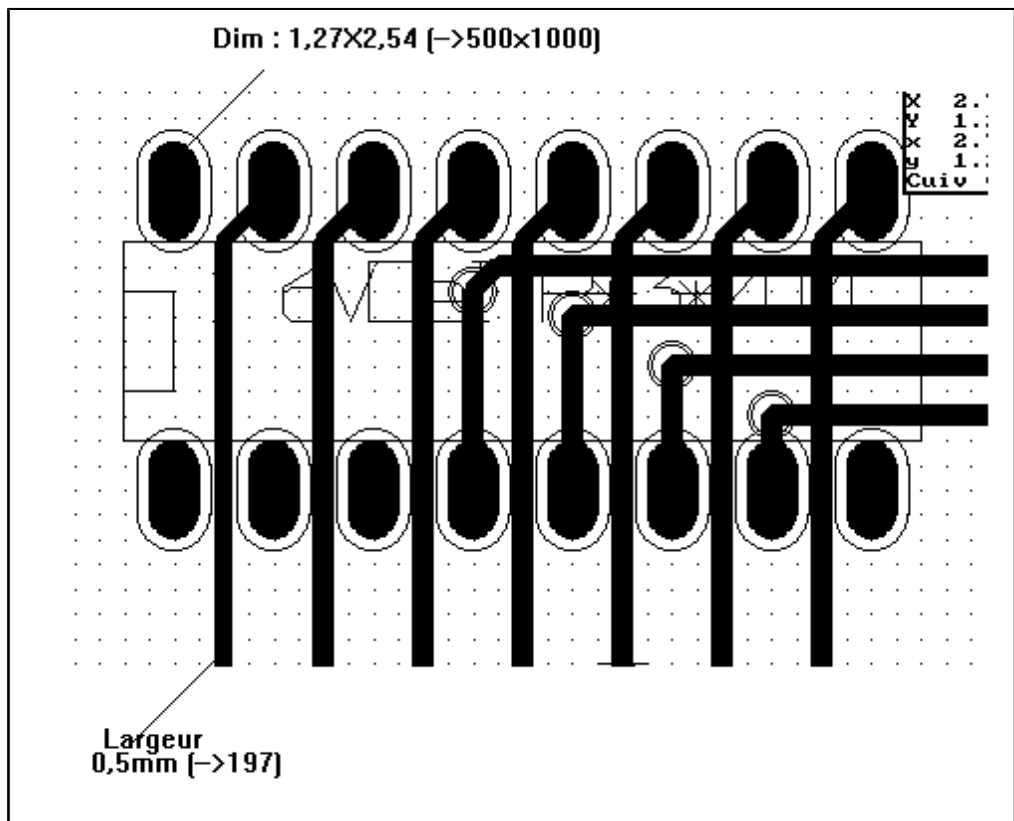
#### 8.4.3.1 - 'Rustic'

- Clearance: 0.35mm (0.0138 inches).
- Track width: 0.8mm (0.0315 inches).
- Pad diameter for ICs and vias: 1.91mm (0.0750 inches).
- Pad diameter for discrete components: 2.54mm (0.1 inches).
- Ground track width: 2.54mm (0.1 inches).



#### 8.4.3.2 - 'Standard'

- Clearance: 0.35mm (0.0138 inches).
- Track width: 0.5mm (0.0127 inches).
- Pad diameter for ICs: make them elongated in order to allow tracks to pass between IC pads and yet have the pads offer a sufficient adhesive surface (1.27 x 2.54 mm -->0.05x 0.1 inches).
- Vias : 1.27mm (0.0500 inches).




## 8.5 - Manual routing

Manual routing is recommended, because it is the only method offering control of routing priorities. For example, it is preferable to start by routing power tracks, making them wide and short and keeping analog and digital supplies well separated. Then sensitive signal tracks should be routed. Amongst other problems, automatic routing often requires many vias. However, automatic routing can offer useful insight into the positioning of modules. With experience, you will probably find that the automatic router is useful for quickly routing the 'obvious' tracks, but the remaining tracks will best be routed by hand.


### 8.5.1 - Help when creating tracks:

Pcbnew can display the full ratsnest, if the tool  is activated.

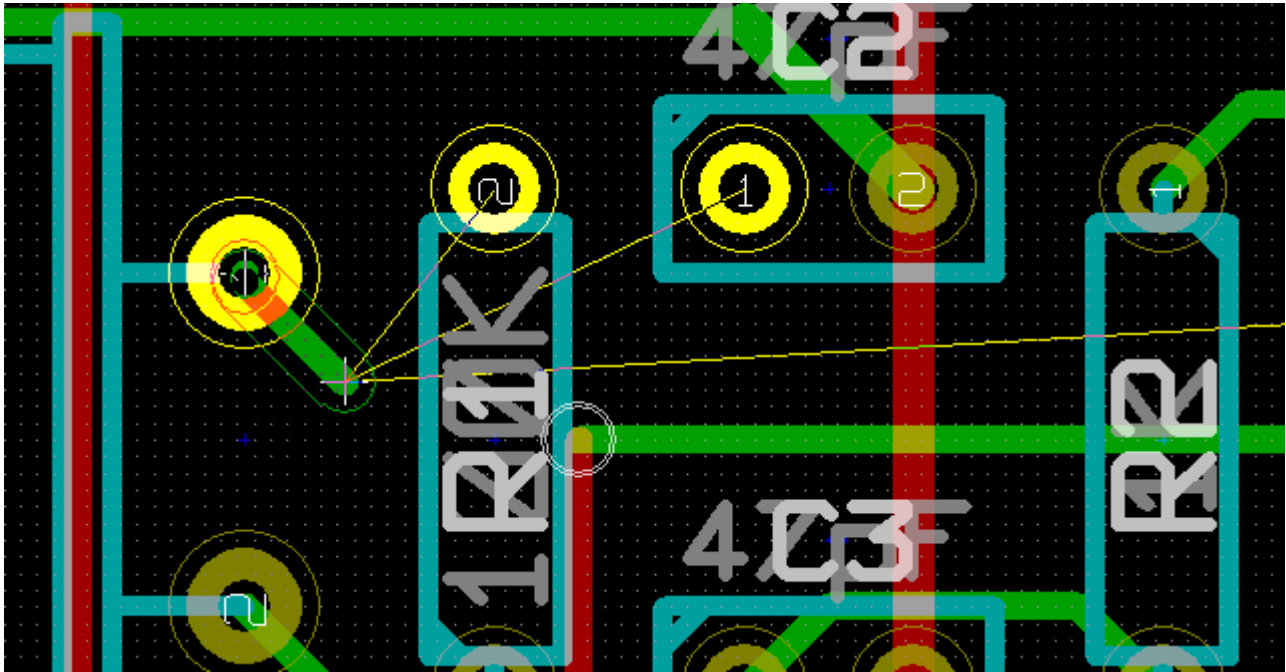
The tool  allows to highlight a net (click to a pad or an existing track to highlight the corresponding net net). The DRC checks in real time tracks when creating them. One cannot create a track which does not match the DRC rules.

It is possible to disable DRC by activate , but **it is very dangerous. Use it only in specific cases.**

### 8.5.2 - Creating tracks:

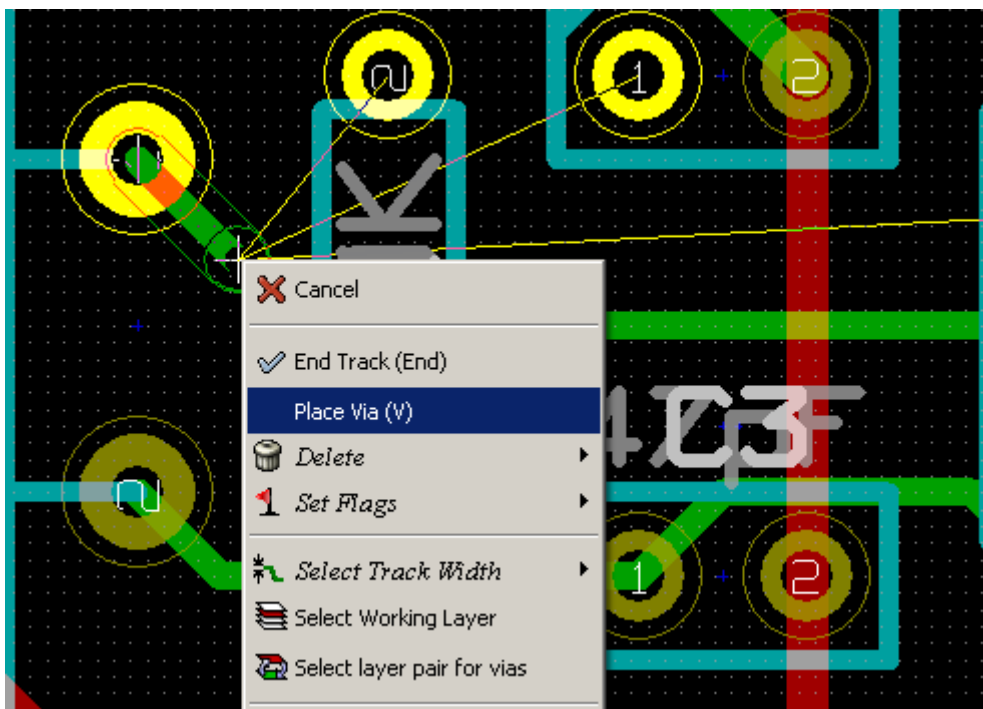
Access by 

A new track must start on a pad or on an other track, because Pcbnew must know the net used for the new track (in order to match the DRC rules).




When creating a new track, Pcbnew shows links to nearest not connected pads (link number set in option "**Max. Links**" in **General Options**).

Finish the track by a double click, or by the Pop-up menu or by the hot key:



### 8.5.3 - Moving and dragging tracks

When the tool  is active, the track where the cursor is pointed to can be moved with the hotkey 'm'. If you want to drag the track you use the hotkey 'g'.

### 8.5.4 - Via Insertion


A via can be inserted only when a track is in progress:

- By the Pop-up menu
- By the hot key (here: 'V').
- By switching to a new copper layer using the suitable hotkey.

## 8.6 - Select/edit the track width and via size

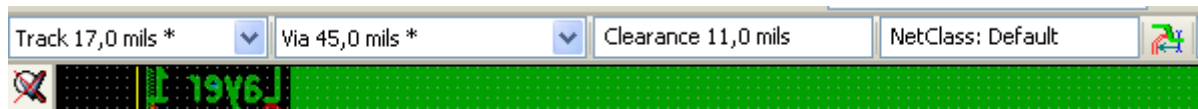
When clicking on a track or a pad, Pcbnew automatically selects the corresponding Netclass, and the track size and vias dimensions from this netclass.

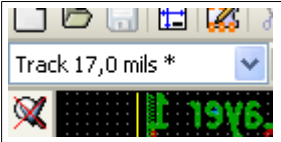
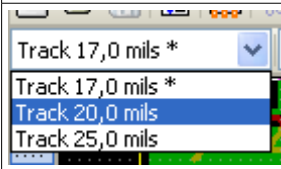
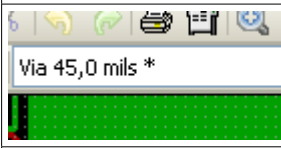
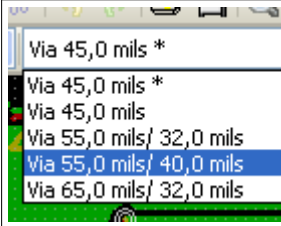
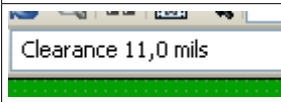
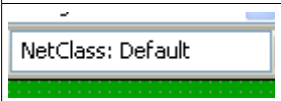

As previously seen, the Global Design Rules editor has a tool to enter extra tracks and vias sizes.

- The horizontal toolbar can be used to select a size.
- When the tool  is active, the current track width can be selected from the Pop-up menu (accessible also when creating a track)

Therefore, the user can use the default Netclasses values, or, when needed, a specified value.

### 8.6.1 - Using the horizontal toolbar:

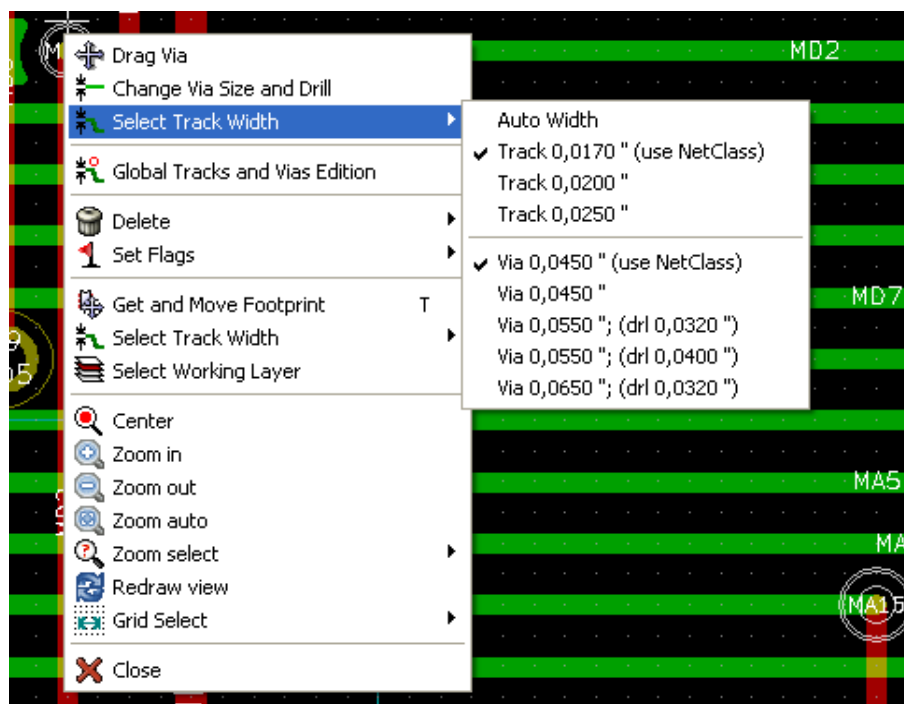


	<p>Track width selection. The symbol * is a mark for default Netclass value selection</p>
	<p>Selecting a specific track width value. The first value in list is always the netclass value. Others values are tracks widths entered from the Global Design Rules editor.</p>
	<p>Via size selection. The symbol * is a mark for default Netclass value selection</p>
	<p>Selecting a specific via dimension value. The first value in list is always the netclass value. Others values are vias dimensions entered from the Global Design Rules editor.</p>
	<p>Display the current clearance value. This is the clearance value set in the current selected Netclass.</p>
	<p>Current selected Netclass. When clicking on a track or a pad, Pcbnew automatically selects the corresponding Netclass, and displays its name here.</p>
	<p>When enabled: Automatic track width selection: When starting a track on an existing track, the new track has the same width as the existing track.</p>

### 8.6.2 - Using the pop-up menu:

One can select a new size for routing, or to change a previously created via or track segment.



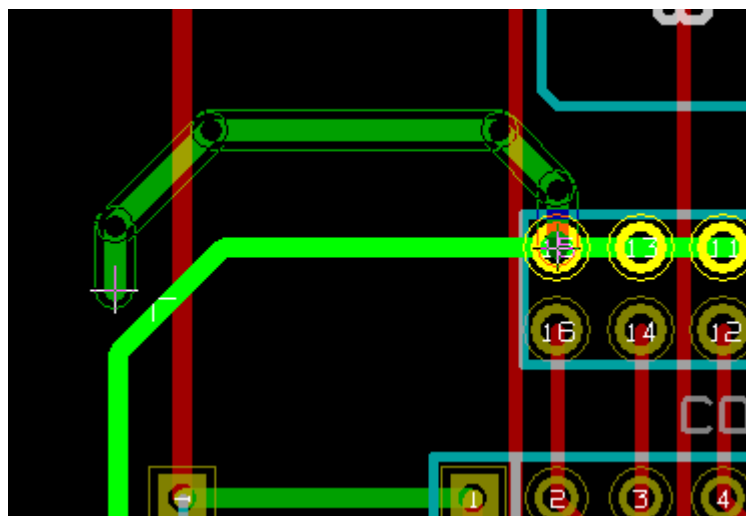


If you want to change many vias (or tracks) size, the best solution is to use a specific Netclass to for the net(s) that must be edited (see global changes).

## 8.7 - Track edition and correction:

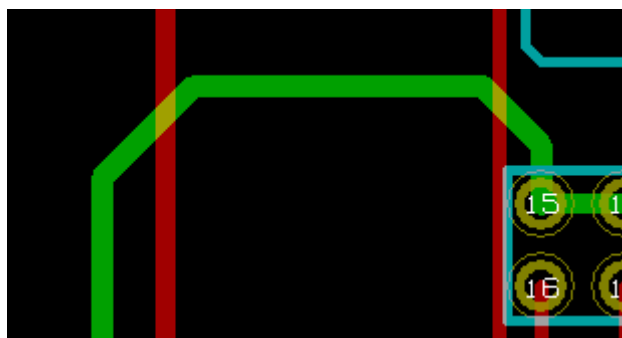
### 8.7.1 - Change a track:

In many cases redraw a track is enough:



new track (in progress).

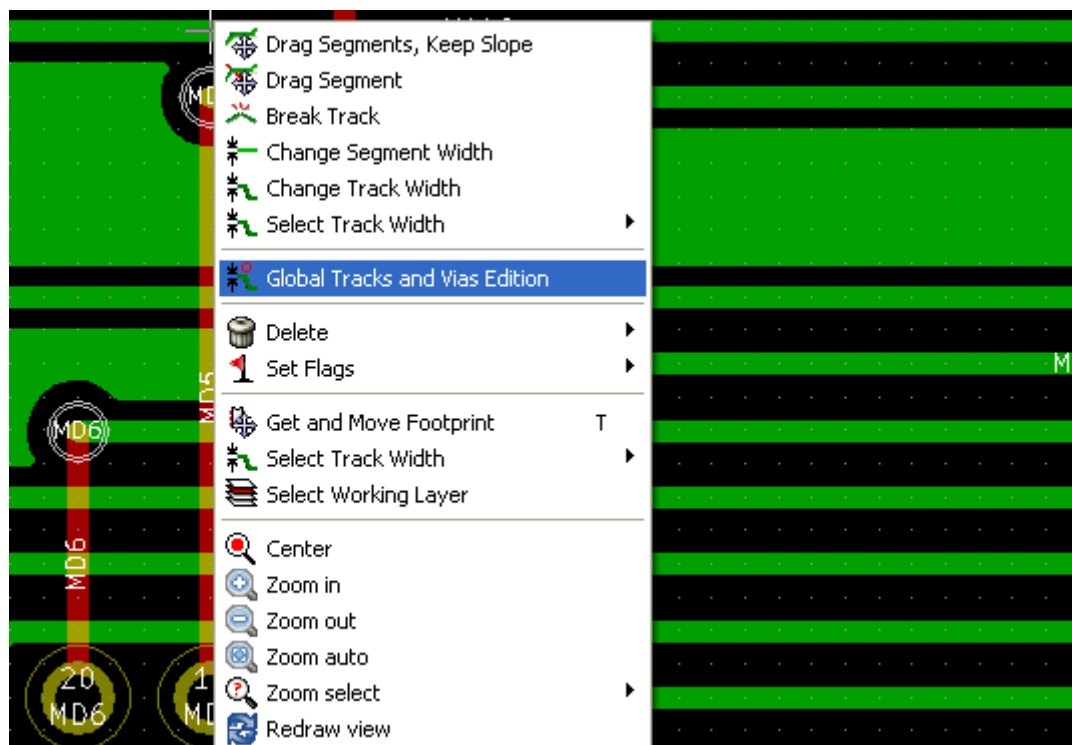
When finished:



Pcbnew **remove automatically** the old track if it is redundant.

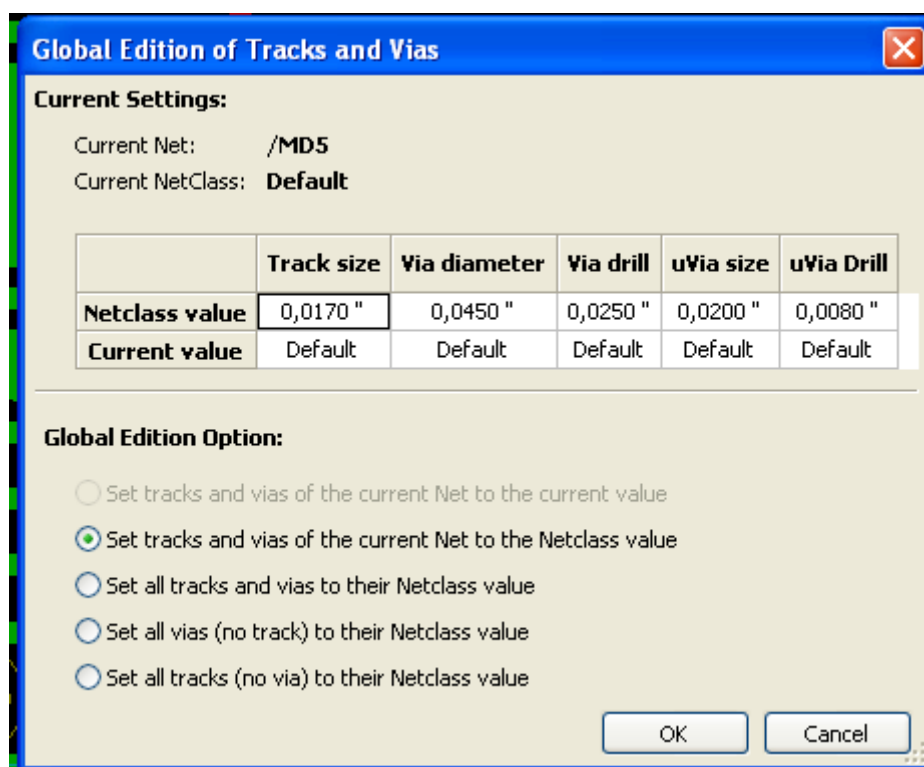
### 8.8 - Global Changes:

Access to the global tracks and vias sizes dialog editor is by right clicking on a track to show the Popup menu



The dialog editor allows a global change of tracks and/or vias for:

- The current net.
- The whole board.



## 9 - Creating zones

### Headings:

#### [9 - Creating zones](#)

##### [9.1 - Creating the zones on copper layers:](#)

##### [9.2 - Creating a zone:](#)

##### [9.2.1 - Creating the zone limits:](#)

##### [9.2.2 - Filling the zone:](#)

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##### [9.3.1 - Mode for filling.](#)

##### [9.3.2 - Clearance and minimum copper thickness](#)

##### [9.3.3 - Pad options](#)

##### [9.3.4 - Thermal reliefs parameters:](#)

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##### [9.4 - Adding a Cutout area inside a zone:](#)

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##### [9.7 - Final zone Filling.](#)

##### [9.8 - Zones net names changes:](#)

##### [9.9 - Creating the zones on technical layers:](#)

##### [9.9.1 - Creating the zone limits:](#)

Copper zones are defined by an outline (closed polygon), and can include holes (closed polygons inside the outline).

A zone can be drawn on a copper layer, or a technical layer.

### 9.1 - Creating the zones on copper layers:

Pad (and tracks) connections by filled copper areas are tested by the DRC. So a zone must be filled (not just created) to connect pads.

Pcbnew uses currently track segments or polygons to fill copper areas.

Each option has its advantages and its drawbacks, mainly when redraw screen.

The final result is the same.

**For calculation time reasons, the zone filling is not remade after each change, but only:**

- If a filling zone command is made.
- When a DRC test is made

***Copper zones must be filled or refilled after changes in tracks or pads.***

Copper zones (Usually ground and power planes) are usually attached to a net.

In order to create a copper zone one must:


- Select parameters (net name, layer ...)  
Switch on the layer and highlight this net is not mandatory but is a good practice.
- Create the zone limit (If no, All the board will be filled.)
- Fill the zone.

Pcbnew try to fill zones all of a piece, and usually, there is not any unconnected copper block. So it can happens some fragments of areas remain not filled.

Zones having no net are not cleaned and can have insulated islands.

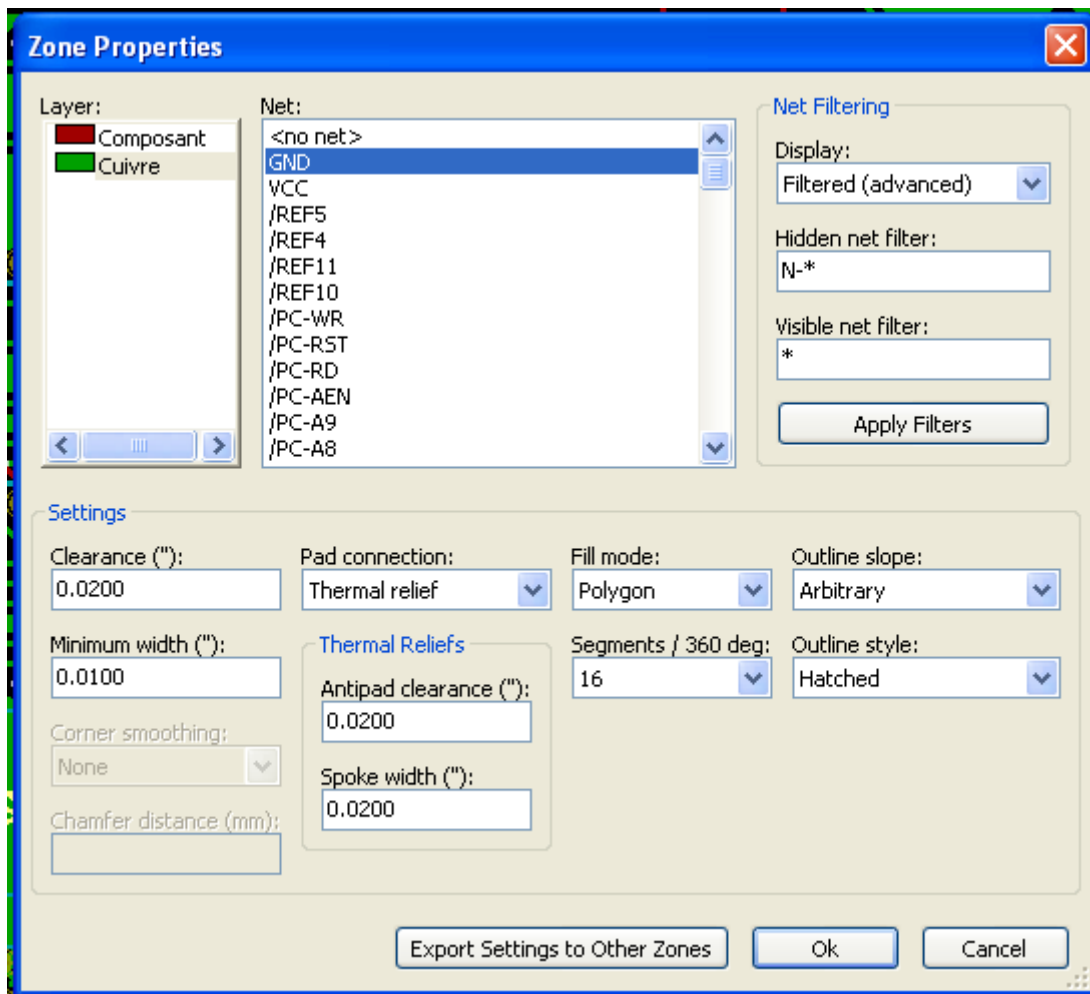
### 9.2 - Creating a zone:

#### 9.2.1 - Creating the zone limits:

Use the tool .

**The active layer must be a copper layer.**

When clicking to start the zone outline, the dialog box is opened:



On can set parameters for this zone (net, layer, filling options, pad options ...)

Draw the zone limit, on this layer.

This zone limit is a polygon, created by a click (left button) for each corner.

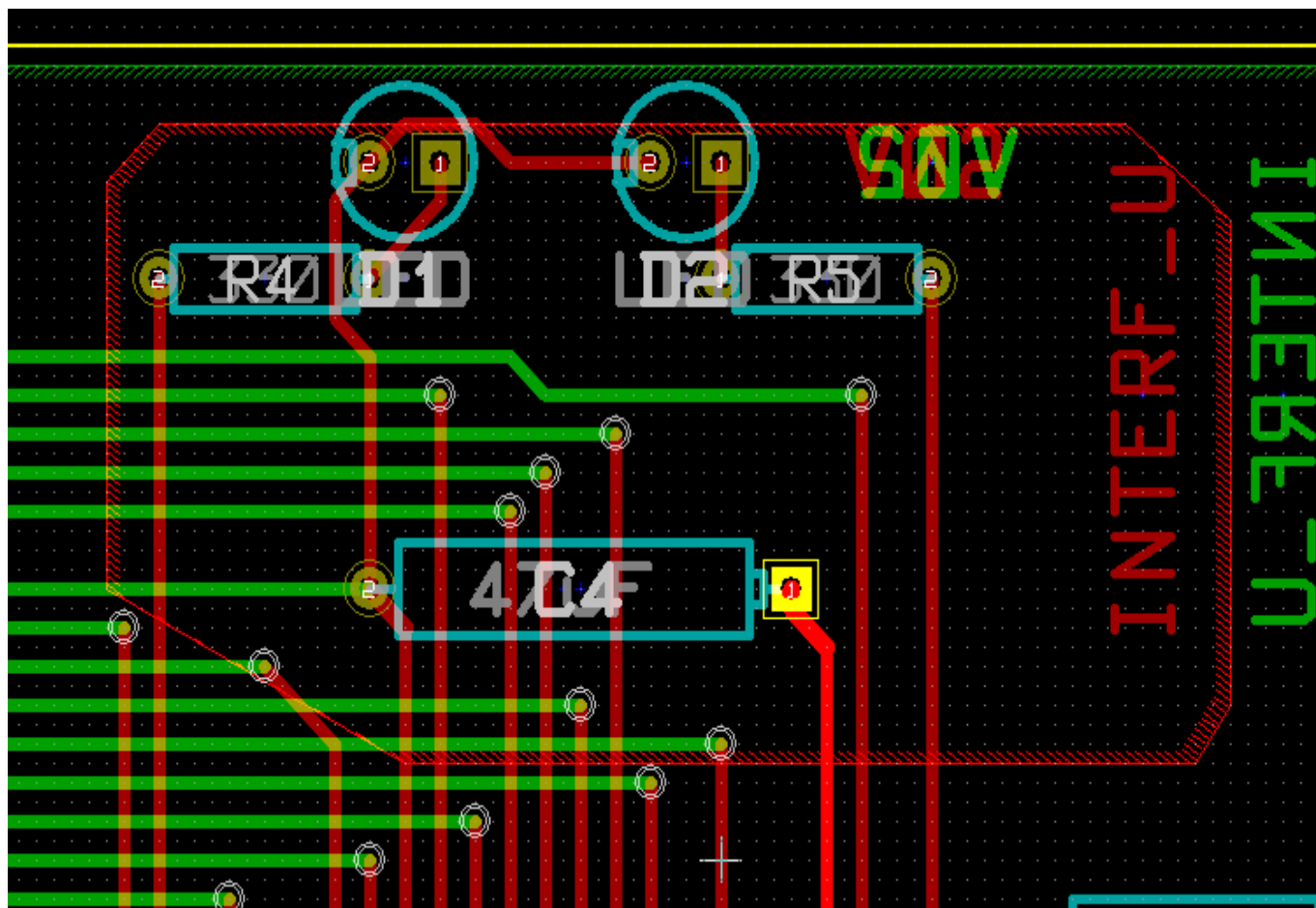
A double click ends the polygon.

The polygon will be automatically closed. If the starting point and the ending point are not at the same coordinate, Pcbnew will add a segment from the end point to the start point.

Remark:

- **The DRC control is active when creating zone outlines.**
- **A corner which creates a DRC error will NOT be accepted by Pcbnew.**

Here is a zone limit created (polygon in thin hatched line):

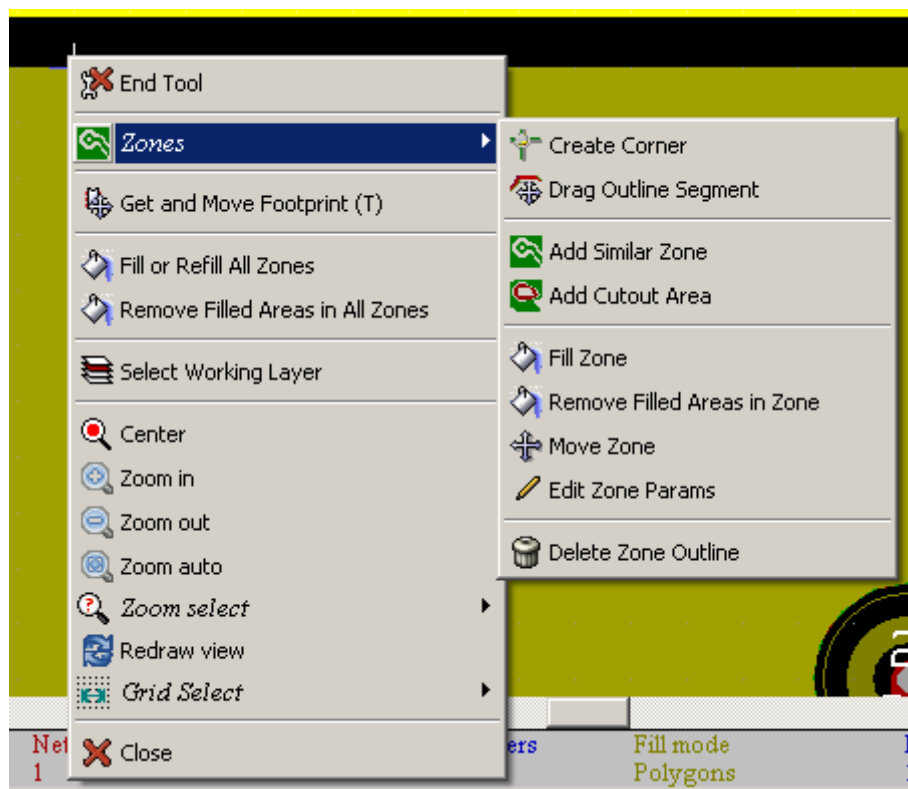


### 9.2.2 - Filling the zone:

**Note:**

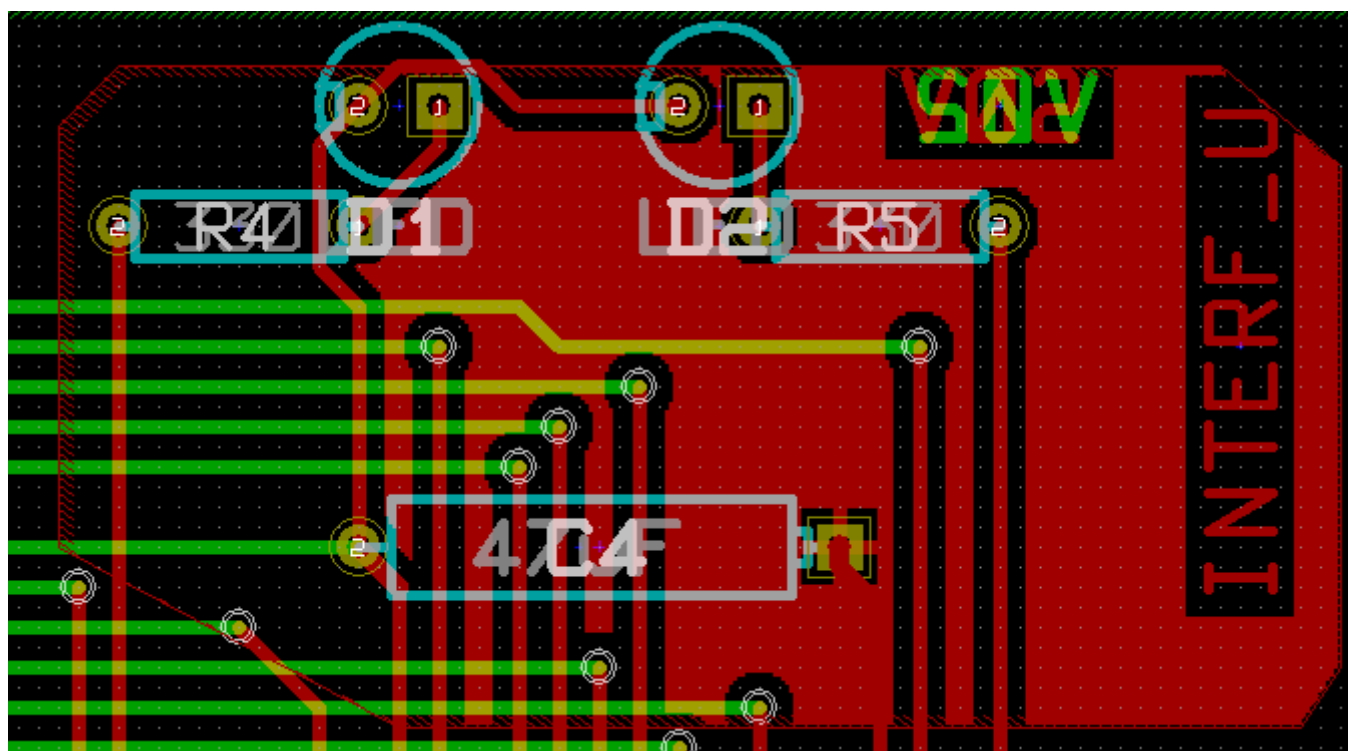
**When filling a zone, Pcbnew remove all not connected copper islands.**

To access to the zone filling command, right click on the edge zone::



Activate the **"Fill Zone"** command

Here is the filling result for a starting point *inside* the polygon:



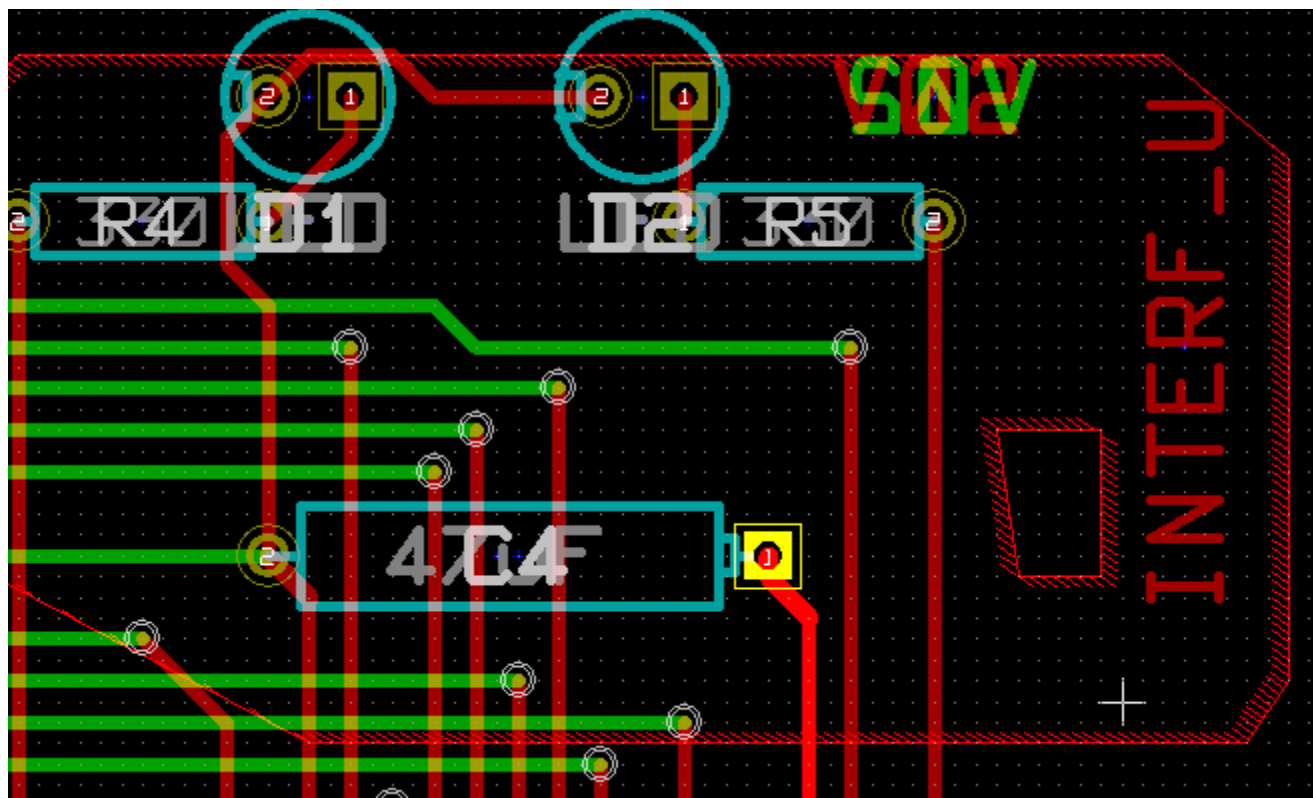
The polygon is a frontier for filling.

One can see a non filled area inside the zone, because this area is not accessible:

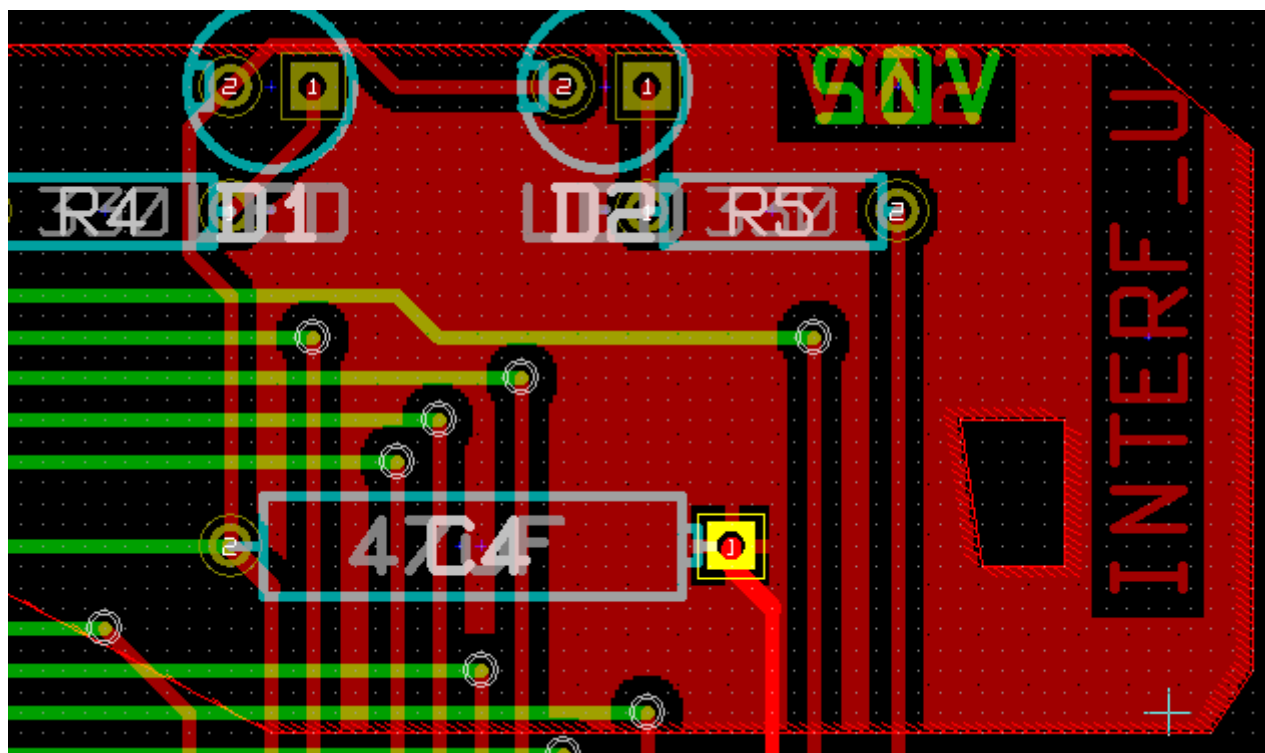
- A track creates a frontier and
- There is no starting point for filling in this area.

**Note:**

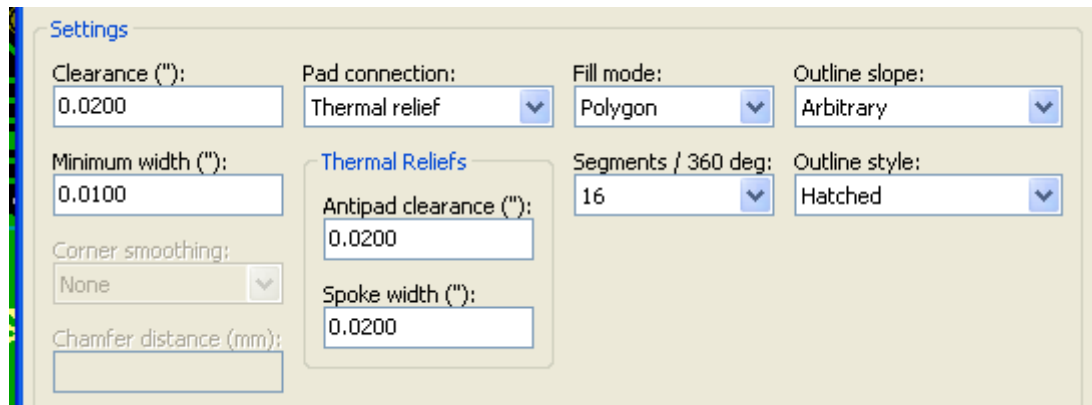
you can use many polygons to create cutout areas. Here is an example:



Here is the result:



### 9.3 - Fill Options:



One must choose:

- The mode for filling.
- The clearance and minimum copper thickness.
- How the pads are drawn inside the zone (or connected to this zone).
- Thermal reliefs parameters.
- ....

### 9.3.1 - Mode for filling.

Zones can be filled using polygons or segments.

Results are same.

If you have problems with polygon mode (slow screen refresh) uses segments.

### 9.3.2 - Clearance and minimum copper thickness

A good choice for clearance is a grid a bit bigger than the routing grid.

Minimum copper thickness value ensures there is no too small copper areas in zones.

Warning: if the value is too large, small shapes like thermal stubs in thermal reliefs cannot be drawn.

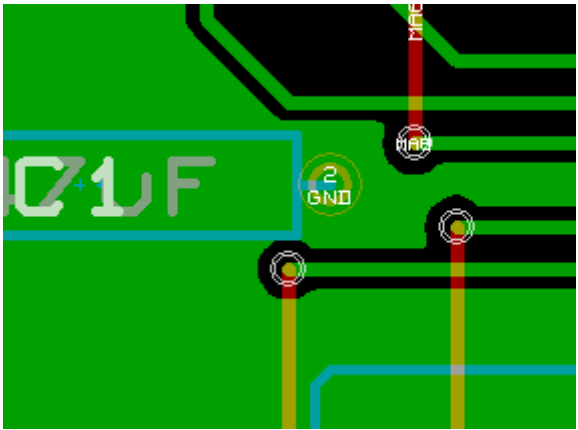
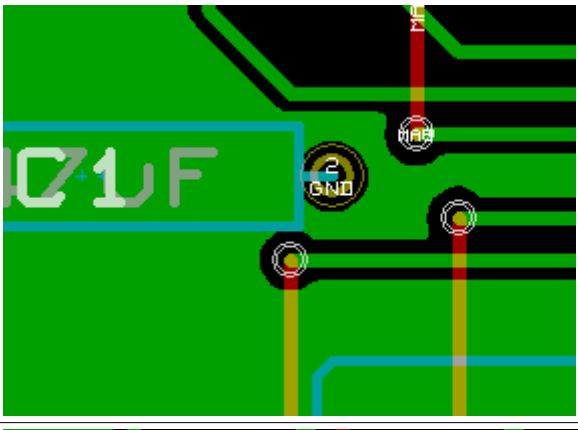
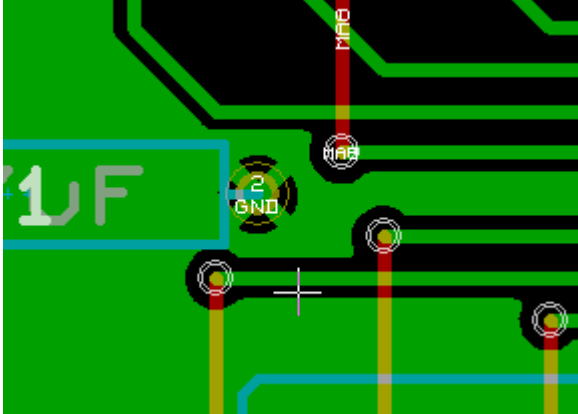
### 9.3.3 - Pad options

Pads of the net can either be included or excluded from the zone, or connected by thermal reliefs.

- If included, soldering and unsoldering can be very difficult.
- If excluded, the connection to the zone is not very good.
- A thermal relief is a good compromise.

Here is the result for the 3 options:



	<p>Include Pads</p>
	<p>Exclude Pads</p> <p>Note:</p> <ul style="list-style-type: none"> <li>• The zone can be filled only if tracks exists to connect zones areas.</li> <li>• Pads must be connected by tracks.</li> </ul>
	<p>Thermal relief. Pad is connected by 4 track segments. The segment width is the current value used for the track width.</p>

### 9.3.4 - Thermal reliefs parameters:

Thermal Reliefs

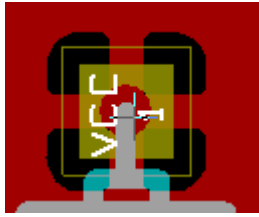
Antipad clearance ("):  
0.0200

Spoke width ("):  
0.0200

One can set 2 parameters for thermal reliefs:

Copper width

AntiPad Size



### 9.3.5 - Parameters choice:

**The copper width value for thermal reliefs must be bigger than the minimum thickness value for copper zone. If not the case, they cannot be drawn.**

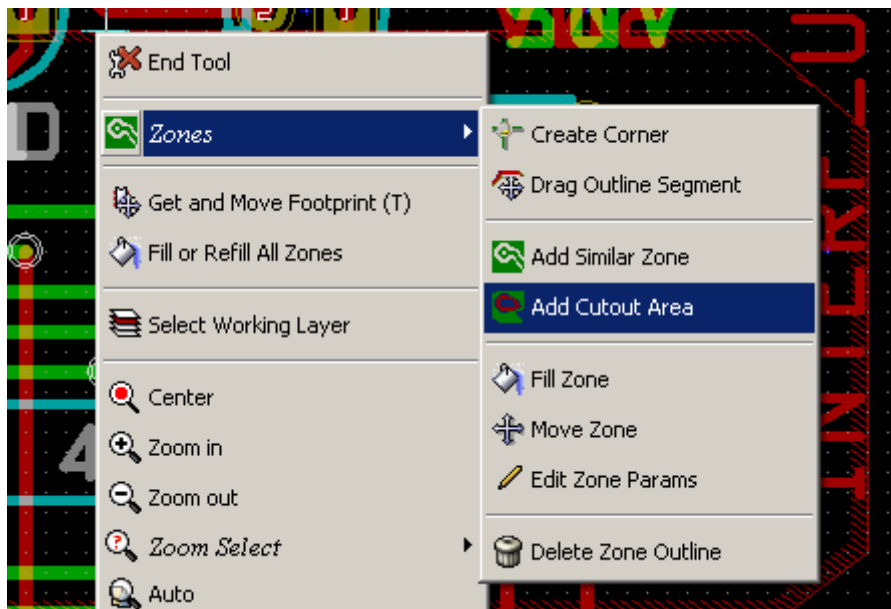
Besides, a too large value for this parameter or for antipad size does not allow to create a thermal relief for small pads (like pads sizes used in SMD).

### 9.4 - Adding a Cutout area inside a zone:

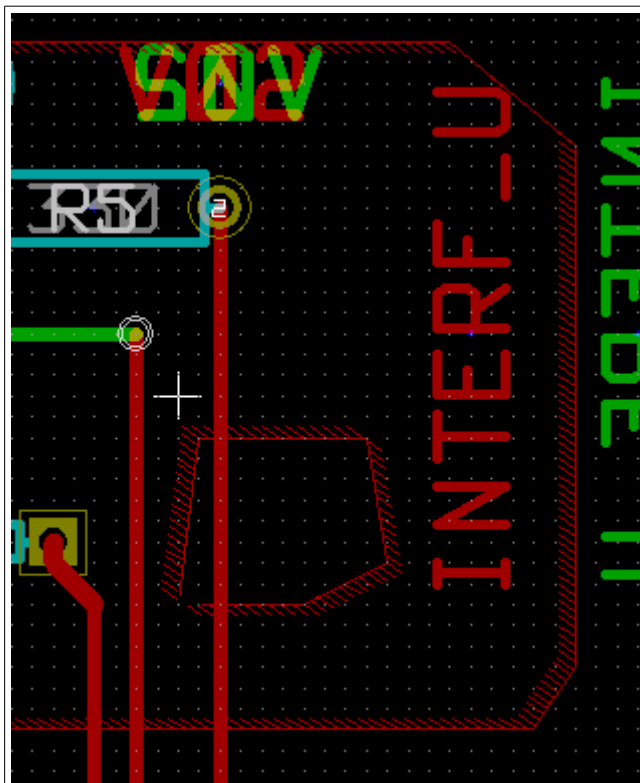
Obviously, a zone must already exist.

To add a cutout area (a non filled area inside the zone):

- Right click on an existing edge outline.
- Select Add Cutout Area.
- Creates the new outline.



After creating outline:



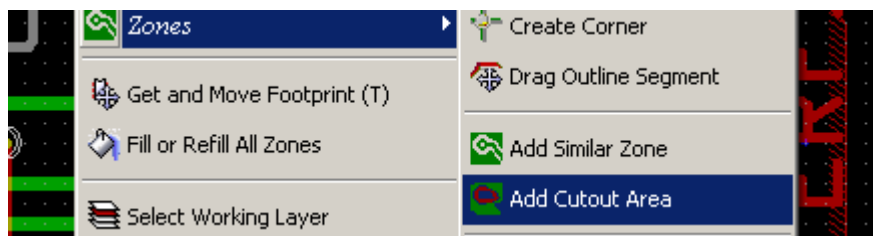
See the cutout outline.

### 9.5 - Outlines editing:

An outline can be modified by:

- Moving a corner or an edge
- Deleting or Adding a corner
- Adding a similar zone, or a cutout area

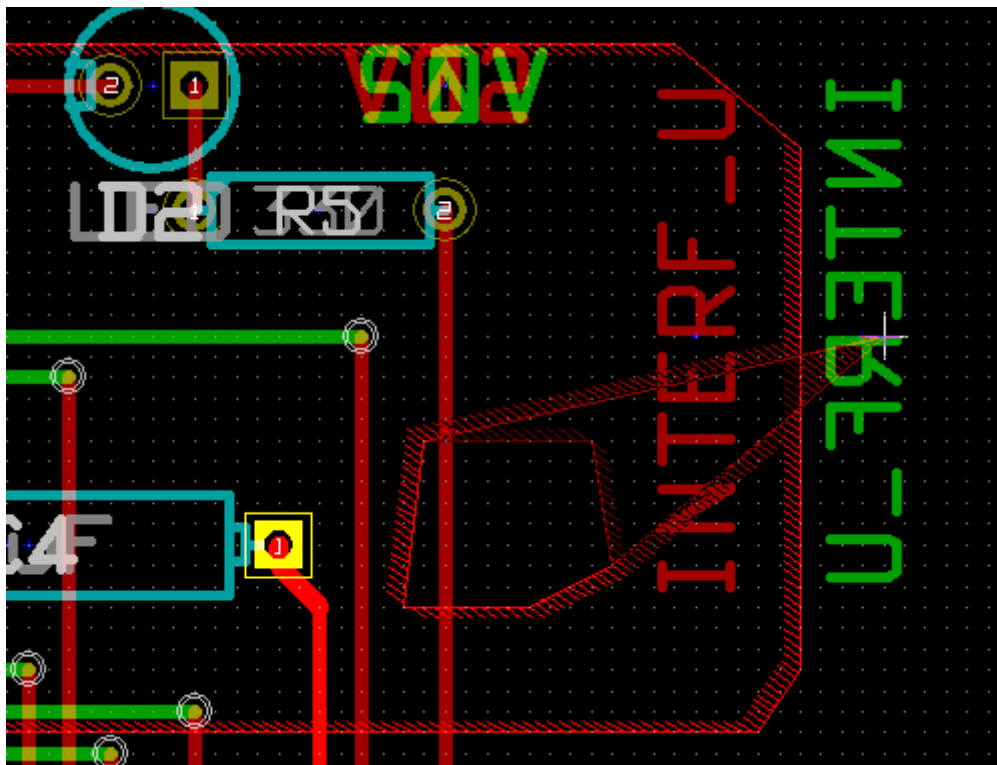
And if polygons are overlapping they will be combined.



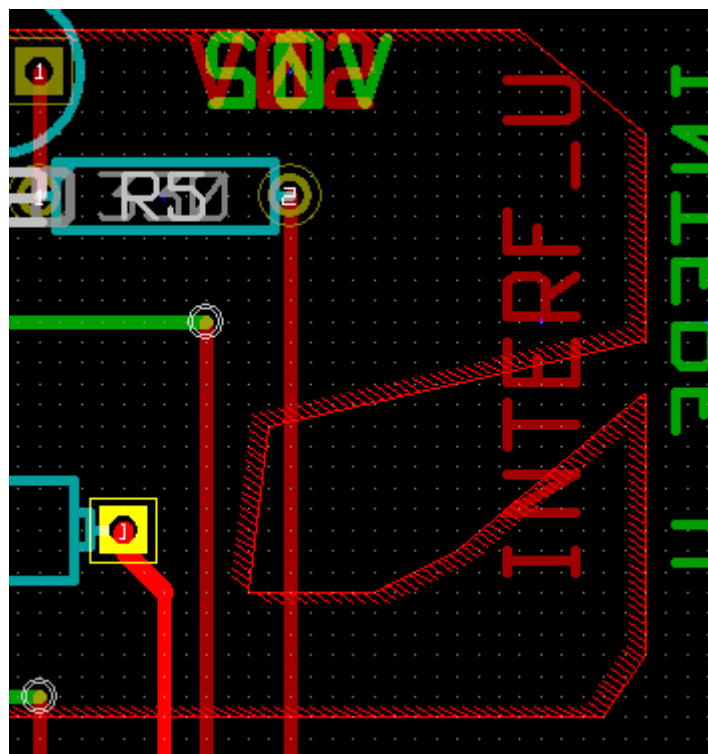
To do that:

Right click on a corner or on an edge, en select the command.

Here is a corner (from a cutout) moving:

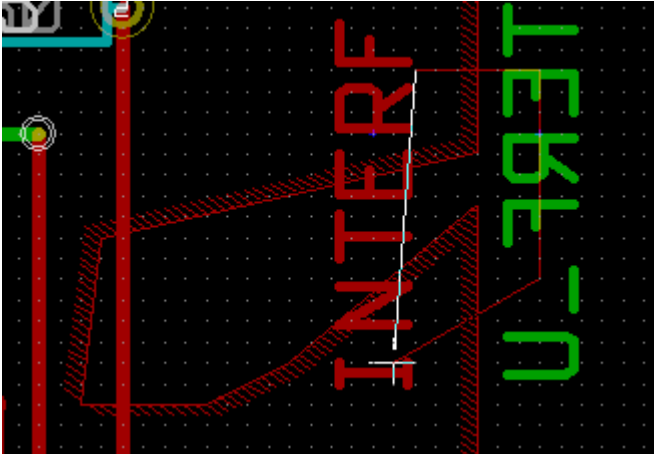
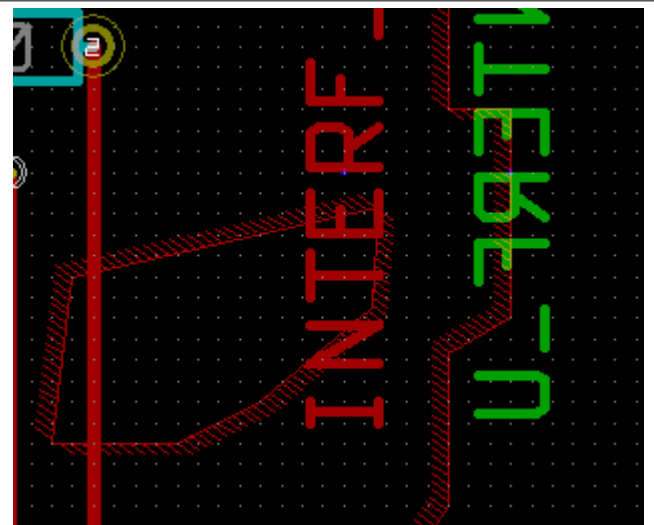


Here is the result:




Polygons are combined.

Adding a similar zone:

	Adding the zone
	Result

### 9.6 - Editing zone: parameters

When right clicking on an outline, and using Edit Zone Params



, The Zone params

Dialog box is opened.

Initial parameters can be edited.

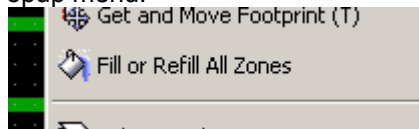
If the zone is already filled, refilling it will be necessary.

### 9.7 - Final zone Filling.

When the board is finished, one must fill or refill all zones.

To do that:

- Activate the tool zones (.
- Right click to display the Popup menu.
- Use Fill or Refill All Zones



Warning, calculations can take some time, if filling grid is small.

### 9.8 - Zones net names changes:

After editing a schematic, nets can have their name changed.


For instance **VCC** can be changed to **+5V**.


When a global DRC control is made Pcbnew checks if the zone net name exists, and displays an error if not.

A "by hand" parameter zone edition will be necessary to change the old name to the new one.

## 9.9 - Creating the zones on technical layers:

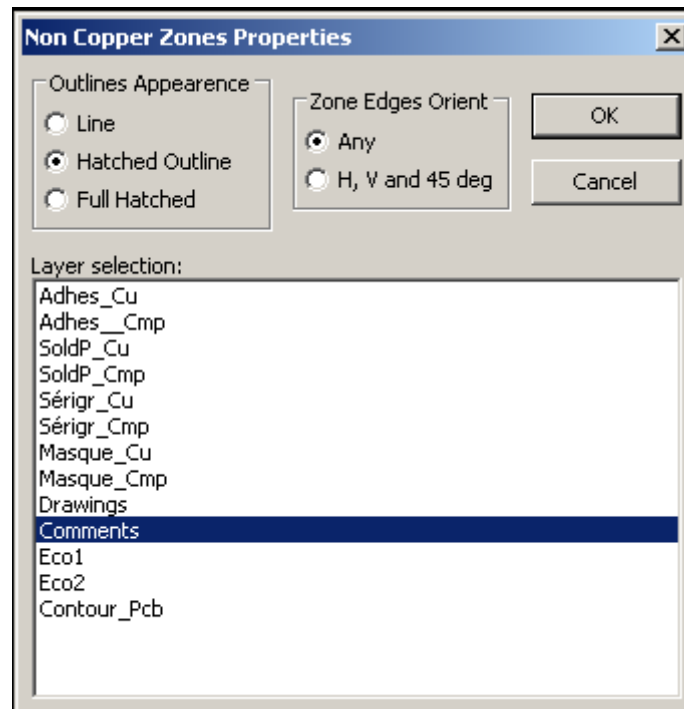
### 9.9.1 - Creating the zone limits:

Sélectionner l'outil .

Use the tool .

**The active layer must be a technical layer.**

When clicking to start the zone outline, the dialog box is opened:



Select the technical layer to place the zone

Draw the zone outline like previously said for copper layers..

#### Notess:

- For outlines edition, uses the same way as for copper zones.
- One also can add Cutout Areas.

## 10 - Preparation of files for circuit fabrication

### Headings:

#### [10 - Preparation of files for circuit fabrication](#)

##### [10.1 - Note:](#)

##### [10.2 - Final preparations](#)

##### [10.3 - Final DRC test:](#)

##### [10.4 - Setting the coordinates origin:](#)

##### [10.5 - Generating files for photo-tracing](#)

###### [10.5.1 - GERBER format:](#)

###### [10.5.2 - HPGL Format:](#)

###### [10.5.3 - POSTSCRIPT Format:](#)

###### [10.5.4 - Plot options:](#)

##### [10.6 - Global clearance settings for the solder stop and the solder paste mask:](#)

###### [10.6.1 - Solder mask clearance:](#)

###### [10.6.2 - Solder paste clearance](#)

##### [10.7 - Generating the drill file\(s\)](#)

##### [10.8 - Generating the cabling documentation:](#)

##### [10.9 - Generation of file\(s\) for automatic component insertion:](#)

##### [10.10 - Advanced tracing options:](#)

### 10.1 - Note:

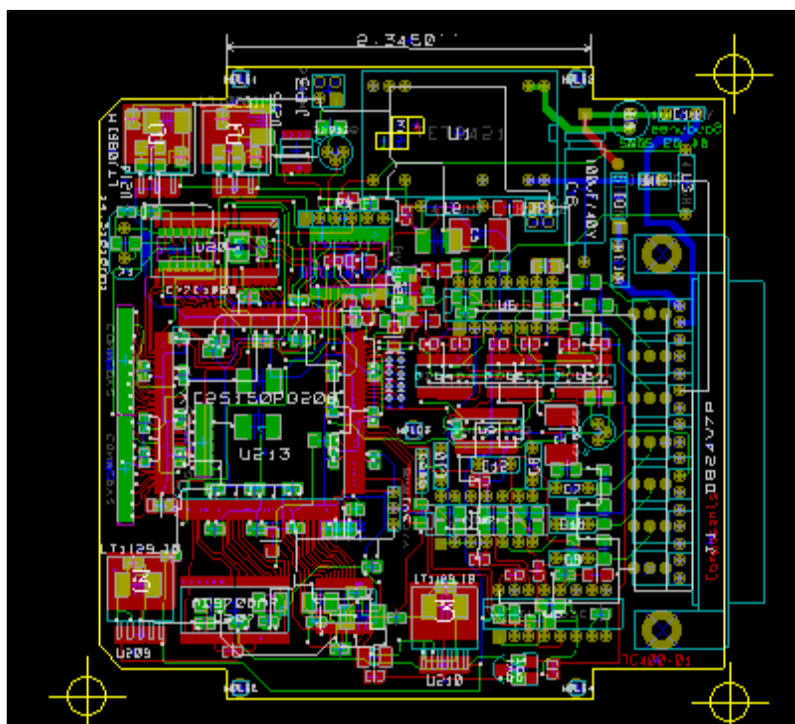
All of the files generated are placed in the working directory, i.e., in the same directory as the file **xxxxxx.brd** of the printed circuit board.

### 10.2 - Final preparations

#### It is necessary to:

- Indicate the layer (e.g., '*top or front*' and '*bottom or back*') and project names by placing appropriate text upon each of the layers.
- All text on the '*copper*' (sometimes called '*solder*' or '*bottom*') side must be mirrored.
- Create any (ground)planes, modifying traces as required to ensure they are contiguous.
- Place alignment crosshairs and possibly the dimensions of the board outline (these are usually placed on one of the general purpose layers).

Here is an example showing all of these elements, except the ground planes, which have been omitted for better visibility:



A color key for the 4 copper layers has also been included:




### 10.3 - Final DRC test:

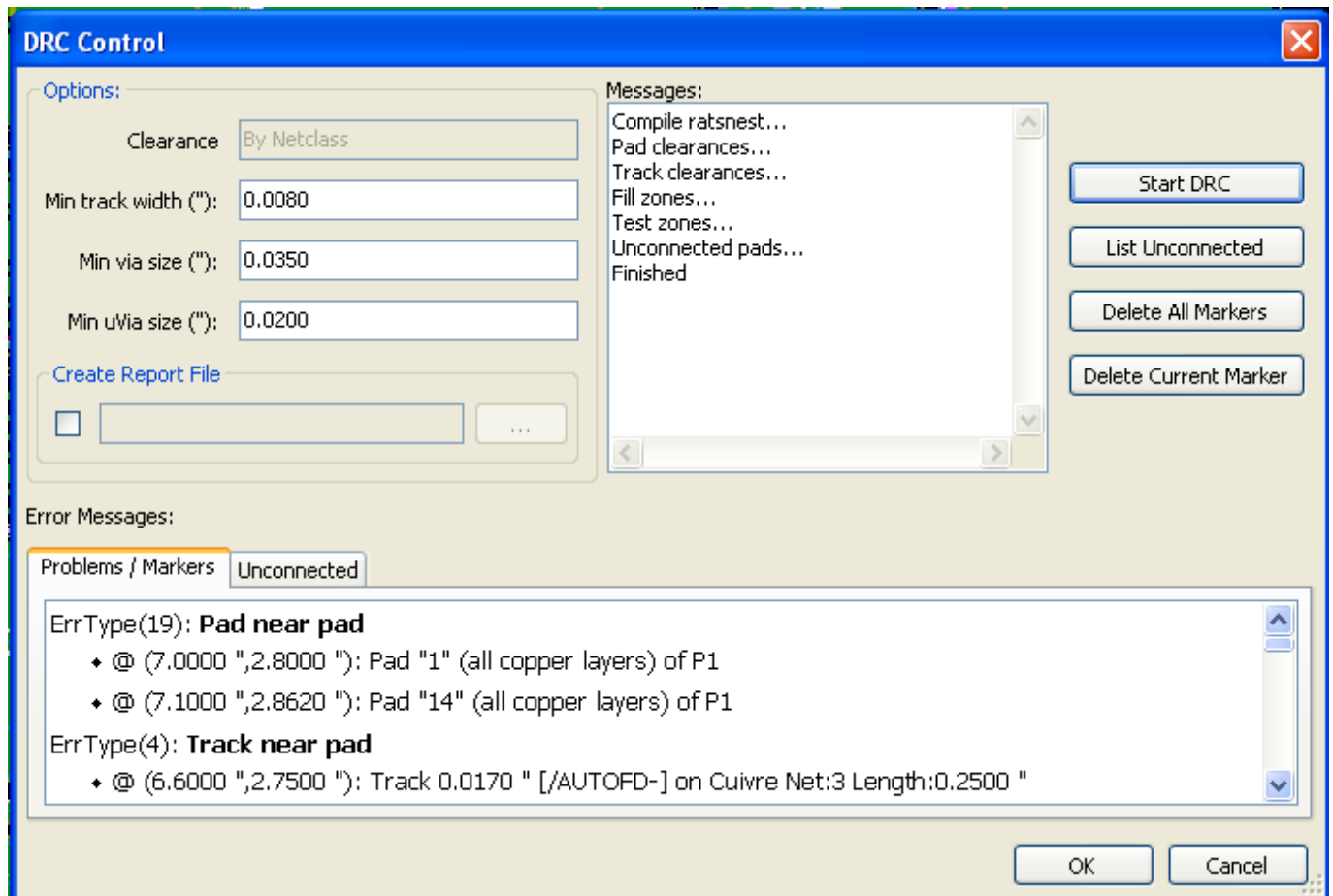
Before generating the output files, a global DRC is **very strongly recommended**.

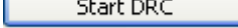
**Note:**

Zones are filled or refilled when starting a DRC.

Press the button  to launch the DRC dialog:





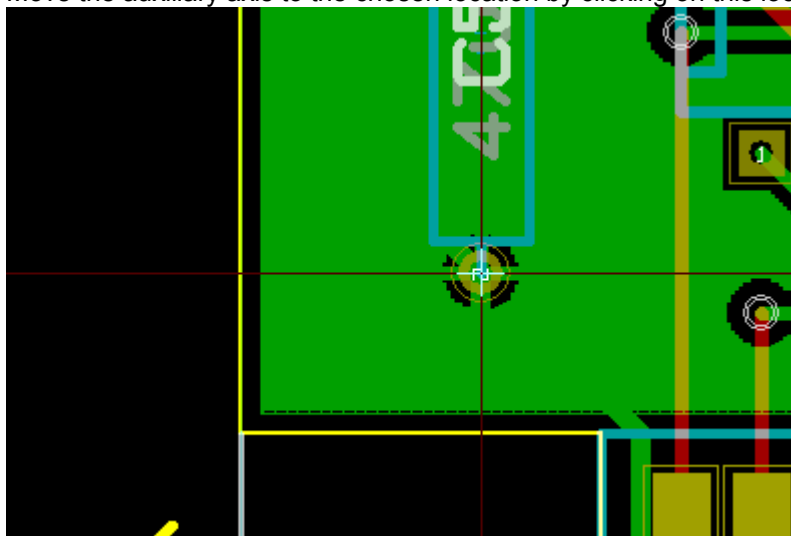
Adjust parameters, and then press . This final check will prevent any unpleasant surprises...

#### 10.4 - Setting the coordinates origin:

Set the coordinates origin for the photo plot and drill files, one must place the *auxiliary axis* on this origin.

Activate .

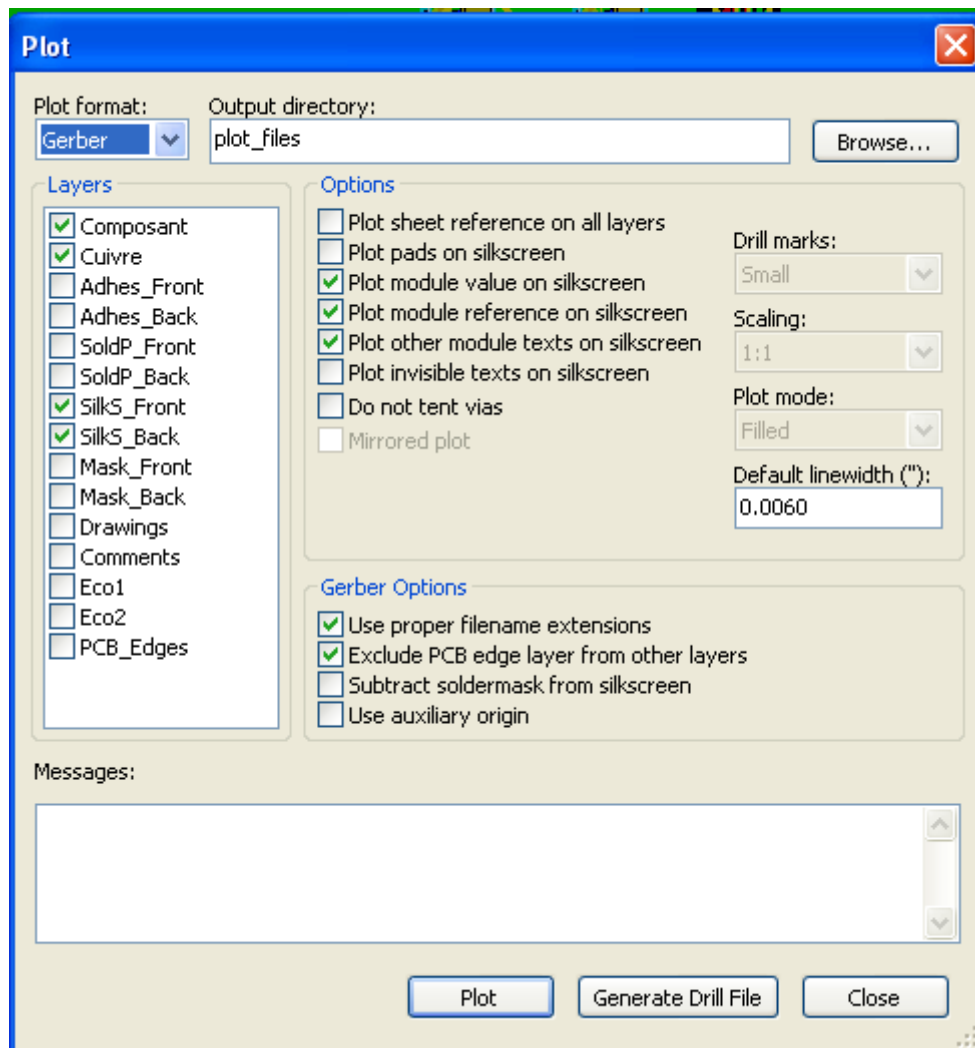
Move the auxiliary axis to the chosen location by clicking on this location:



Here is the auxiliary axis on the pad

#### 10.5 - Generating files for photo-tracing

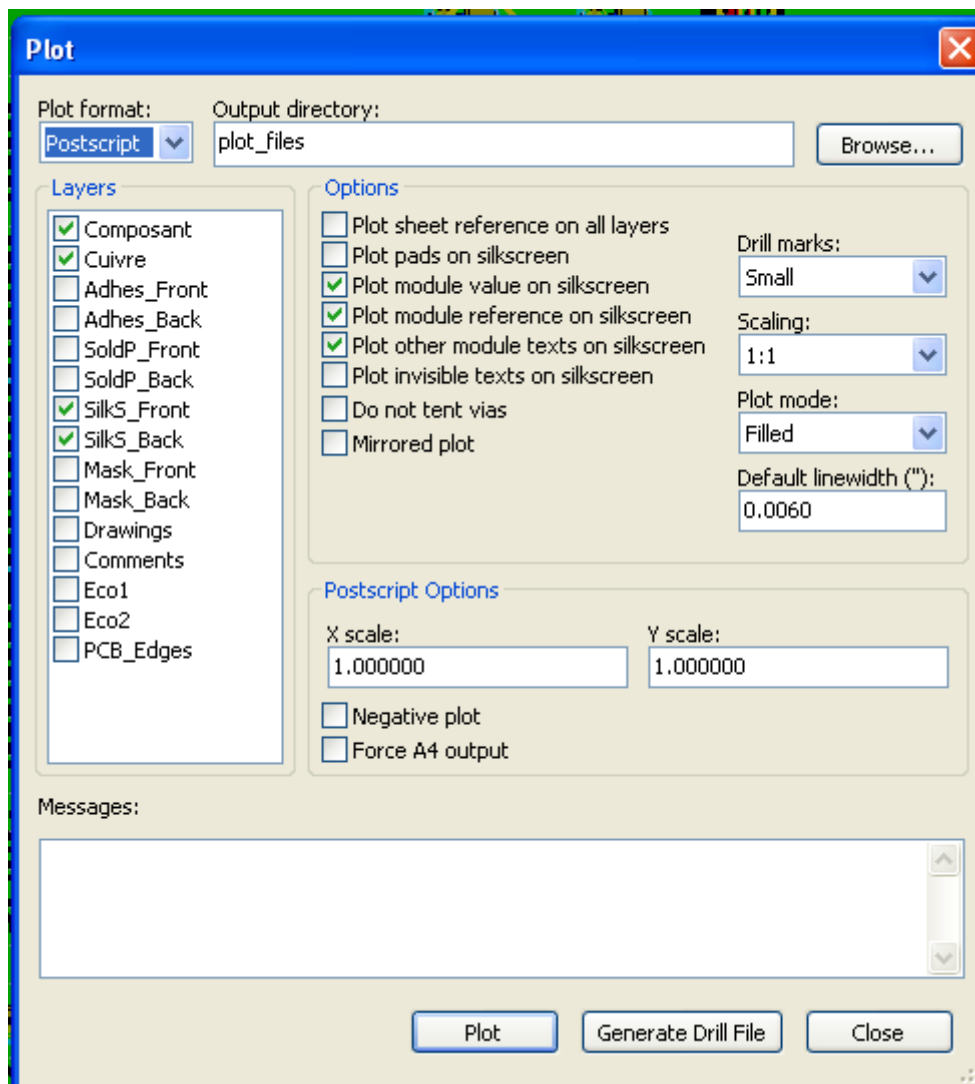
This is done via the **Files/Plot** menu option.



Usually, the files are in the GERBER format.

Nevertheless, it is possible to produce output in both HPGL and POSTSCRIPT formats.

When Postscript format is selected, the dialog is:



In these formats, a fine scale adjust can be used to compensate the plotter accuracy and to have a true scale 1 for the output:

X scale:	Y scale:
1.000000	1.000000

### 10.5.1 - GERBER format:

For each layer, Pcbnew generates a separate file following the **GERBER 274X** standard, by default in 3.4 format (each coordinate in the file is represented by 7 digits, of which 3 are before the decimal point and 4 follow it; the units are inches).

The tracing is always drawn to scale 1)

It is normally necessary to create files for all of the copper layers and, depending on the type of circuit, for the solder stop, solder mask, and silkscreen (component markings) layers. All of these files can be produced in one go, by selecting the appropriate check boxes.

For example, for a double-sided circuit with solder stop, silkscreen and solder mask (for CMS components), 8 files would be generated ('xxxx' represents the name of the .brd file), something like (real names depending on the default language, and the Pcbnew version):

- **xxxx.copper.pho** for the copper side.
- **xxxx.cmp.pho** for the component side.
- **xxxx.silkscmp.pho** for the component-side silkscreen markings.

## Pcbnew

- **xxxx.silkscu.pho** for the copper-side silkscreen markings.
- **xxxx.soldpcmp.pho** for the component-side solder mask.
- **xxxx.soldpcu.pho** for the copper-side solder mask.
- **xxxx.maskcmp.pho** for the component-side solder stop mask.
- **xxxx.maskcu.pho** for the copper-side solder stop mask.

### GERBER files format:

The format used by Pcbnew is:

RS274X

format 3.4, Imperial, Leading zero omitted, Abs format

This is very usual settings.

### 10.5.2 - HPGL Format:

The standard extension for the output files is **.plt**.

The tracing can be done at user-selected scales and can be mirrored.

The **Print Drill Opt** list offers the option of pads that are filled, drilled to the correct diameter or drilled with a small hole (to guide hand drilling).

If the **Print Sheet Ref** option is active, the sheet cartridge is traced.

### 10.5.3 - POSTSCRIPT Format:

The standard extension for the output files is **.ps** in the case of postscript output.

As for HPGL output, the tracing can be at user-selected scales and can be mirrored.

If the **Org = Centre** option is active, the origin for the coordinates of the tracing table is assumed to be in the centre of the drawing.

If the **Print Sheet Ref** option is active, the sheet cartridge is traced.

### 10.5.4 - Plot options:

<input type="checkbox"/> Use Proper Gerber Extensions <input type="checkbox"/> Exclude pcb edge layer <input type="checkbox"/> Subtract Mask from Silk <input type="checkbox"/> Print sheet reference <input checked="" type="checkbox"/> Print pads on silkscreen <input checked="" type="checkbox"/> Print module value <input checked="" type="checkbox"/> Print module reference <input checked="" type="checkbox"/> Print other module texts <input type="checkbox"/> Force print invisible texts	<input type="checkbox"/> Use Proper Gerber Extensions <input type="checkbox"/> Exclude pcb edge layer <input type="checkbox"/> Subtract Mask from Silk <input type="checkbox"/> Print sheet reference <input checked="" type="checkbox"/> Print pads on silkscreen <input checked="" type="checkbox"/> Print module value <input checked="" type="checkbox"/> Print module reference <input checked="" type="checkbox"/> Print other module texts <input type="checkbox"/> Force print invisible texts
Gerber format	other formats

GERBER format specific options:

Use Proper Gerber Extensions	use <b>.gbl .gtl...</b> instead of <b>.pho</b> for file name extensions.
Exclude Pcb Edge Layer	Do no draw items on Edge layer onto other layers
Subtract Mask from Silk	Remove all Silk from solder paste areas.

## 10.6 - Global clearance settings for the solder stop and the solder paste mask:

Masks clearances values can be set globally for the solder mask layers and the solder paste layers.

These clearances can be set:

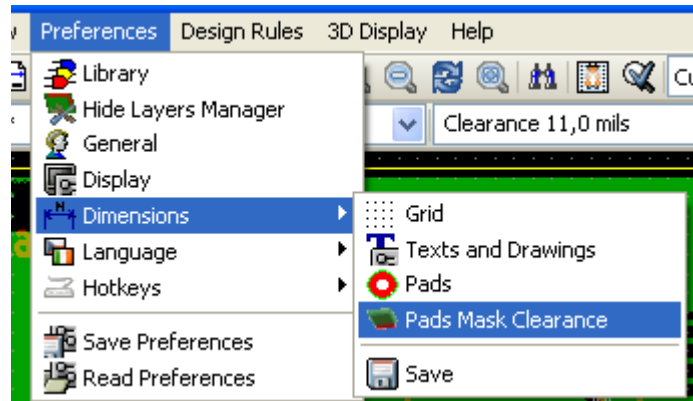
- At pads level
- At footprint level
- Or globally.

## Pcbnew

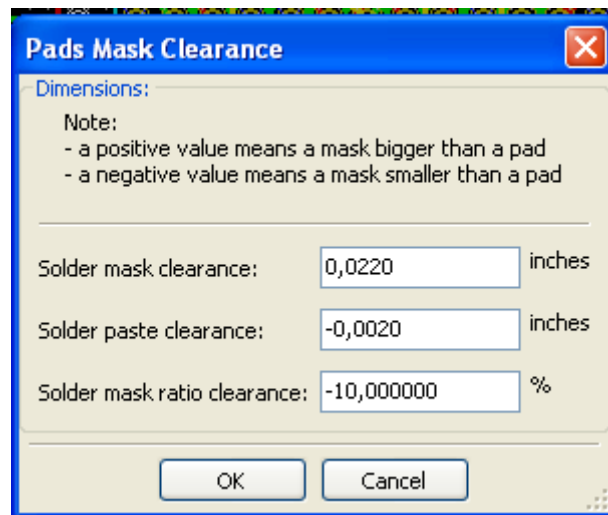
And Pcbnew uses by priority order:

- Pad values. *If null:*
  - Footprint values. *If null:*
  - Global values.

The menu option for this is accessed via **Preferences/Dimensions:**



And the dialog is:



### 10.6.1 - Solder mask clearance:

A value near to 10 mils is usually good.

This value is positive because the mask usually is bigger than the pad

### 10.6.2 - Solder paste clearance

The final clearance is the sum of the solder paste clearance and a percentage of the pad size.

This value is negative because the mask usually is smaller than the pad

## 10.7 - Generating the drill file(s)

The creation of a drill file xxxxxx.drl following the **EXCELLON** standard is always necessary.

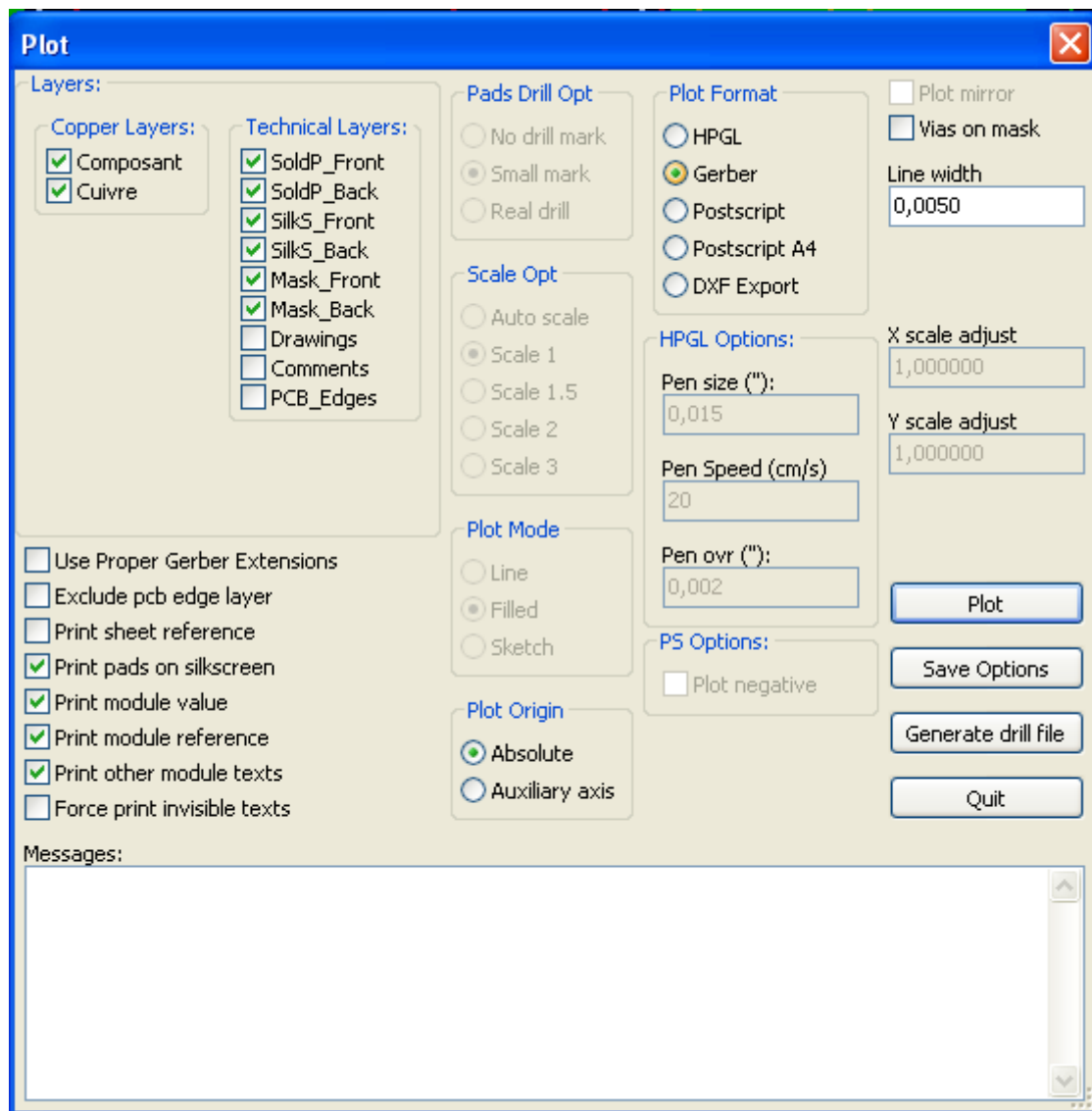
One can also produce an optional drill plan, which will be in HPGL (xxxxxx.plt) or POSTSCRIPT (xxxxxx.ps) format, and/or an optional drill report (a plain text file)

However, this is only occasionally useful, as an additional check.

The generation of these files is controlled via :

- the  button
- or the **Files/Fabrication Outputs/Drill file** menu selection.

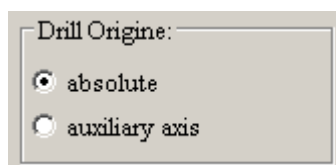
The Drill tools dialog box is:




For the HPGL tracing of the drill plan, it is possible to define the n°. and speed of the pen used.

#### **Coordinate origin:**

The dialog box is:



- **Absolute:** absolute coordinates are used
- **Auxiliary axis:** coordinates are relative to the auxiliary axis (use the tool  (right toolbar) to put it in good place.

### **10.8 - Generating the cabling documentation:**

To produce these files, the component and copper silkscreen layers can be traced. Usually, just the component-side silkscreen markings are sufficient for cabling a PCB. If the copper-side silkscreen is used, the text it contains should be mirrored in order to be readable.

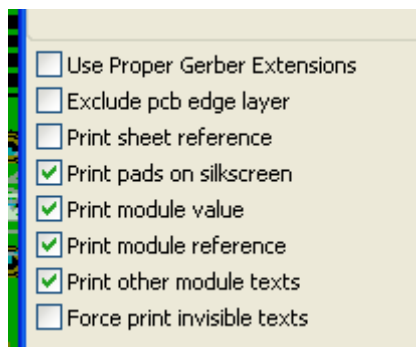
### **10.9 - Generation of file(s) for automatic component insertion:**

This option is accessed via the **Postprocess/Create Cmp file** menu option. However, no file will be generated unless at least one module has the **Normal+Insert** attribute activated (see Editing Modules). One or two files will be produced, depending upon whether insertable components are present

on one or both sides of the PCB. A dialogue box will display the names of the file(s) created.

### 10.10 - Advanced tracing options:

The options described below (part of the **Files/Plot** dialogue) allow for fine-grained control of the tracing process. They are particularly useful when printing the silkscreen markings for cabling documentation.



The options are:

<b>Use Proper Gerber Extensions</b>	GERBER format specific. When creating files, use specific extensions for each file. If disabled the Gerber file extension is <b>.pho</b>
<b>Exclude pcb edge layer</b>	GERBER format specific. Do not plot graphic items on edge layer.
<b><i>Print Sheet Ref</i></b>	Trace sheet outline and the cartridge.
<b><i>Print Pads on Silkscreen</i></b>	Enables/disables printing of pad outlines on the silkscreen layers (if the pads have already been declared to appear on these layers). In fact useful for preventing any pads from being printed, in the disabled mode.
<b><i>Print Module Value</i></b>	Enables printing of VALUE text on the silkscreen.
<b><i>Print Module Reference</i></b>	Enables printing of the REFERENCE text on the silkscreen.
<b><i>Print other module texts</i></b>	Enables the printing of other text fields on the silkscreen.
<b><i>Force Print Invisible Texts</i></b>	Forces printing of fields (reference, value) declared as invisible. In combination with <b><i>Print Module Reference</i></b> and <b><i>Print Module Value</i></b> , this option enables production of documents for guiding cabling and repair. These options have proven necessary for circuits using components that are too small (CMS) to allow readable placement of two separate text fields.

## 11 - ModEdit: Managing LIBRARIES

### Headings:

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### 11.1 - Overview of ModEdit

PCBNEW simultaneously maintains several libraries. Thus, when a module is loaded, all of the libraries that appear in the library list are searched until the first instance of the module is found. In what follows, note that the 'active library' is the library selected within the Module Editor (**ModEdit**), the program we shall now describe.

**ModEdit** enables the **creation and editing** of modules:

- Adding and removing pads;
- Changing pad properties (shape, layer) for individual pads or globally for all pads of a module;
- Editing graphic elements (lines, text);
- Editing information fields (value, reference, ...);
- Editing the associated documentation (description, keywords).;

as well as the **maintenance of the active library**:

- Listing the modules in the active library;
- Deletion of a module from the active library;
- Saving a module to the active library;
- Saving all of the modules contained by a printed circuit.

It is also possible to create new libraries.


A library is in fact made up of two files:

- The library itself (file extension **.lib**)
- The associated documentation (file extension **.dcm**)

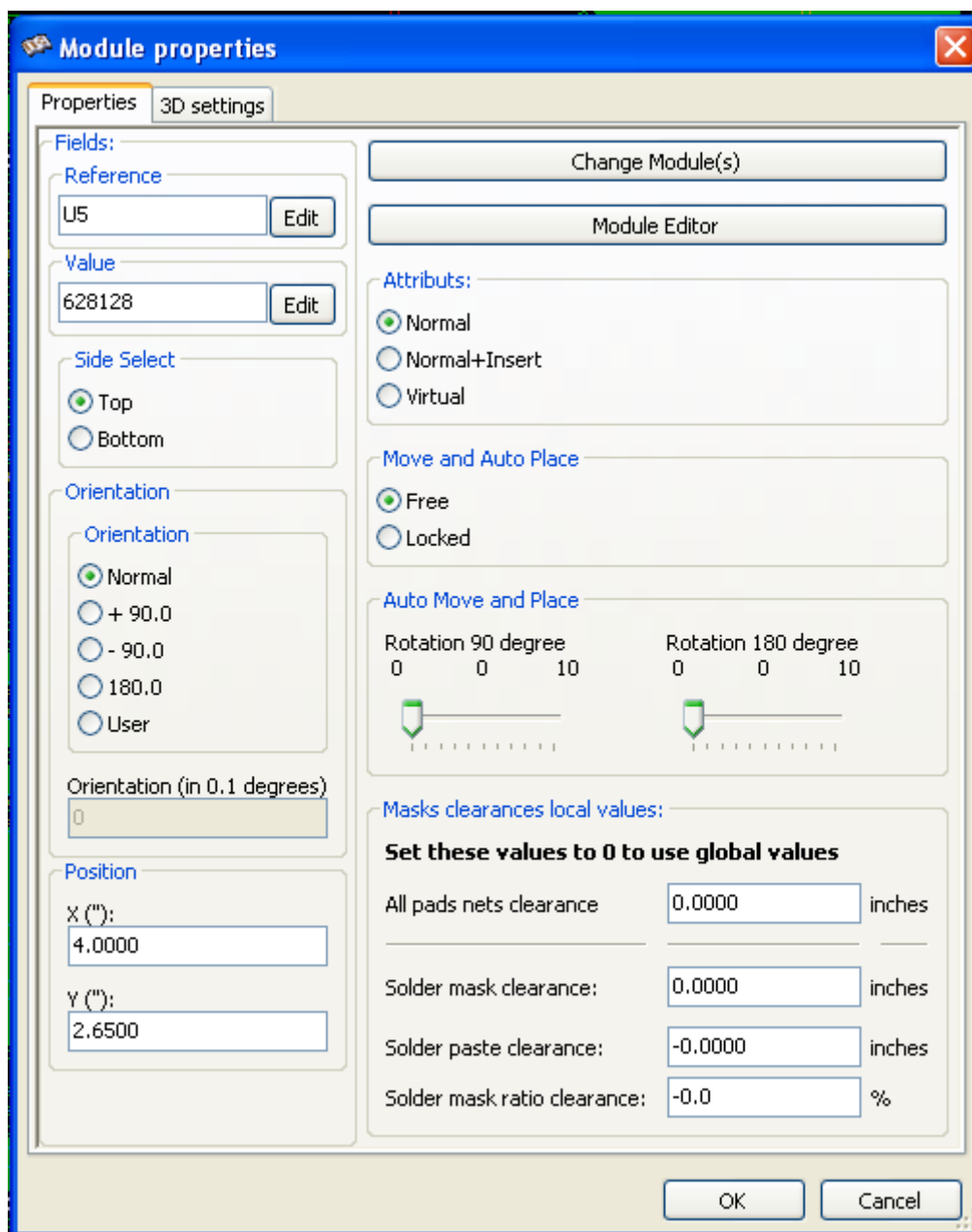
The documentation file is systematically regenerated after each modification of the corresponding .lib file; in this way it can easily be recovered if lost. The documentation file serves to accelerate access to module documentation.

### 11.2 - ModEdit:

The Module Editor can be accessed in two ways:

- Directly, via the icon  in the main toolbar of Pcbnew;
- In the edit dialog for the active module (see figure below; accessed via the context menu), there is a button (**Module Editor**).

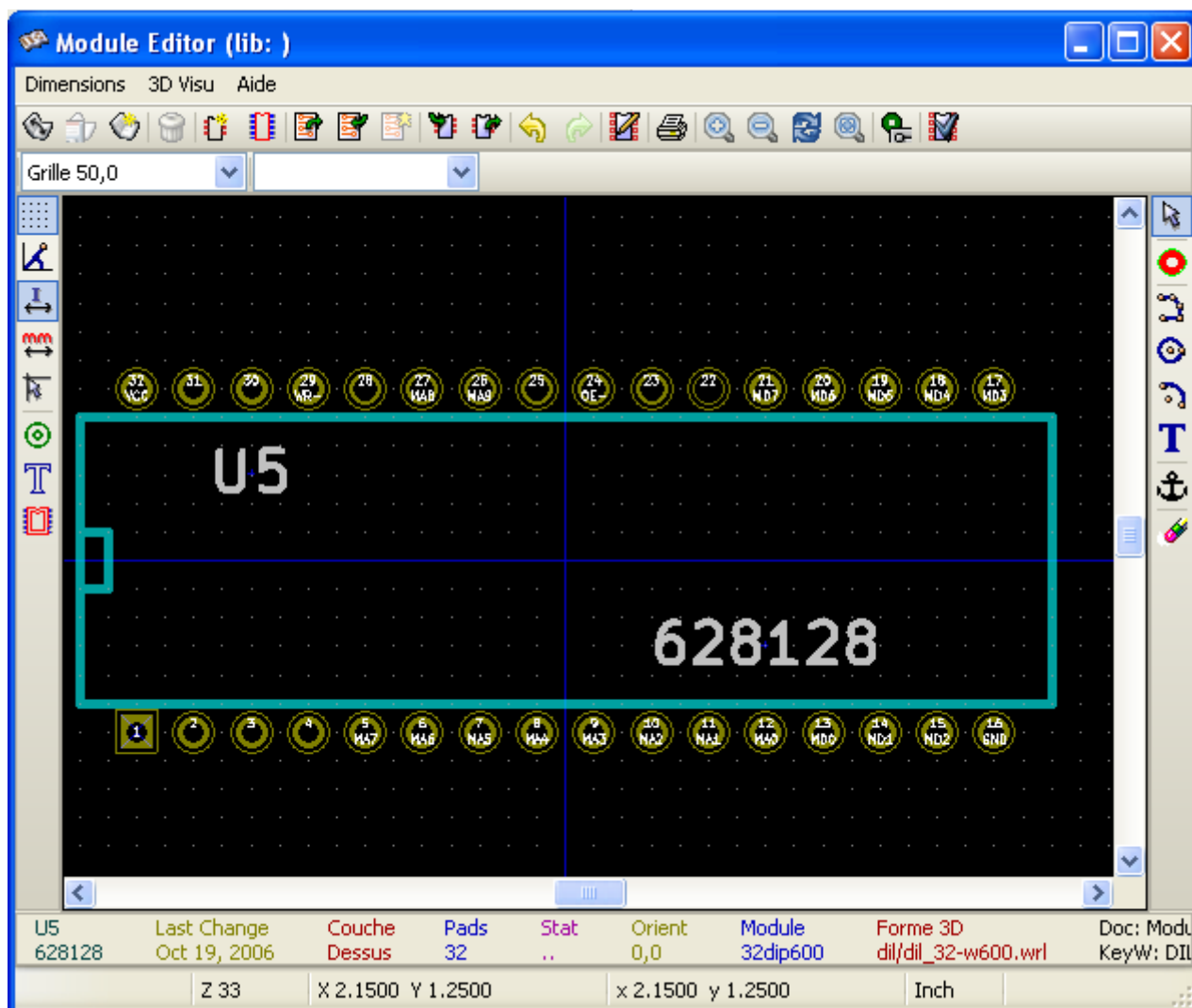




In this case, the active module of the board will be loaded automatically in ModEdit, enabling immediate editing (or archiving).

### 11.3 - ModEdit user interface:




















Calling ModEdit causes the following window to appear:



#### 11.4 - Main toolbar in Modedit:



The following functions are available:

	Select the active library.
	Save the current module to the active library.
	Create a new library and save the current module in it.
	Access a dialog for deleting a module from the active library.
	Create a new module.
	Load a module from the active library.
	Load (import) a module from the printed circuit board.
	Export the current module to the printed circuit board. If the module was previously imported from the current board, it will replace the corresponding module on the board (i.e., respecting position and orientation). If the module was loaded from a library, it will be copied on to the printed circuit board at position 0.
	Import a module from a file created by the Export command (  )
	Export a module. This command is essentially identical to that for creating a library, the only difference being that  creates a library in the user directory, while  creates a library in the standard library directory (usually <i>kiCad/modules</i> ).
	Undo - Redo
	Not used.
	Call the print dialog.
	Standard zoom commands.
	Call the pad editor.
	Not used.

### 11.5 - Creating a new module:






Allows creation of a new module. You will be asked for the name by which the module will be identified in the library. This text also serves as the module reference, which will be replaced by the final reference on the printed circuit board (U1, IC3...)

It will be necessary to add the following to the new module:

- Outlines (and possibly text);
- The pads;
- A value (place-holding text that will subsequently be replaced by the true value).

When a new module is similar to an existing module in a library or board, an alternative and often quicker method is the following:

1. Load the similar module (, , ou )
2. **Modify the reference field** to the name of the new module.
3. Edit and save the new module.

### 11.6 - Creating a new library:

The creation of a new library is done using:



, in which case the file is created by default in the library directory;  
or by




, in which case the file is created by default in the working directory.

A file-choosing dialog allows the name of the library to be specified and its directory to be changed. In both cases, the library will contain the module being edited.

**Warning:**

If an old library of the same name exists, it will be overwritten without warning.

### 11.7 - Saving a module to the active library:

The action of saving a module (thereby modifying the file of the active library) is performed using this icon .


If a module of the same name already exists, it will be replaced.


Since you will depend upon the accuracy of the library modules, it is worth double-checking the module before saving.


It is also recommended to edit either the reference or value field text to the name of the module as identified in the library.



### 11.8 - Transferring a module from one library to another:

Select the source library(.

Load the module (.

Select the destination library (.

Save the module (.

You may also wish to delete the source module: reselect the source library then delete the old module ( then ).

### 11.9 - Saving all the modules of a circuit in the active library:

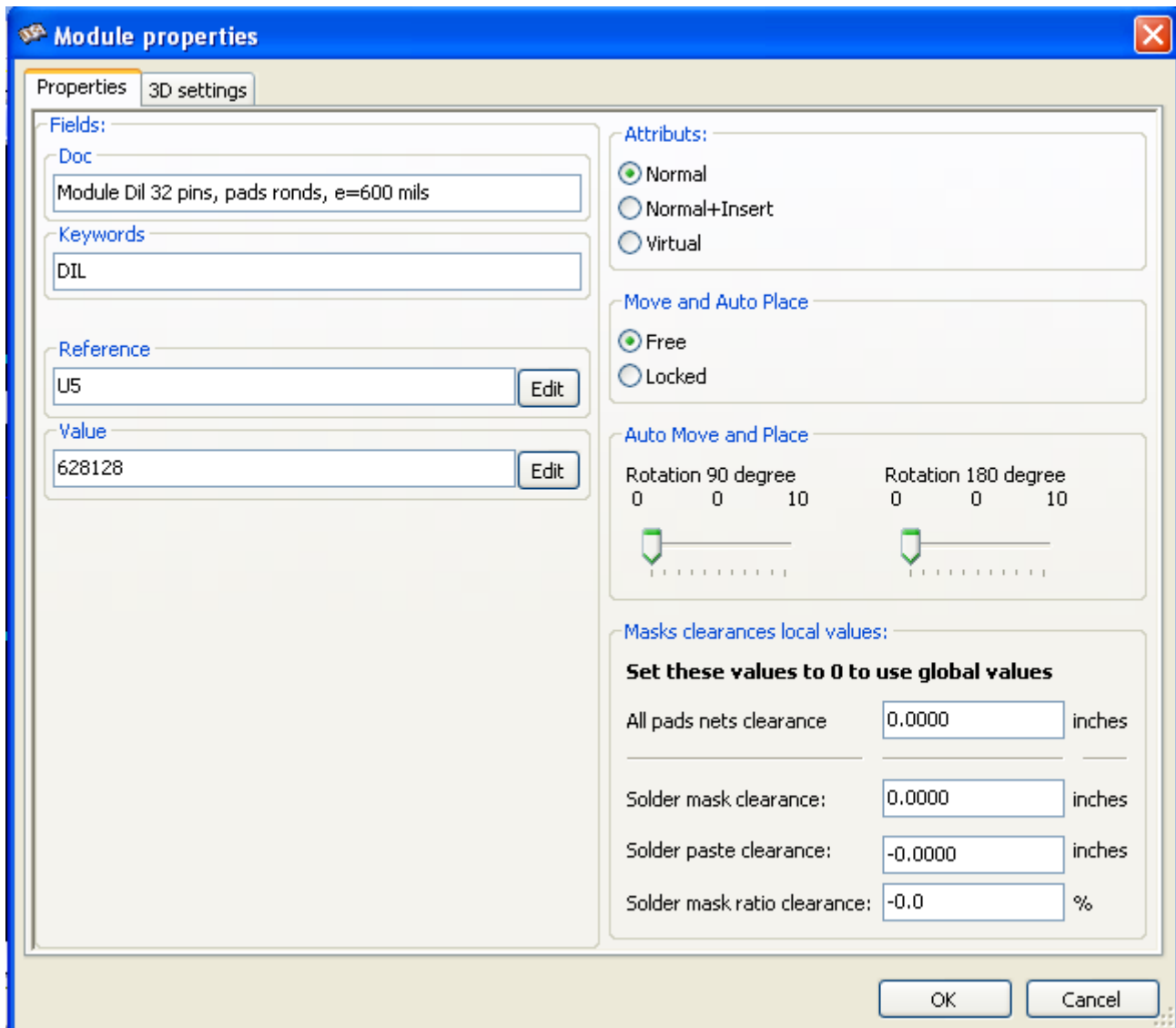
It is possible to copy all of the modules of a given board design to the active library. These modules will keep their current library names.

This command has two uses :

- To create an archive or complete a library with the modules from a circuit, in the event of the loss of a library;
- More importantly, it facilitates library maintenance by enabling the production of documentation for the library, as explained below.

### 11.10 - Documentation for library modules:

It is strongly recommended to document the modules you create, in order to enable rapid and error-free searching. For example, who is able to remember all of the multiple pinout variants of a TO92 package? The **Module Properties** dialog offers a simple solution to this problem.




This dialog accepts:

- A one-line comment/description;
- Multiple keywords.

The description is displayed with the component list in CVPCB and, in PCBNEW, it is used in the module selection dialogs.

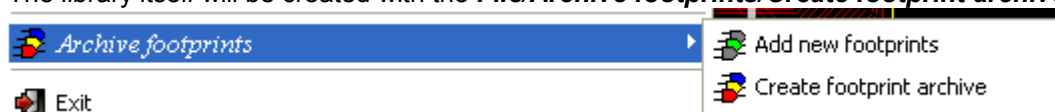
The keywords enable searches to be restricted to those modules corresponding to particular keywords.

When directly loading a module (the icon  of the right-hand Pcbnew toolbar), keywords may be entered in the dialog box. Thus, entering the text "=CONN" will cause the display of the list of modules whose keyword lists contain the word CONN.

### 11.11 - Documenting libraries – recommended practice:

It is recommended to **create libraries indirectly**, by **creating one or more auxiliary circuit boards** that constitute the **'source'** of (part of) the library, as follows:

- Create a circuit board in A4 format, in order to be able to print easily to scale (scale = 1).
- Create the modules that the library will contain on this circuit board.
- The library itself will be created with the **File/Archive footprints/Create footprint archive** command.



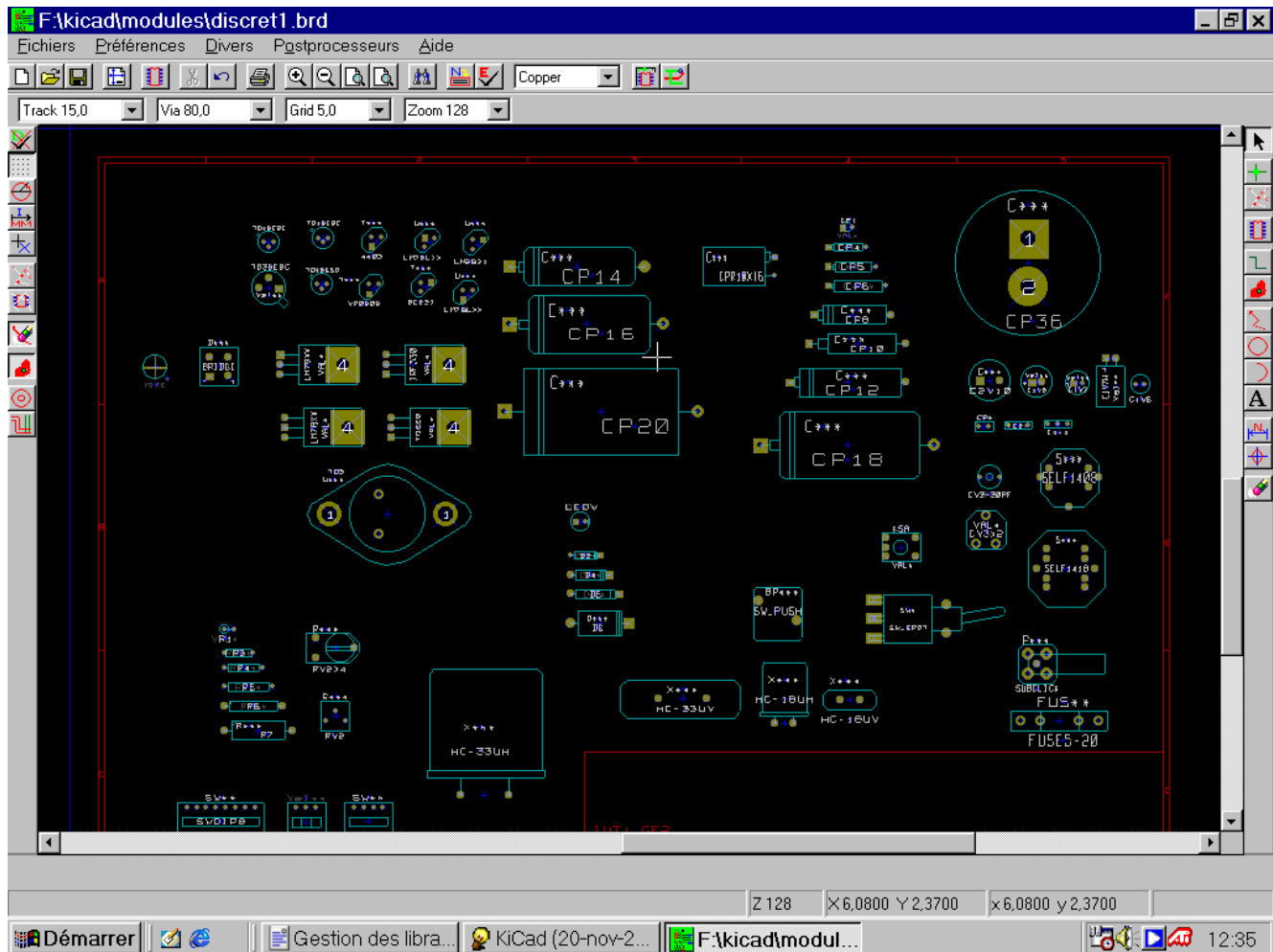
The "true source" of the library will thus be the auxiliary circuit board, and it is on this circuit that any subsequent alterations of modules will be made.

## Pcbnew

Naturally, several circuit boards can be saved in the same library.

It is generally a good idea to make different libraries for different kinds of components (connectors, discretes,...), since Pcbnew is able to search many libraries when loading modules.

Here is an example of such a library source:



This technique has several advantages:

1. The circuit can be printed to scale and serve as documentation for the library with no further effort.
2. Future changes of Pcbnew may require regeneration of the libraries, something that can be done very quickly if circuit-board sources of this type have been used. This is important, because the circuit board file formats are guaranteed to remain compatible during future development, but this is not the case for the library file format.

## 12 - ModEdit: Creating/editing modules

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### 12.1 - Overview.

**ModEdit** is used for **editing and creating modules**; this includes:

- Adding and removing pads.
- Changing pad properties (shape, layer), for individual pads or for all pads in a module.
- Adding and editing graphic elements (contours, text).
- Editing fields (value, reference,...)
- Editing the associated documentation (description, keywords).

### 12.2 - Module elements.

A module is the physical representation of the part to be inserted, but it must also link to the schematic. Each module comprises three different elements:

- The pads.
- Graphical contours and text.
- Fields.

In addition, a number of other parameters must be correctly defined if the autoplacement function is to be used. The same holds for the generation of auto-insertion files.

#### 12.2.1 - Pads.

Two pad properties are important:

## Pcbnew

- Geometry (shape, layers, drill holes).
- The pad 'number', which is constituted by **up to four alphanumeric characters**. Thus, the following are all valid pad numbers: 1, 45 and 9999, but also AA56 and ANOD. The pad number must be identical to that of the corresponding pin number in the schematic, because it is by matching pin and pad numbers that Pcbnew links pins and pads for the module.

### 12.2.2 - Contours.


Graphical contours are used to draw the physical shape of the module. Several different types of contour are available: lines, circles, arcs, and text. Contours have no electrical significance – they are simply graphical aids.

### 12.2.3 - Fields.

These are text elements associated with a module. Two are obligatory and always present: the **reference** and the **value** fields. These are automatically read and updated by Pcbnew when a netlist is read during the loading of modules into a board. The reference is replaced by the appropriate schematic reference (U1, IC3,...). The value is replaced by the value of the corresponding part in the schematic (47K, 74LS02,...). Other fields can be added; these will behave like graphical text.

## 12.3 - Starting ModEdit and selecting a module to edit.

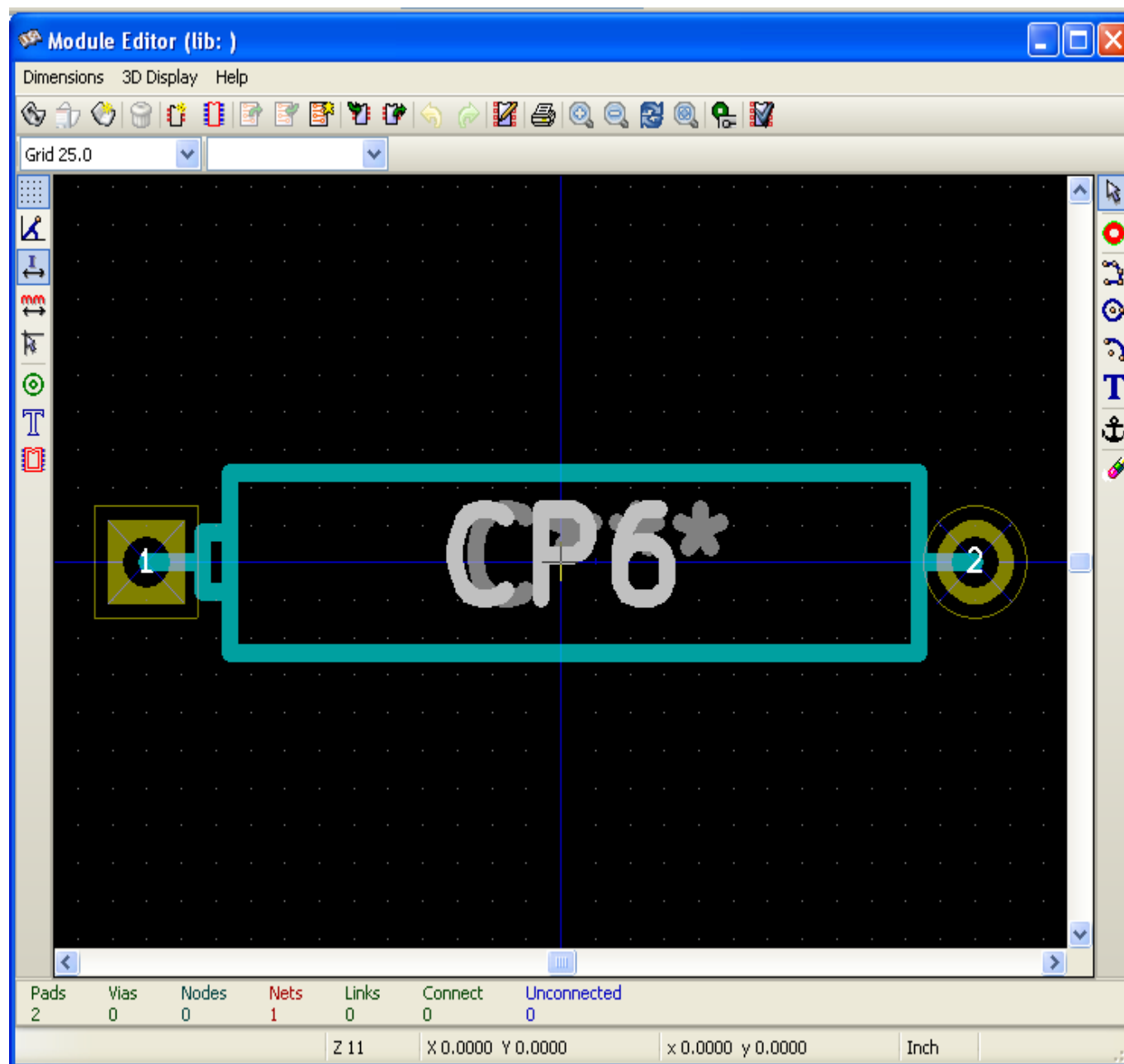
ModEdit can be started in two ways:

- Directly via the  icon of the main toolbar of Pcbnew. This allows creation or modification of a module in the library.
- Double-clicking a module will launch the 'Module Properties' menu, which offers a 'Goto Module Editor' button. If this option is used, the module from the board will be loaded into the editor, for modification (or saving).

## 12.4 - Module Editor Toolbars:

Calling ModEdit will launch a window like this:














### 12.4.1 - Righthand Toolbar - editing

	<p>This toolbar contains tools for:</p> <ul style="list-style-type: none"> <li>● Placing pads.</li> <li>● Adding graphic elements (contours, text).</li> <li>● Positioning the anchor.</li> <li>● Deleting elements.</li> </ul>
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The specific functions are the following:

	Add a pad.
	Draw line segments and polygons.
	Draw circles.
	Draw circular arcs.
	Add graphical text (fields are <b>not</b> managed by this tool).
	Position the module anchor.
	Delete elements.
	Grid origin. (grid offset). Useful for placement of pads. The grid origin can be put on a given location (the first pad to place), and after the grid size can be set to the pad pitch. Placing pads is therefore very easy

### 12.4.2 - Left hand Toolbar – display options

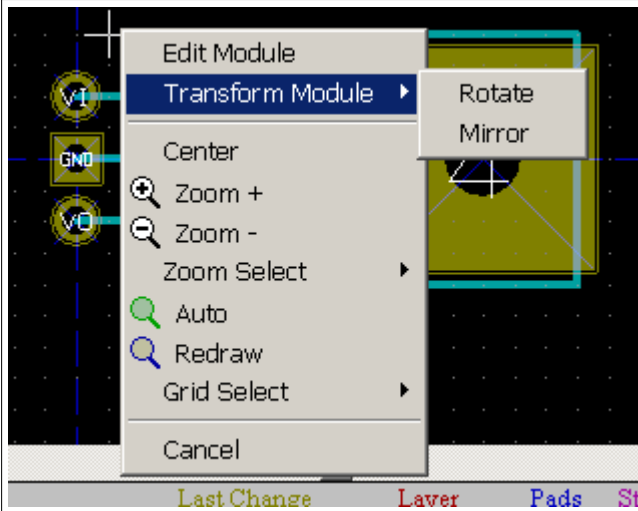
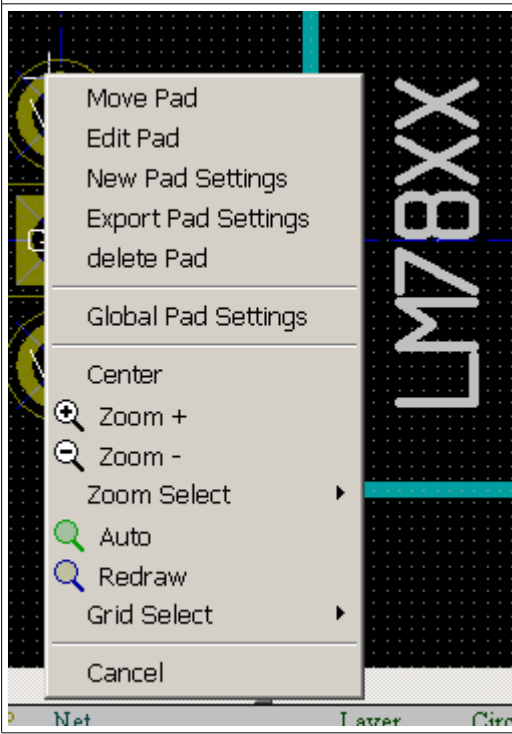
	These tools manage the display options in ModEdit
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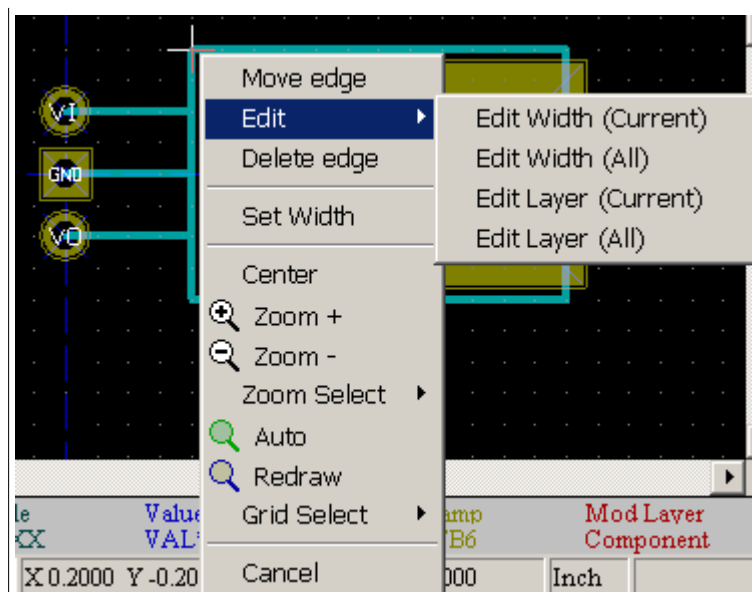
These options are active when the button is pressed:

	Display the grid.
	Display polar coordinates.
	Use units of mm (update: now mm/inches are toggled via two buttons).
	Crosshair cursor.
	Display pad in outline mode.
	Display text in outline mode.
	Display contours in outline mode.

12.5 - Context Menus.

The right mouse button calls up menus that depend upon the element beneath the cursor:

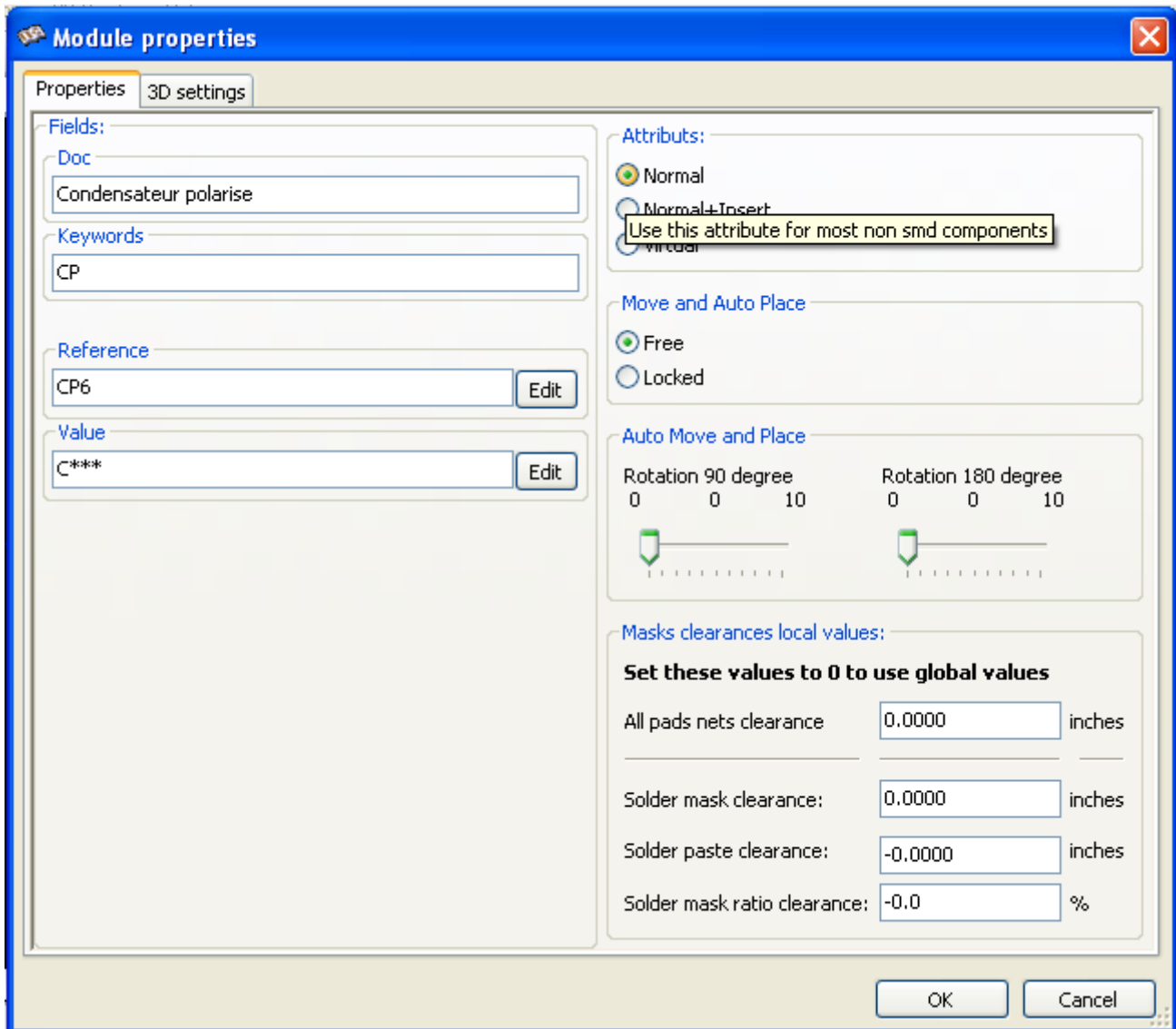
	The context menu for editing module parameters.
	The context menu for editing pads.



The context menu for editing graphic elements.

## 12.6 - The Module Properties dialog.

This dialog can be launched when the cursor is over a module by clicking on the right mouse button and then selecting 'Edit Module'.



The dialog can be used to define the main module parameters.

## 12.7 - Creating a new module.



This icon is used to create a new module.

The name of the new module will be requested (this will be the name by which the module will be identified in the library).




This text also serves as the module reference, which is ultimately replaced by the real reference (U1, IC3...).

The new module will require:

- Contours (and possibly graphic text).
- Pads.
- A value (hidden text that is replaced by the true value when used).

### Alternative method:

When a new module is similar to an existing module in a library or a circuit board, an alternative and quicker method of creating the new module is as follows:


1. Load the similar module ( , , or  )
2. **Modify the reference field** in order to generate a new identifier (name).
3. Edit and save the new module.

## 12.8 - Adding and editing pads.

Once a module has been created, pads can be added, deleted or modified.


Modification of pads can be local, affecting only the pad under the cursor, or global, affecting all pads of the module.

### 12.8.1 - Adding a pad.

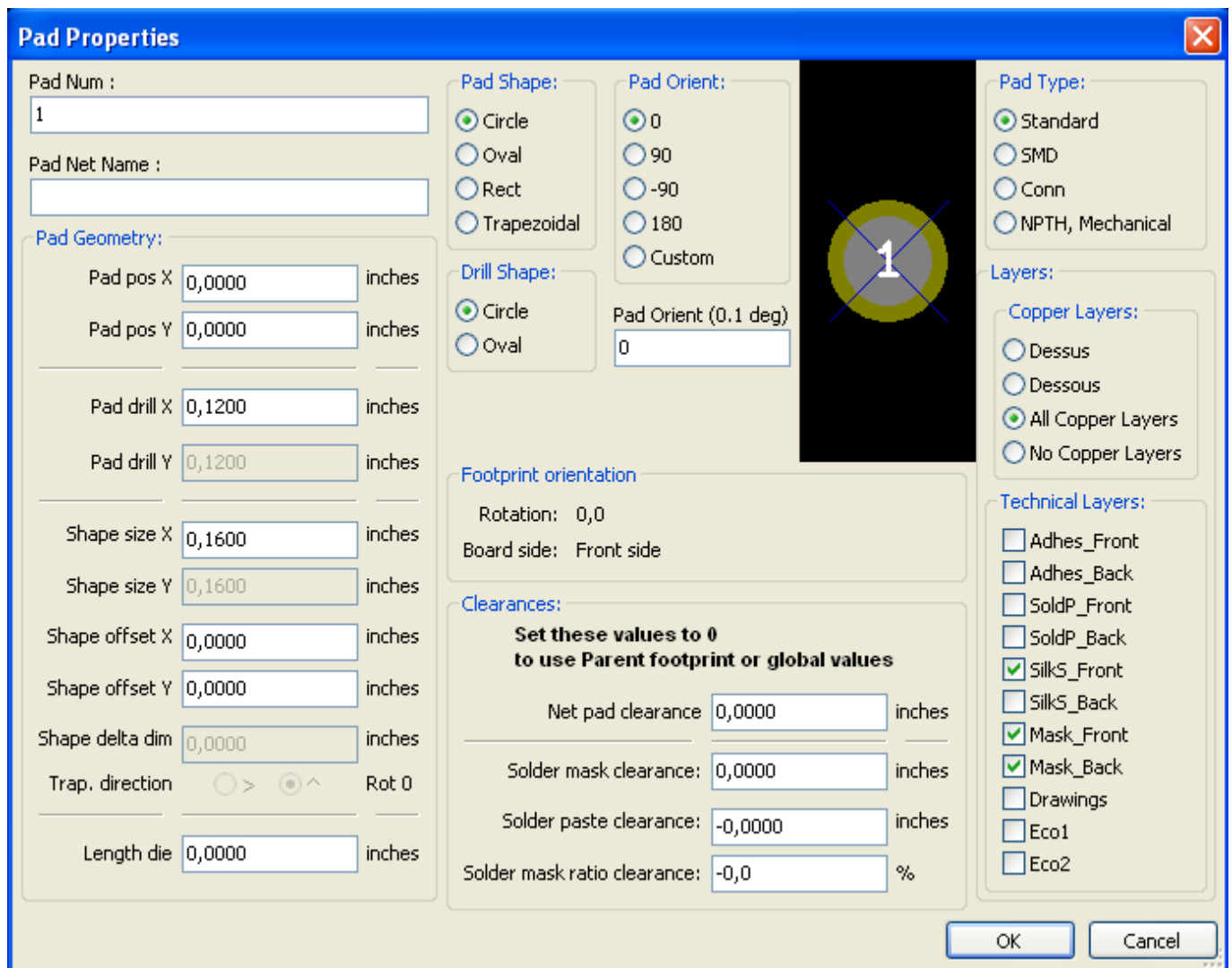
Select the  tool in the right-hand toolbar.  
Pads can be added by clicking in the desired position with the left mouse button.  
Their properties are predefined in the *Pad properties* menu.  
Don't forget to enter the **pad number**.

### 12.8.2 - Setting pad properties.

This can be done in three ways.

1. Selecting the  tool from the horizontal toolbar.
2. Clicking on an existing pad and selecting '**Edit Pad**'. The pad's settings can then be edited.
3. Clicking on an existing pad and selecting '**Export Pad Settings**'. In this case, the geometrical properties of the selected pad will become the default pad properties.

In the first two cases, the following dialog window will be displayed:



The **Pad Properties** dialog window is shown with the following settings:

- Pad Num :** 1
- Pad Net Name :** (empty)
- Pad Geometry:**
  - Pad pos X: 0,0000 inches
  - Pad pos Y: 0,0000 inches
  - Pad drill X: 0,1200 inches
  - Pad drill Y: 0,1200 inches
  - Shape size X: 0,1600 inches
  - Shape size Y: 0,1600 inches
  - Shape offset X: 0,0000 inches
  - Shape offset Y: 0,0000 inches
  - Shape delta dim: 0,0000 inches
  - Trap. direction: ☐ > ☒ ^ Rot 0
  - Length die: 0,0000 inches
- Pad Shape:**
  - ☒ Circle
  - ☐ Oval
  - ☐ Rect
  - ☐ Trapezoidal
- Drill Shape:**
  - ☒ Circle
  - ☐ Oval
- Pad Orient:**
  - ☒ 0
  - ☐ 90
  - ☐ -90
  - ☐ 180
  - ☐ Custom
- Pad Orient (0.1 deg):** 0
- Footprint orientation:**
  - Rotation: 0,0
  - Board side: Front side
- Clearances:**

**Set these values to 0 to use Parent footprint or global values**

  - Net pad clearance: 0,0000 inches
  - Solder mask clearance: 0,0000 inches
  - Solder paste clearance: -0,0000 inches
  - Solder mask ratio clearance: -0,0 %
- Pad Type:**
  - ☒ Standard
  - ☐ SMD
  - ☐ Conn
  - ☐ NPTH, Mechanical
- Layers:**
  - Copper Layers:**
    - ☐ Dessus
    - ☐ Dessous
    - ☒ All Copper Layers
    - ☐ No Copper Layers
  - Technical Layers:**
    - ☐ Adhes\_Front
    - ☐ Adhes\_Back
    - ☐ SoldP\_Front
    - ☐ SoldP\_Back
    - ☒ Silks\_Front
    - ☐ Silks\_Back
    - ☒ Mask\_Front
    - ☒ Mask\_Back
    - ☐ Drawings
    - ☐ Eco1
    - ☐ Eco2

Buttons: OK, Cancel

Care should be taken to define correctly the layers to which the pad will belong.  
In particular, although the copper layers are easy to define, the management of non-copper layers (solder mask, solder pads...) is equally important for circuit manufacture and documentation.  
The **Pad Type** selector triggers an automatic selection of layers that is generally sufficient.

#### 12.8.2.1 - Note 1:

For smd modules of the VQFP/PQFP type, which have rectangular pads on all four sides, i.e., both horizontal and vertical, it is recommended to use just one shape (for example, a horizontal rectangle) and to place it with different orientations (0 for horizontal and 90 degrees for vertical). Global resizing of pads can then be done in a single operation.

### 12.8.2.2 - Note 2:

Rotations of -90 or -180 are only required for trapezoidal pads used in microwave modules.

### 12.8.2.3 - Note 3 : Not Plated Through Hole pads:

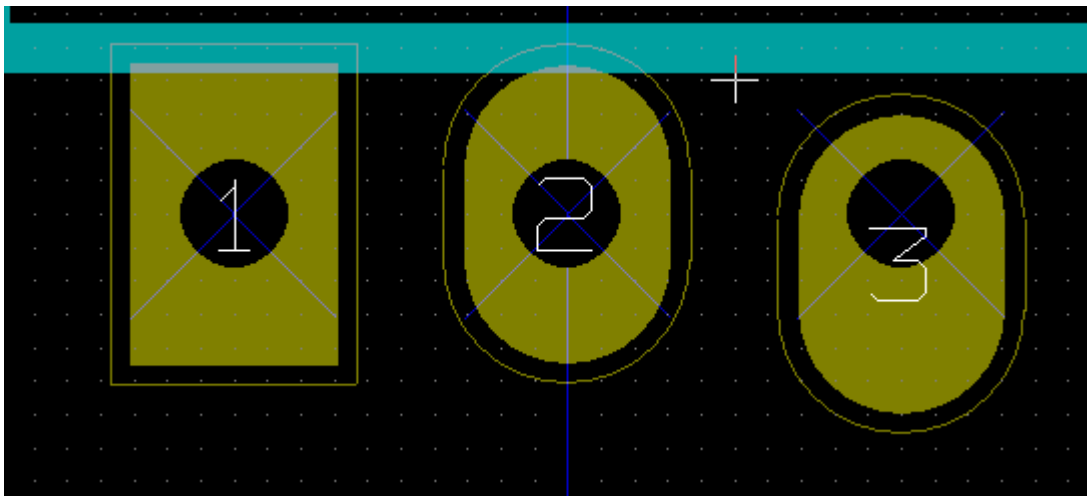
- Pads can be defined as **Not Plated Through Hole** pads (NPTH pads).
- These pads must be defined on one or all copper layers (obviously, the hole exists on all copper layers).
- This requirement allows you to define specific clearance parameters ( for instance clearance for a screw).
- **When the pad hole size is the same as the pad size, for a round or oval pad, this pad is NOT plotted on copper layers in GERBER files.**
- These pads are used for mechanical purposes, therefore no pad name or net name is allowed. A connection to a net is not possible.

### 12.8.2.4 - Note 4: Pads not on copper layers:

They are unusual pads.

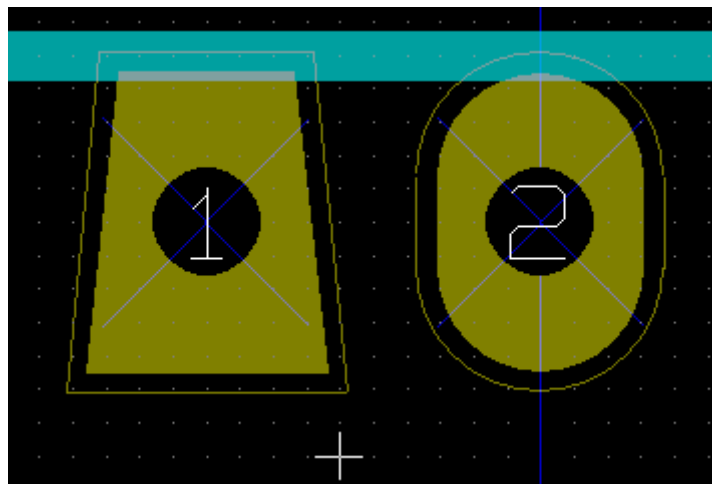
This option can be used to create fiducials or masks on technical layers.

### 12.8.2.5 - Offset Parameter:



Pad 3 has an offset Y = 15 mils.

### 12.8.2.6 - Delta Parameter (trapezoidal pads):



Pad 1 has its parameter Delta X = 10 mils

## 12.8.3 - Setting clearance for pads solder mask and solder paste mask layers:

Setting clearance can be made at 3 levels:

- ➡ Global level.
- ➡ Footprint level.

## Pcbnew

- ➡ Pad level.

Pcbnew uses to calculate clearance:

- ➡ Pad settings.  
If null:
- ➡ Footprint settings.  
If null:
- ➡ Global settings.

### 12.8.3.1 - Remarks:

The solder mask pad shape is usually bigger than the pad itself.

So the clearance value is positive.

The solder paste mask pad shape is usually smaller than the pad itself.

So the clearance value is negative.

### 12.8.3.2 - Solder paste mask parameters:

There are 2 parameters:

A fixed value.

A percentage of the pad size.

The real value is the sum of these 2 values.

### 12.8.3.3 - Footprint level settings:

Masks clearances local values:

**Set these values to 0 to use global values**

All pads nets clearance	<input type="text" value="0.0000"/>	inches
Solder mask clearance:	<input type="text" value="0.0000"/>	inches
Solder paste clearance:	<input type="text" value="-0.0000"/>	inches
Solder mask ratio clearance:	<input type="text" value="-0.0"/>	%

### 12.8.3.4 - Pad level settings:

Clearances:

**Set these values to 0 to use Parent footprint or global values**

Net pad clearance	<input type="text" value="0.0000"/>	inches
Solder mask clearance:	<input type="text" value="0.0000"/>	inches
Solder paste clearance:	<input type="text" value="-0.0000"/>	inches
Solder mask ratio clearance:	<input type="text" value="-0.0"/>	%

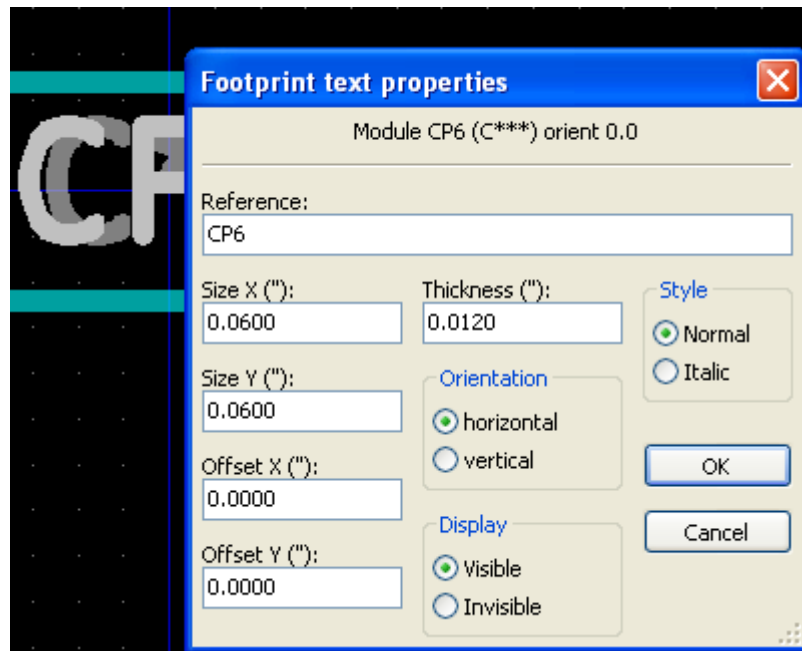
## 12.9 - Fields Properties.

There are at least 2 fields: reference and value.

Their parameters (attribute, size, width) must be updated.

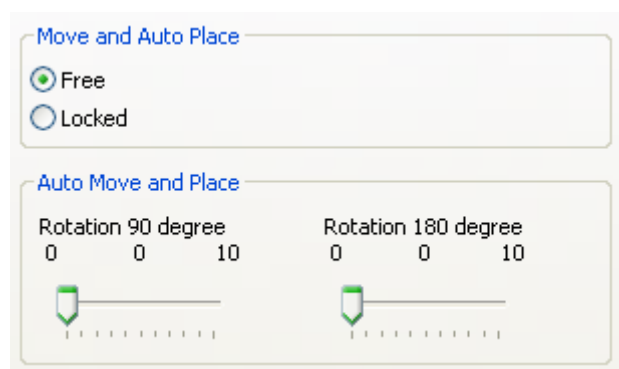
Access to the dialog box by the Pop-up menu, by double clicking on the champ, or by the Footprint properties dialog box.





### 12.10 - Information about automatic placement for a module.

If the user wishes to exploit the the full capabilities of the auto-placement functions, it is necessary to define the allowed orientations of the module (**Module Properties** dialog).



Usually, rotation of 180 degrees is permitted for resistors, non-polarized capacitors and other symmetrical elements. Some modules (small transistors, for example) are often permitted to rotate by +/- 90 or 180 degrees.

By default, a new module will have its rotation permissions set to zero.

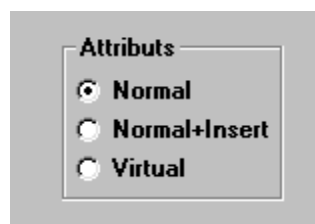
This can be adjusted according to the following rule:

A value of 0 makes rotation impossible, 10 allows it completely, and any intermediate value represents a penalty for rotation.

For example, a resistor might have a permission of 10 to rotate 180 degrees (unrestrained) and a permission of 5 for a +/- 90 degree rotation (allowed, but discouraged).

### 12.11 - Attributes.

The attributes section is the following:



- **Normal** is the standard attribute.
- **Normal+Insert** indicates that the module must appear in the automatic insertion file (for automatic insertion machines).  
This attribute is most useful for surface mount components (SMDs).

## Pcbnew

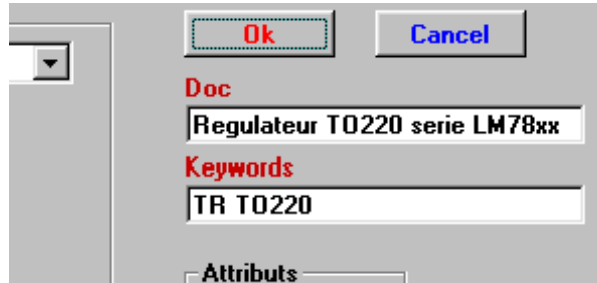
- **Virtual** indicates that a component is directly formed by the circuit board. Examples would be edge connectors or inductors created by a particular track shape (as sometimes seen in microwave modules).

### 12.12 - Documenting modules in a library.

It is strongly recommended to document newly created modules, in order to facilitate their rapid and accurate retrieval.

Who is able to recall the multiple pin-out variants of a TO92 module?


The **Module Properties** dialog offers a simple solution to this problem.



It allows:

- The entry of a comment line (description).
- Multiple keywords.

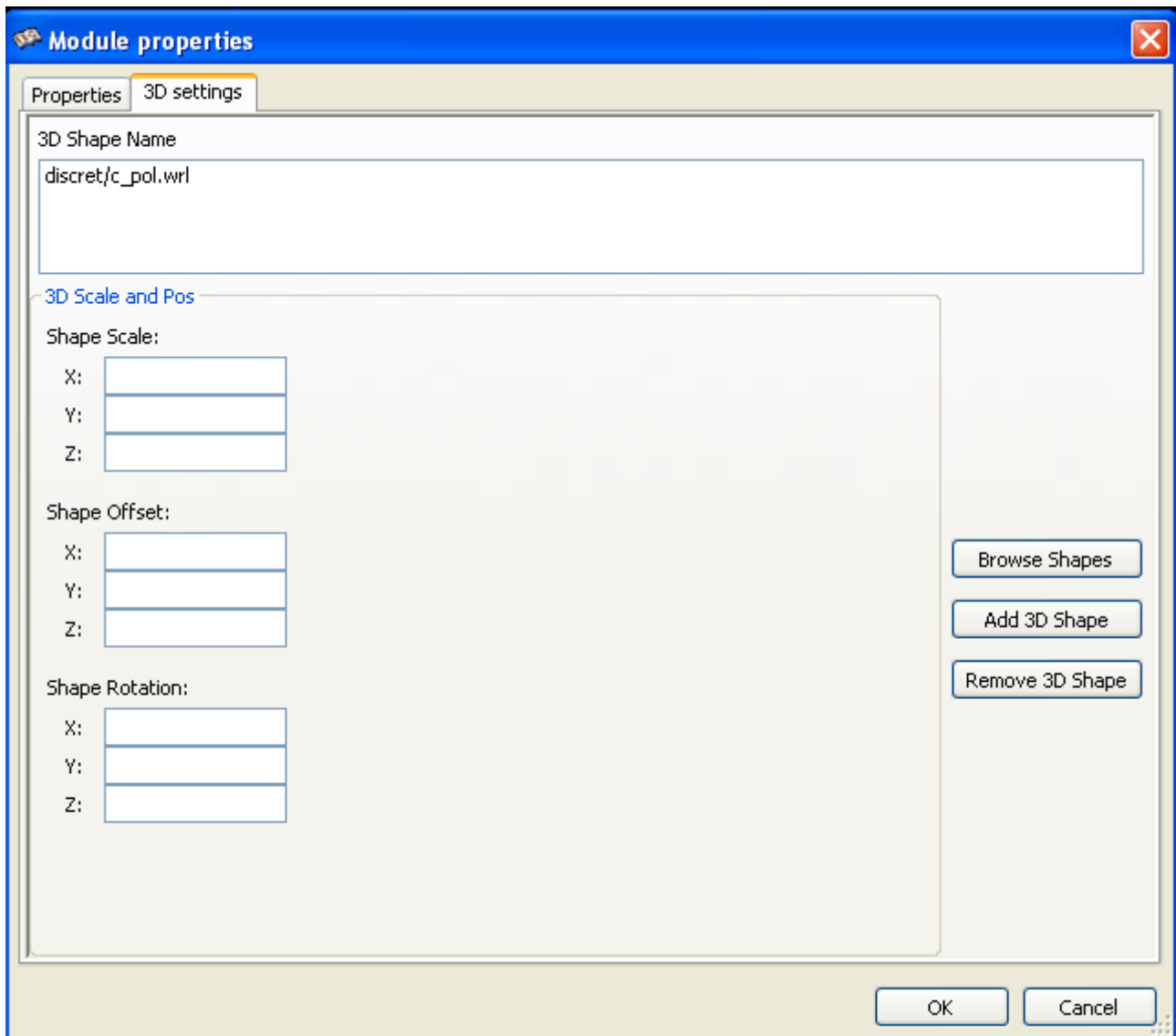
The comment line is displayed with the component list in CVPCB and in the module selection menus in PCBNEW. The keywords can be used to restrict searches to those parts possessing the given keywords.

Thus, while using the load module command (  in the right-hand toolbar in Pcbnew), it is possible to type the text "=TO220" into the dialog box to have PCBNEW display a list of the modules possessing the keyword "TO220".

### 12.13 - Managing 3-dimensional visualization

A module may have associated with it a file containing a three-dimensional representation of the component. In order to associate such a file with a module, select the **3D Settings** tab.

The options panel is the following:



The following information should be specified:

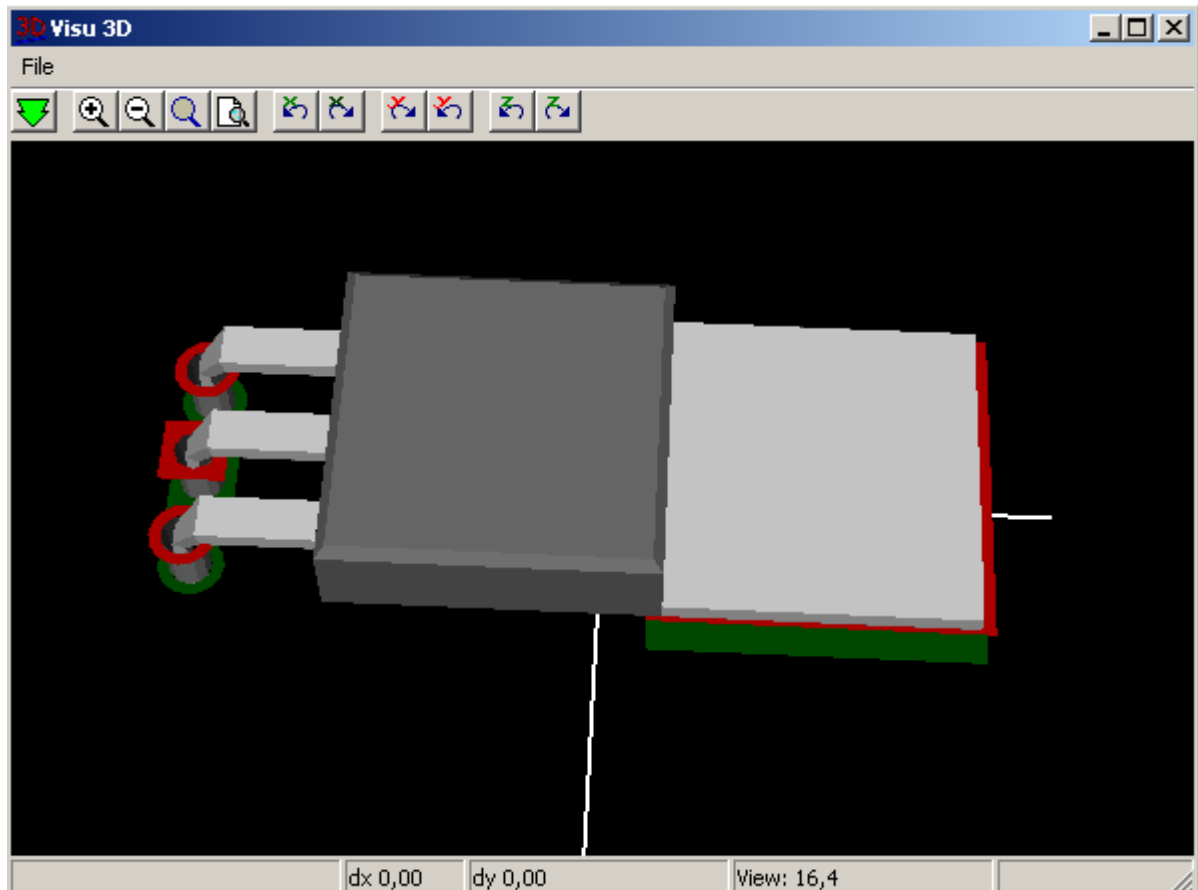
- The file containing the 3D representation (created by the 3D modeler **wings3d**, in **vrml format**, via the export to vrml command).  
The default path is **kicad/modules/package3d**. In the example, the file name is *discret/to\_220horiz.wrl*, using the default path)
- The x, y and z scales.
- The offset with respect to the anchor point of the module (usually zero).
- The initial rotation in degrees about each axis (usually zero).

Setting scale allows:

- To use the same 3D file for footprints which have similar shapes but different sizes (resistors, capacitors, SMD components...)
- For small (or very large) packages, a better use of the wings3D grid:


**Scale 1 -> 0.1 inch in Pcbnew = 1 grid unit in wings3D**

If such a file has been specified, it is possible to view the component in 3D:



The 3D model will automatically appear in the 3D representation of the printed circuit board.

#### 12.14 - Saving a module to the active library

The save command (modification of the file of the active library) is activated by the  icon.

If a module of the same name exists (an older version), it will be overwritten.

Because it is important to be able to have confidence in the library modules, it is worth double-checking the module for errors before saving.

Before saving, it is also recommended to change the reference or value of the module to be equal to the library name of the module..

#### 12.15 - Saving a module to the Board.

If the edited footprint comes from the current Board, the tool  updates this footprint on board.