

POWER SUPPLY /D.I.P. READ ONLY MEMORY SECTIONS

sheet 1 of 6


---


## A P P L E

M5660 "MacINTOSH IIcx"

## **COMPUTER LOGIC BOARD**

8/8/91

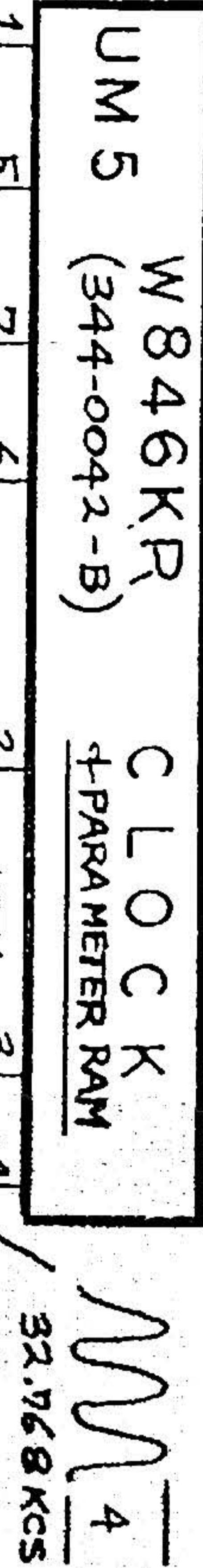





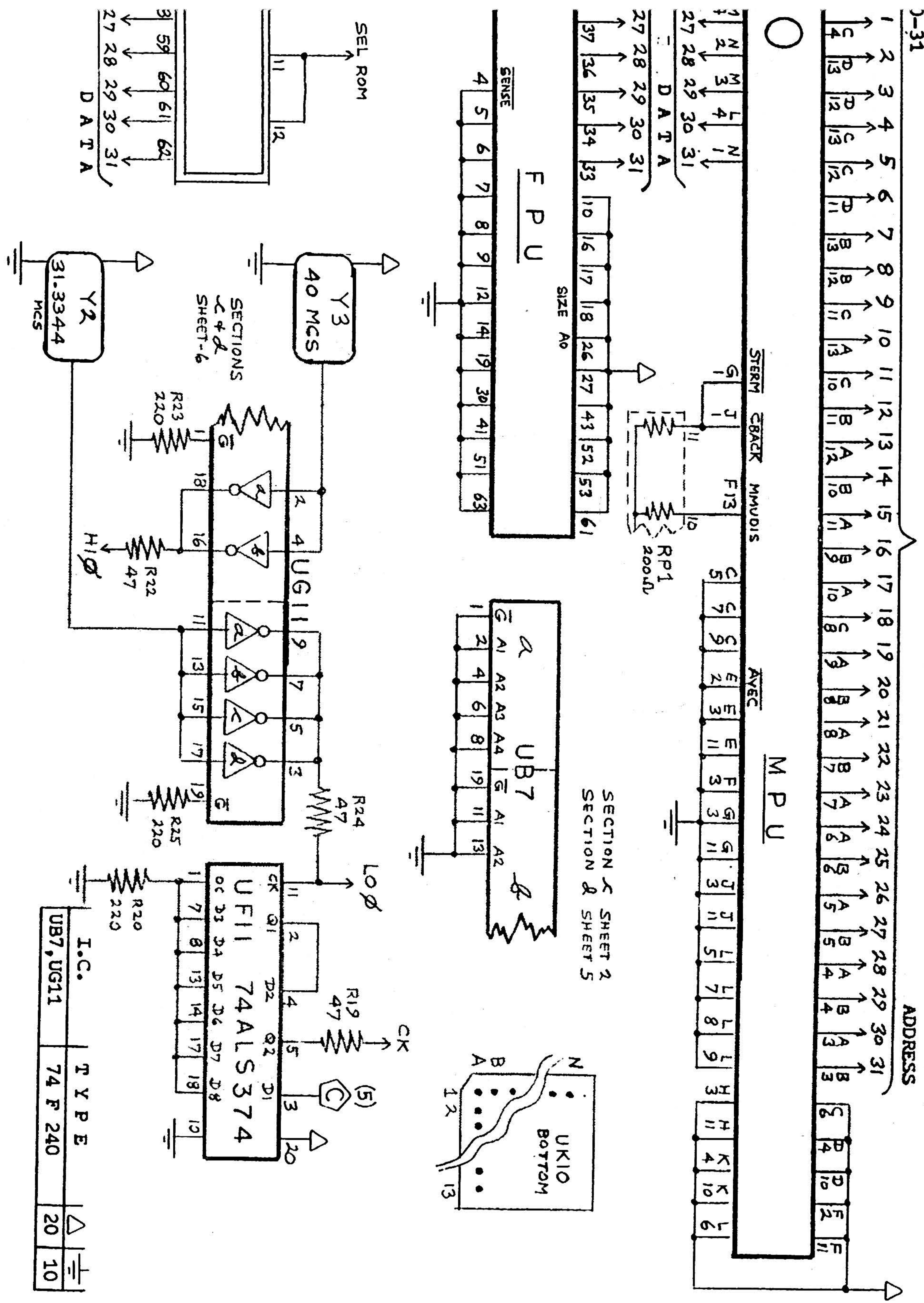
UM 5

W 846 KR  
(344-0042-B)

CLOCK  
+PARAMETER RAM







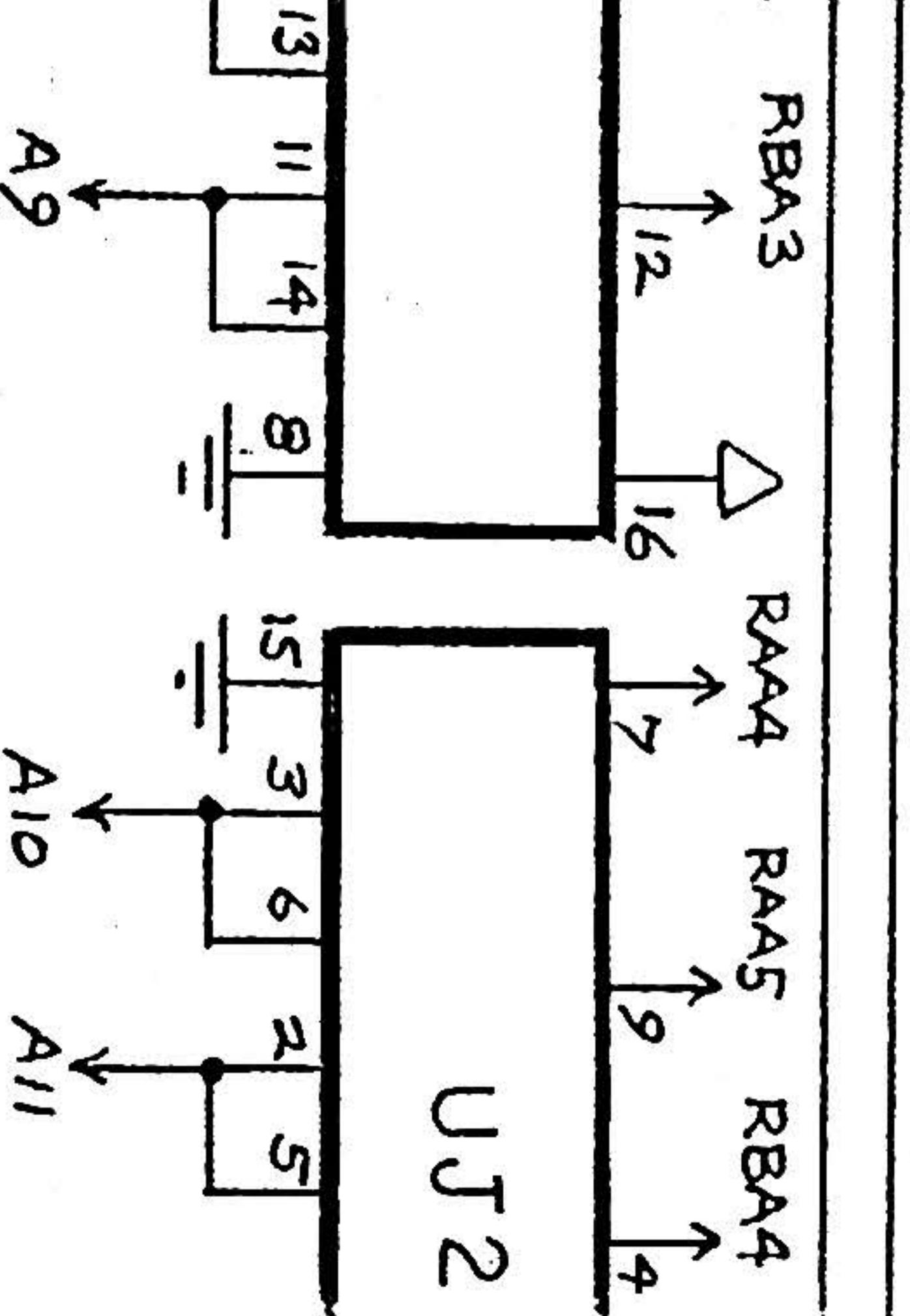
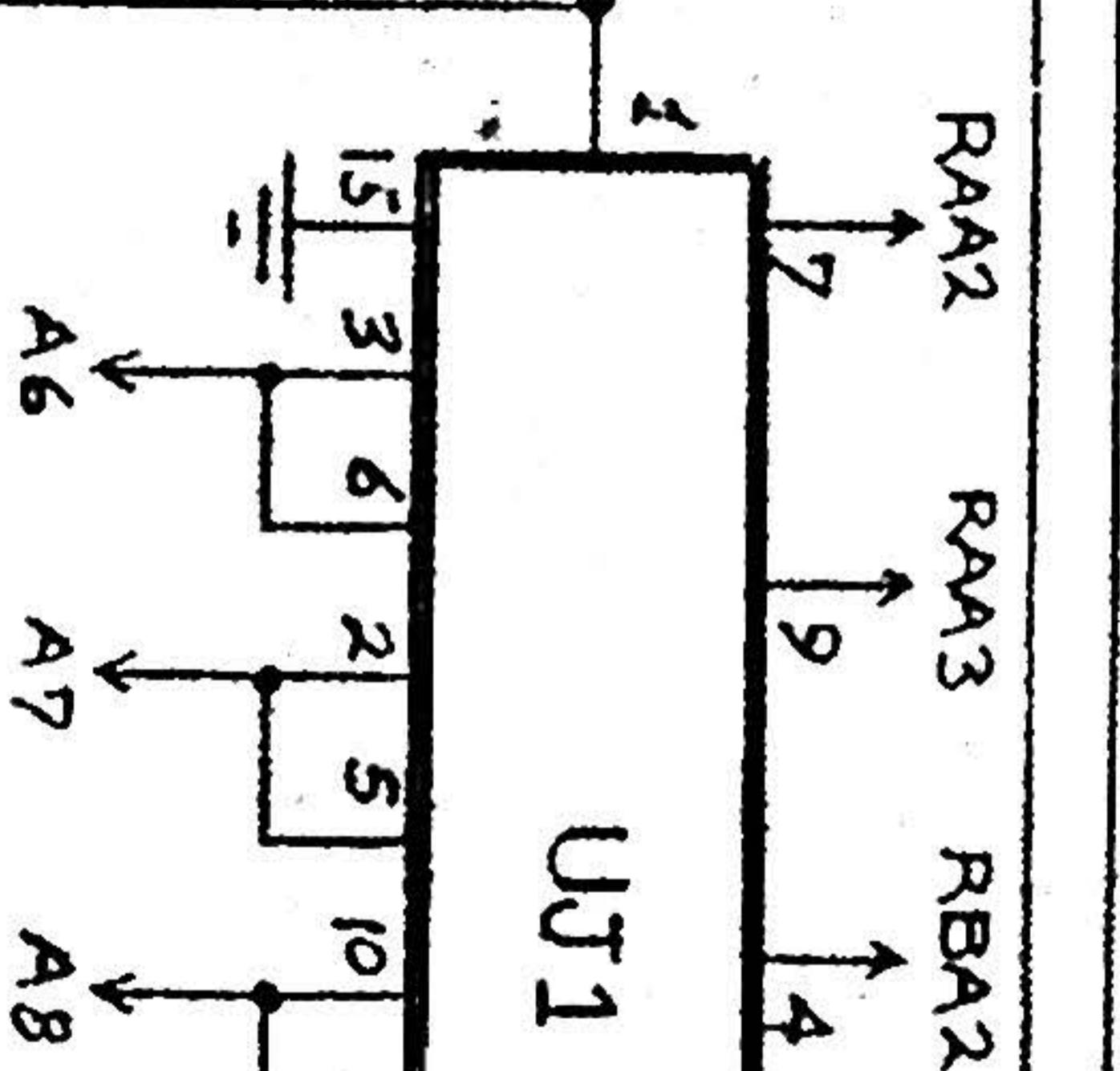
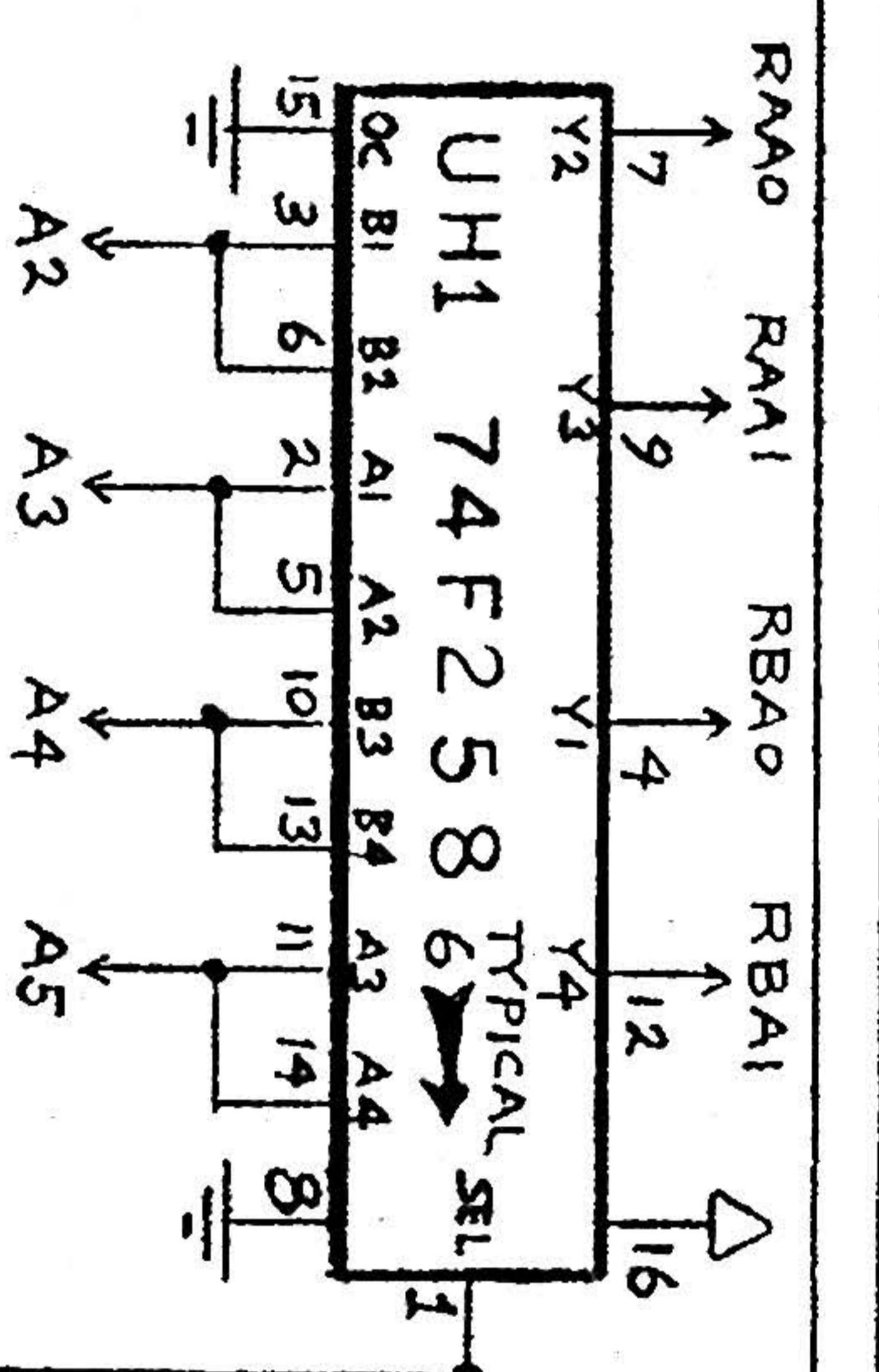
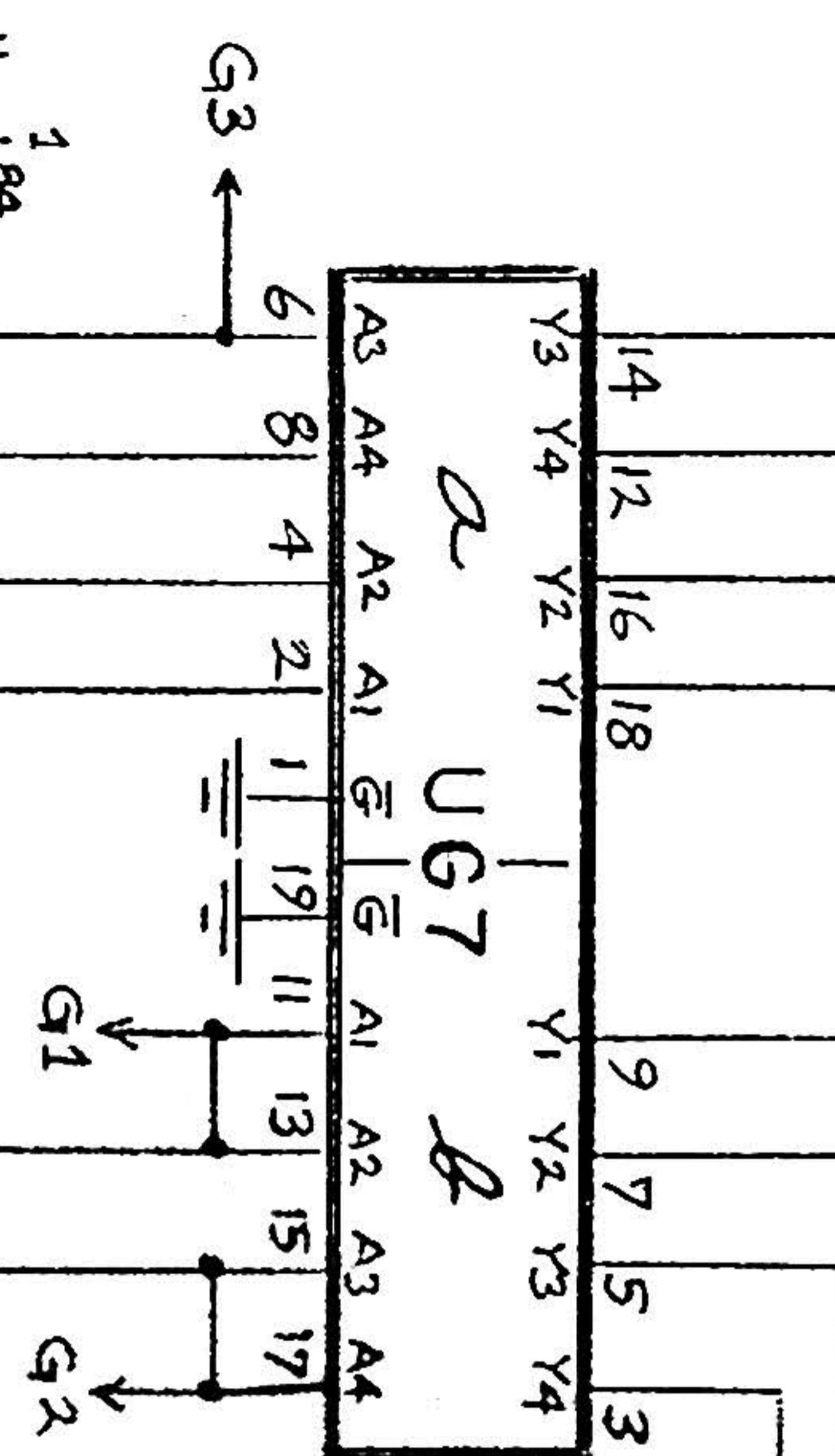
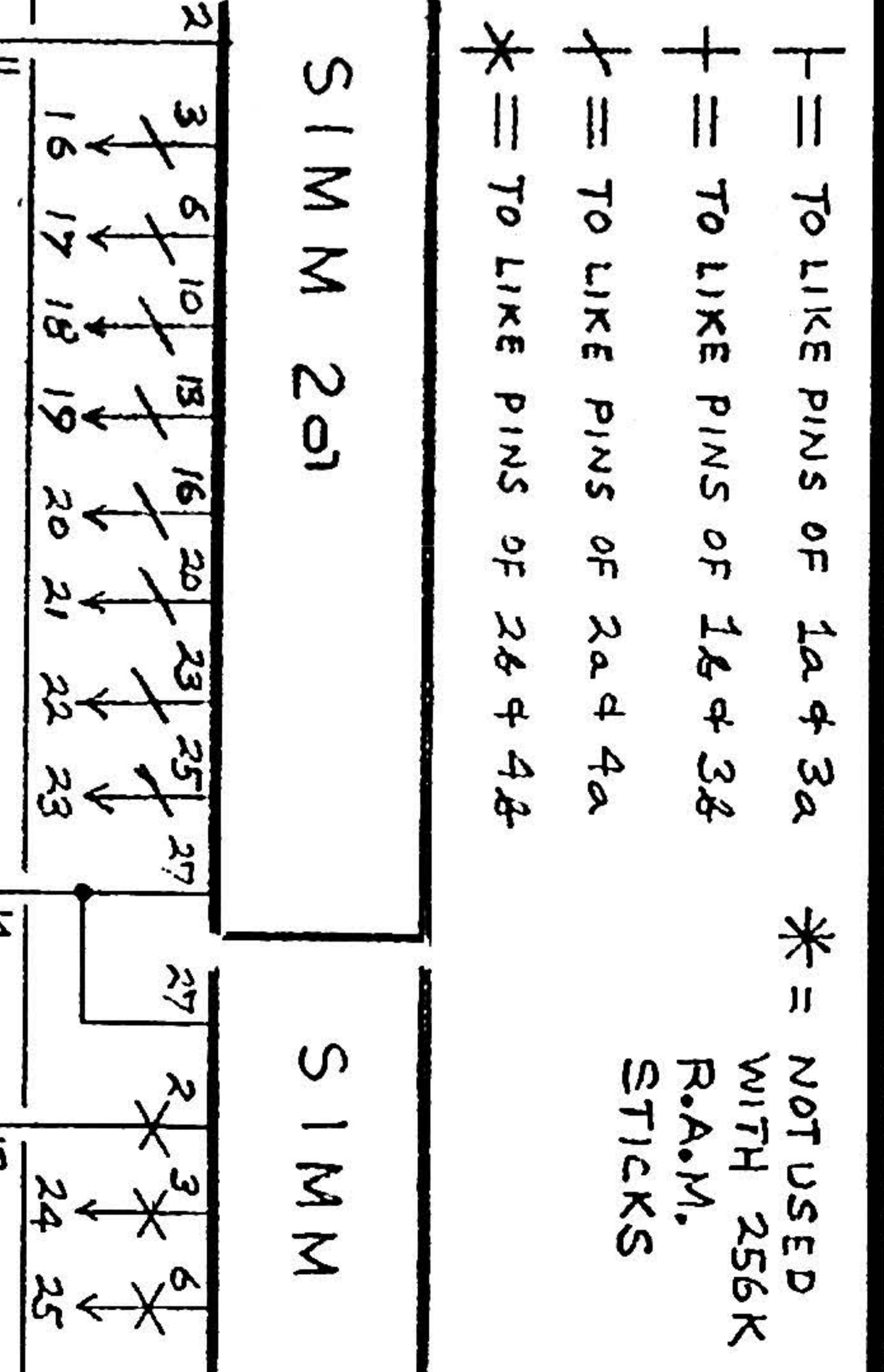
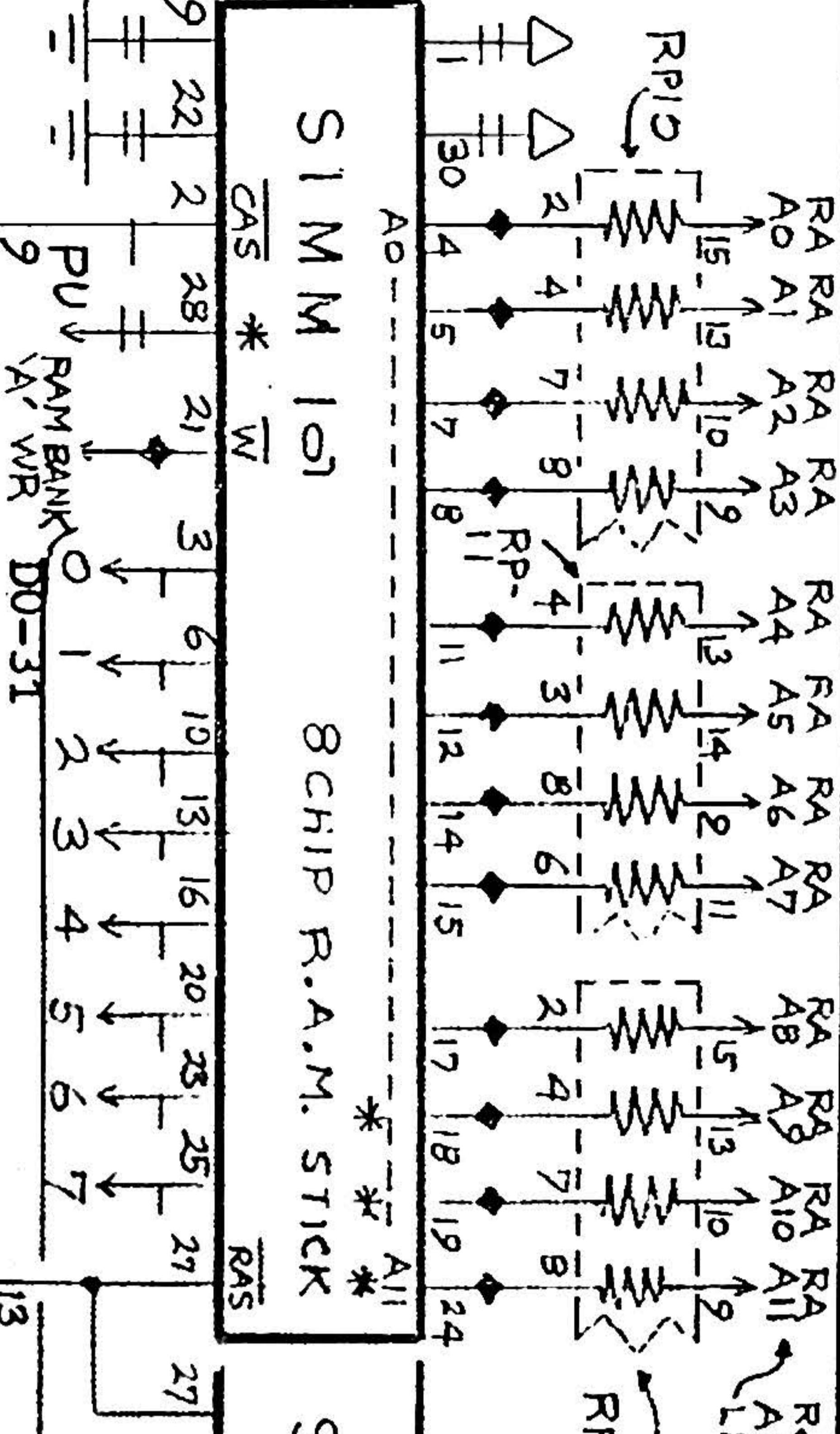
M.P.U. / F.P.U. / ALTERNATE R.O.M. SECTIONS

sheet 4 of 6

**A P P L E** | M5660 "MacINTOSH IIcx"

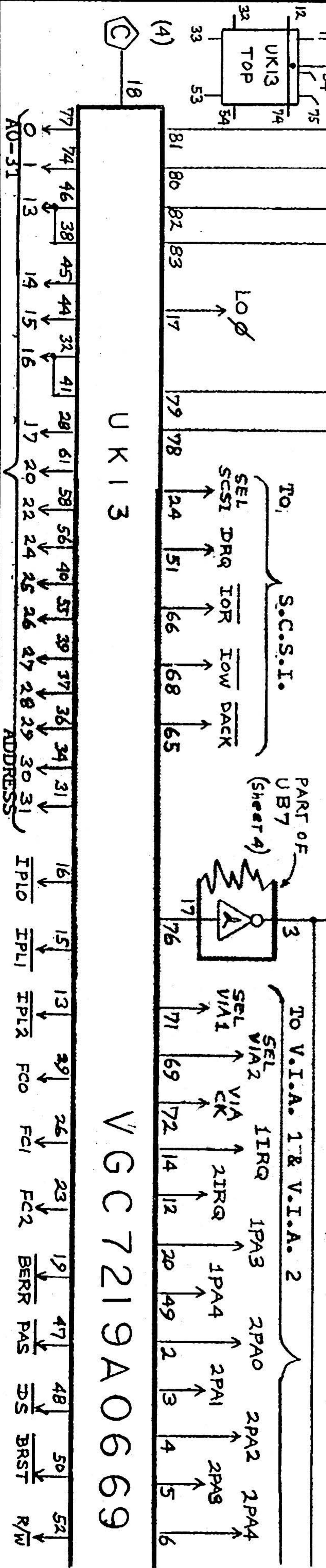
**COMPUTER LOGIC BOARD 820F0230-R**

8/16/91- 



U K 13

V G C 7219A0669



R.A.M. ADDRESS  
lines

$RPI_0, RPI_1, RPI_2 = 11250300 = 22.5\Omega \times 8$

I.C.	TYPE	$\Delta$	$\overline{\Delta}$
UG7	74F240	20	10

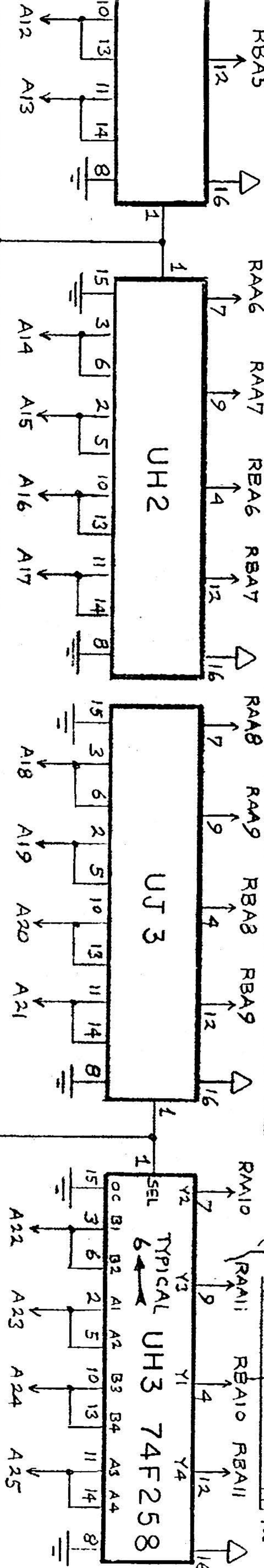
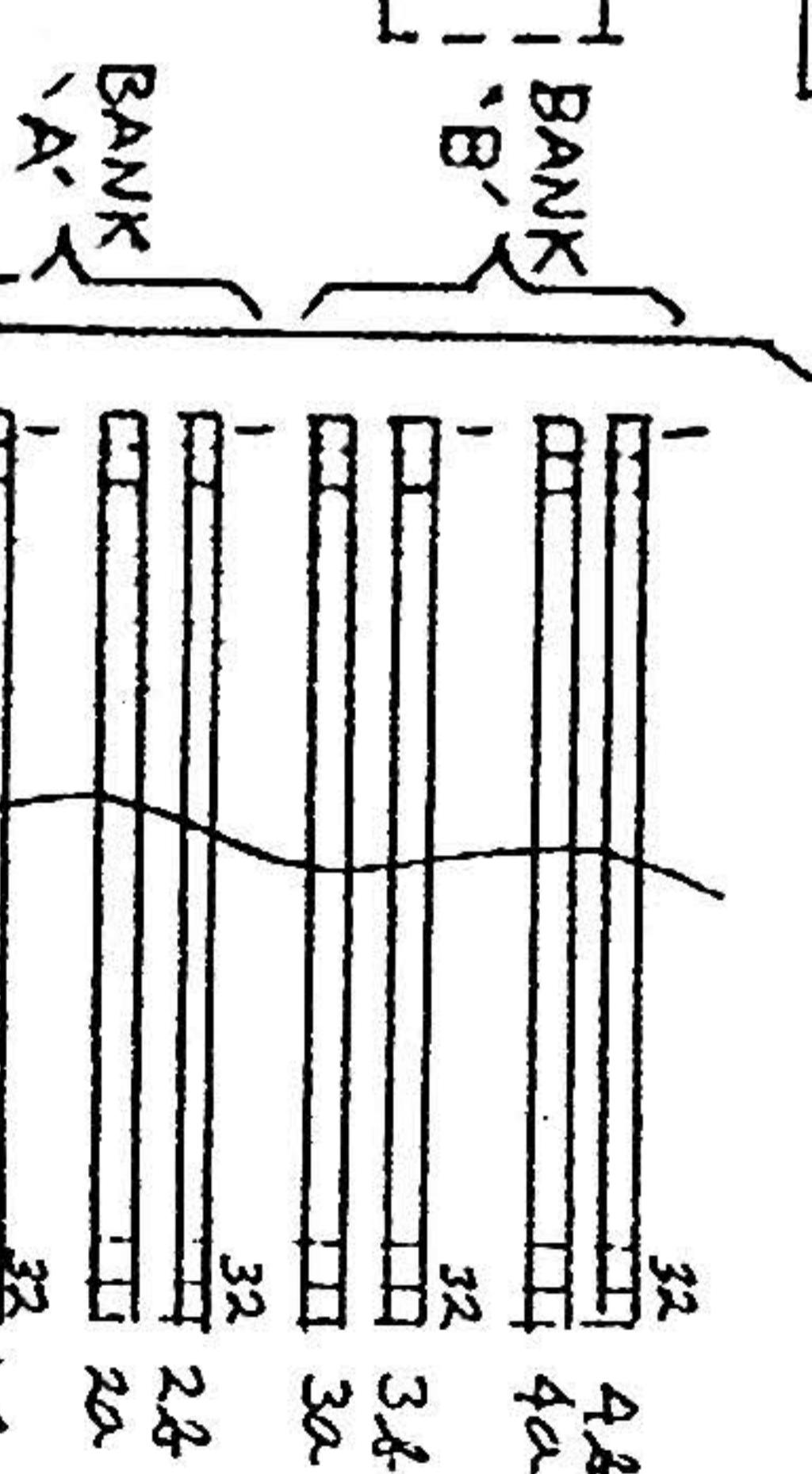
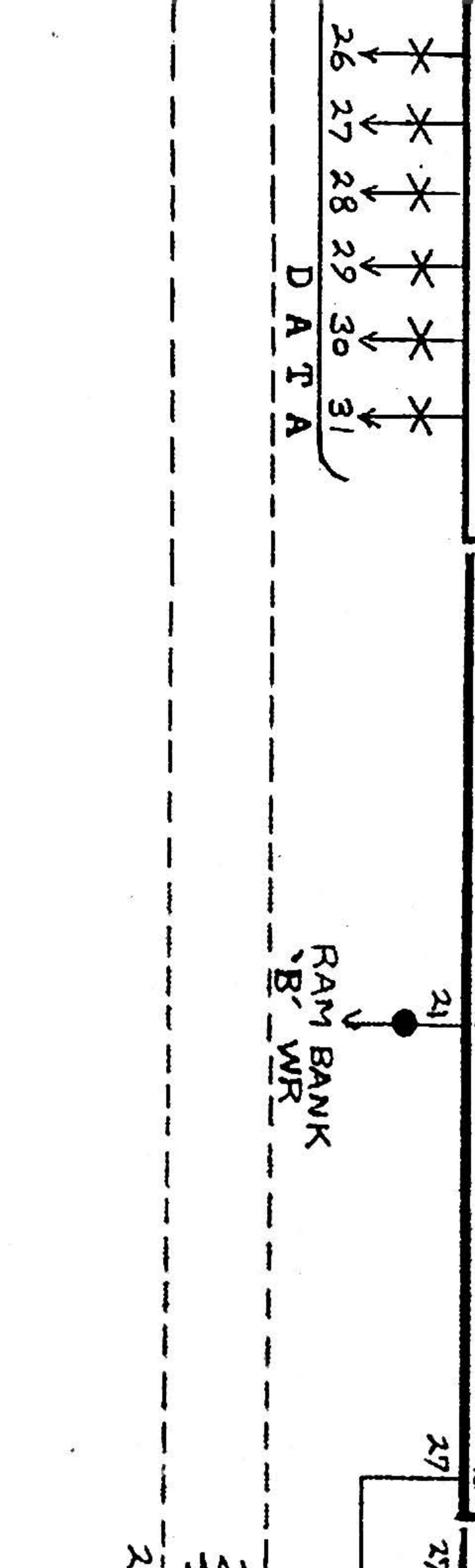
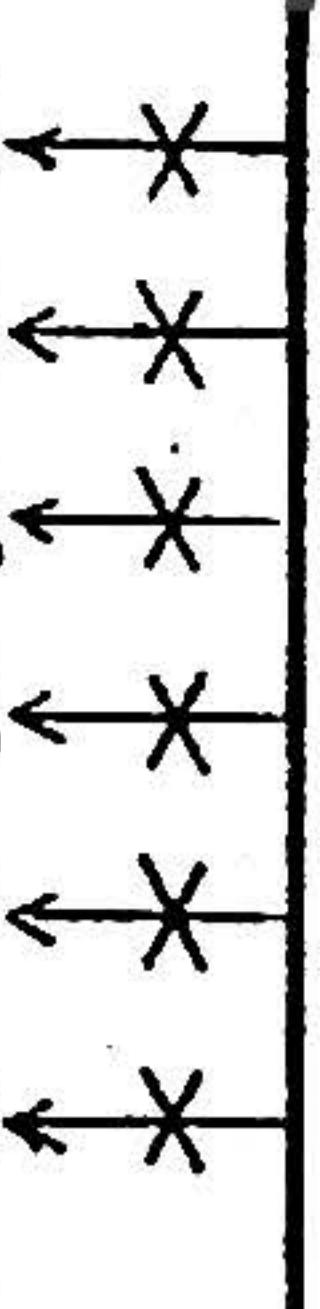
2 b

SIMM 301

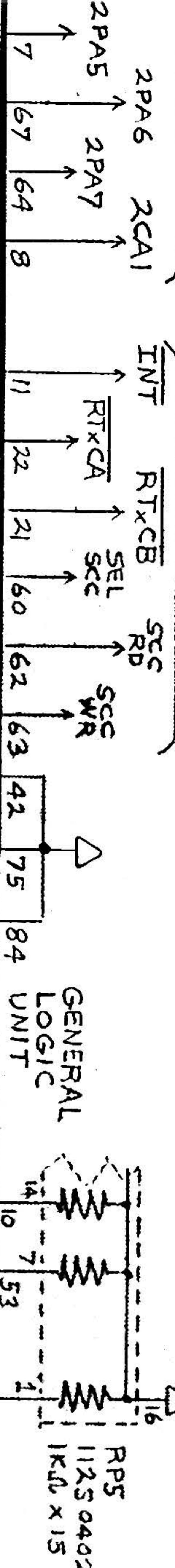
SIMM 3 b

SIMM 4 a

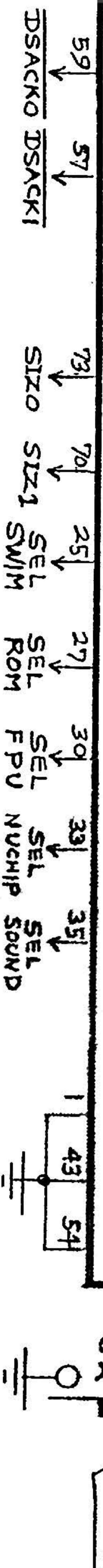
SIMM 4 b



To S.C.C.

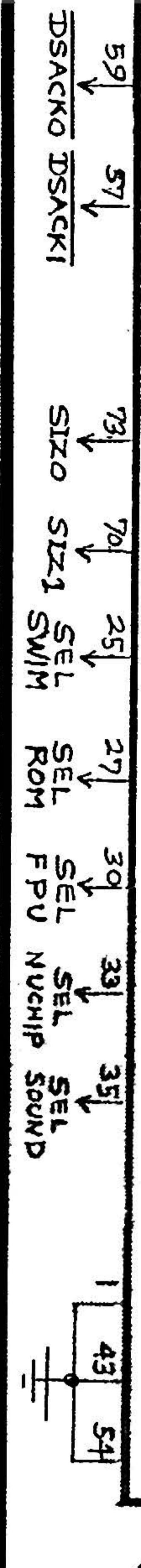


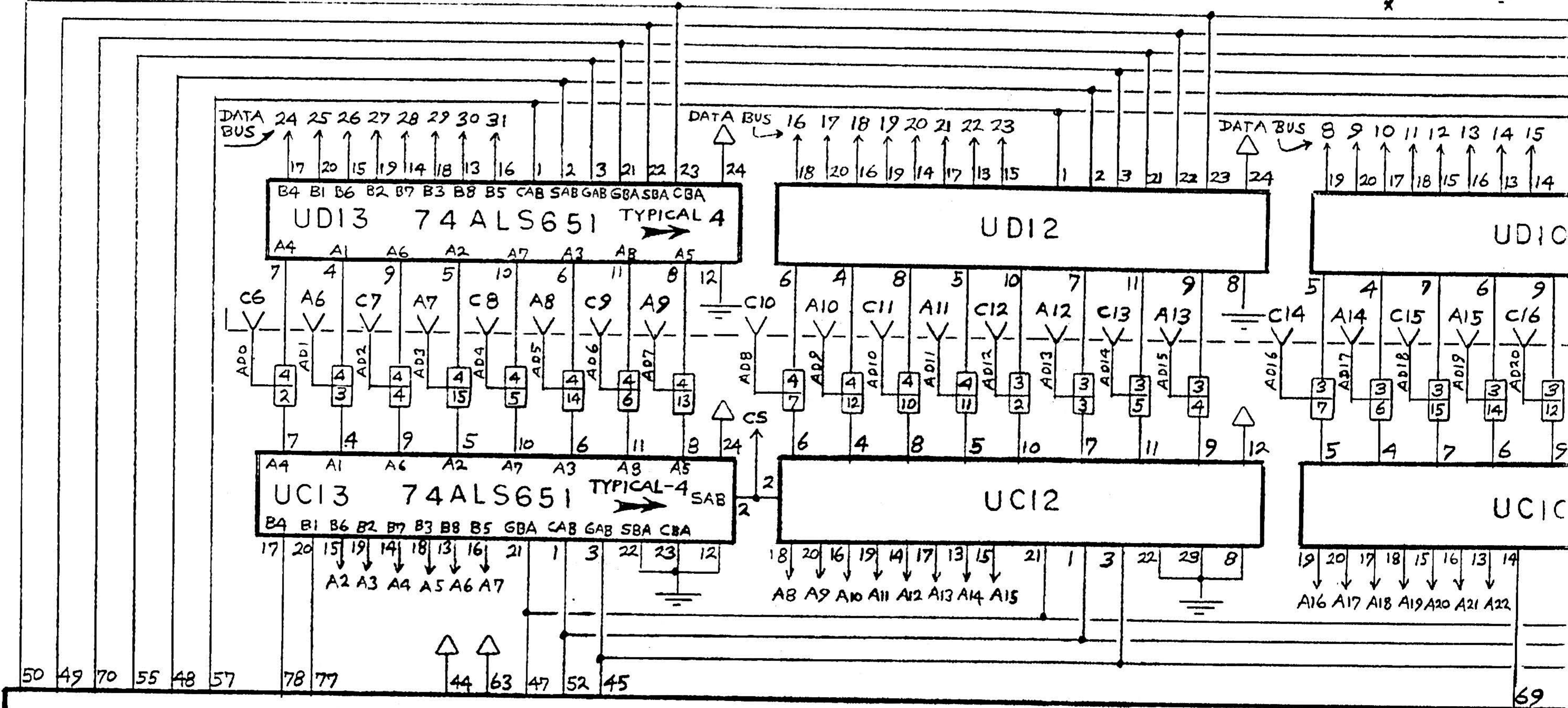
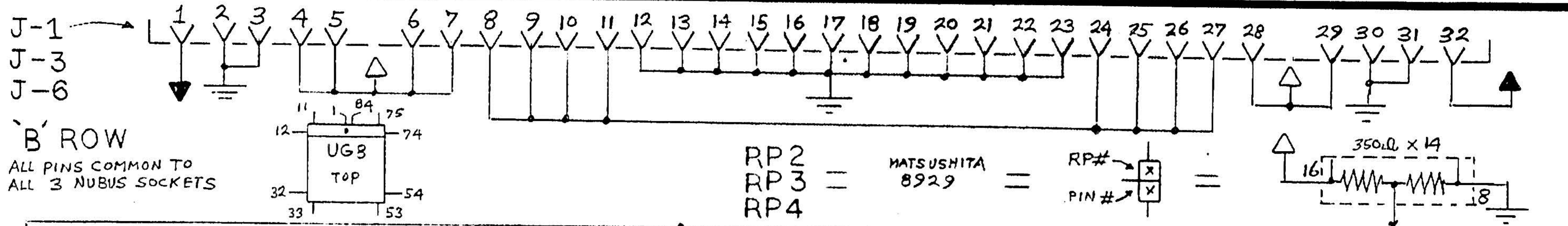
(34450602-A)



GLU

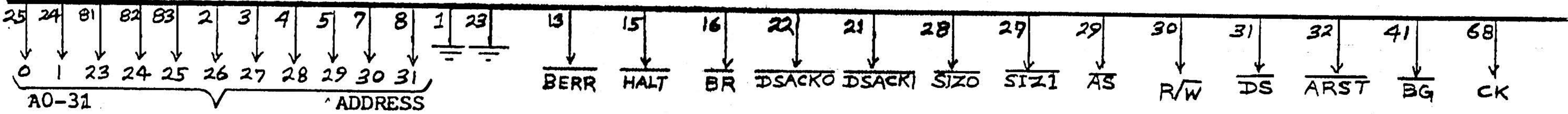
N.M.I.



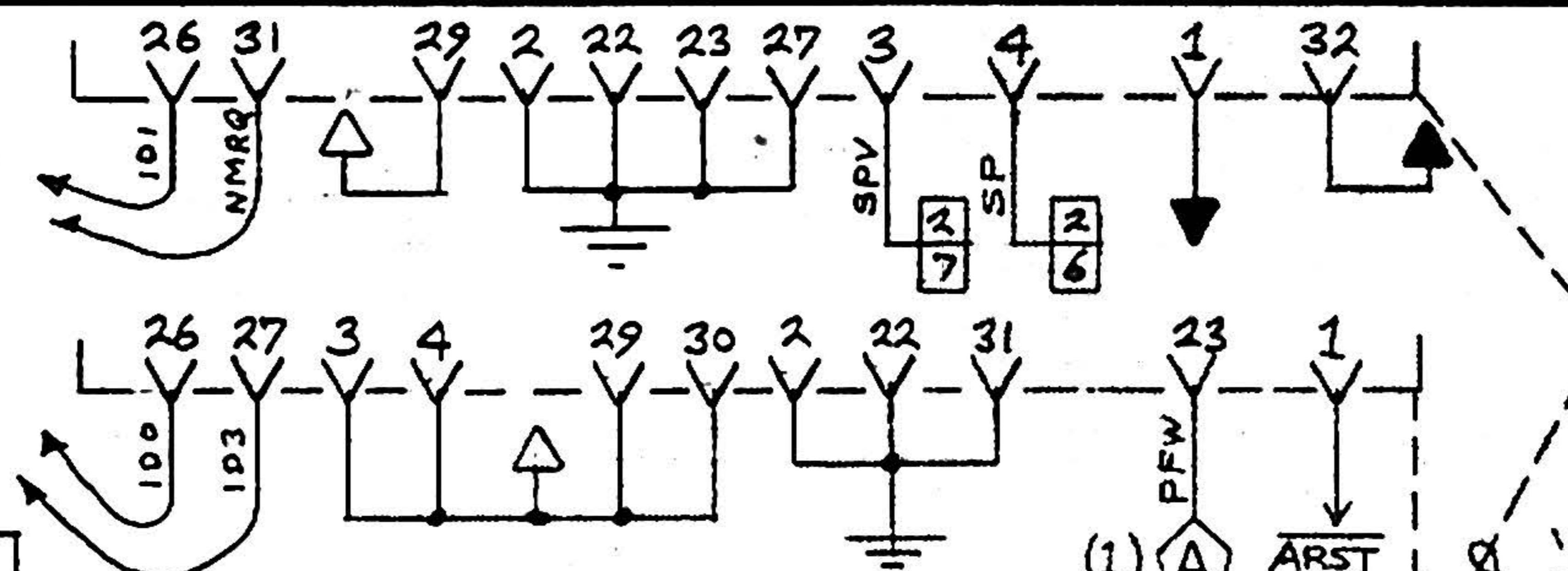


UG 8

VGC7219A6063



PIN → SOCKET	A26	A31	C26	C27
J1	OPEN	2PA0	GROUND	OPEN
J3	GROUND	2PA1	OPEN	OPEN
J6	GROUND	2PA2	GROUND	OPEN



SOCKETS J1, J3, J6 (PIN VIEW)  

320 0 0 0	0 0 0 0 1
320 0 0 0	0 0 0 0 1
320 0 0 0	0 0 0 0 1

 ROW A  

0 0 0 0 0	0 0 0 0 1
0 0 0 0 0	0 0 0 0 1
0 0 0 0 0	0 0 0 0 1

 ROW B  

0 0 0 0 0	0 0 0 0 1
0 0 0 0 0	0 0 0 0 1
0 0 0 0 0	0 0 0 0 1

 ROW C

I.C. TYPE △ ─  
 UA7, UF7 74F240 20 10

