


POWER SUPPLY / D.I.P. READ ONLY MEMORY SECTIONS


sheet 1 of 7


APPLE W4740 "MacINTOSH IIci"

COMPUTER LOGIC BOARD 820F0242-01

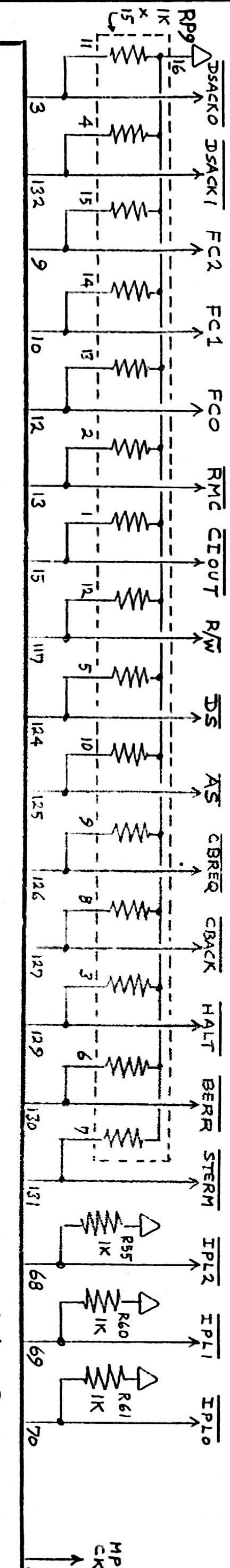
1/29/92- 2







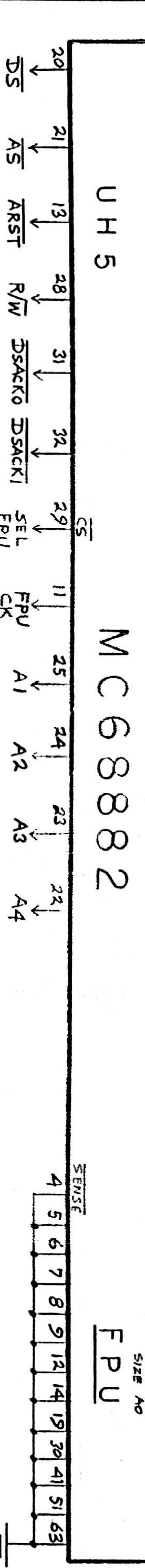
1A = MMBT3904
MX = 1SMB48AT3



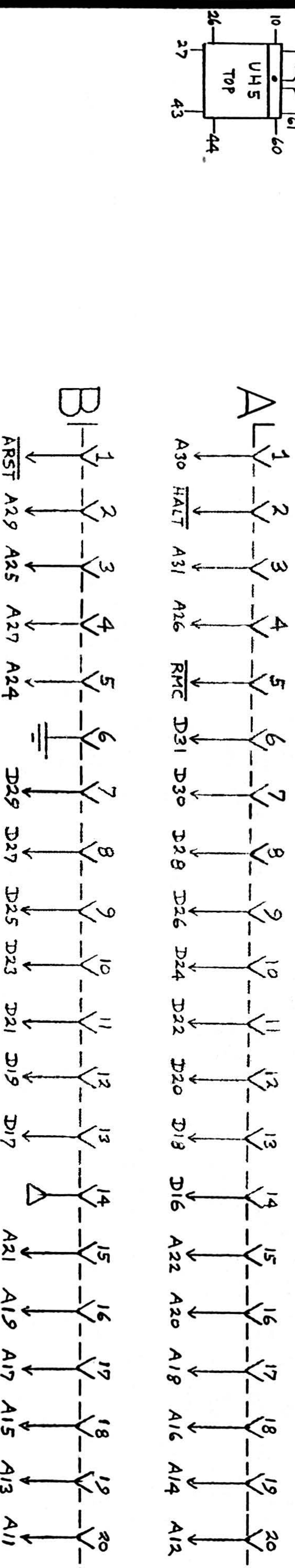
M C 6 8 8 8 2

F P U

S E N S E 4 5 6 7 8 9 12 14 19 30 41 51 63



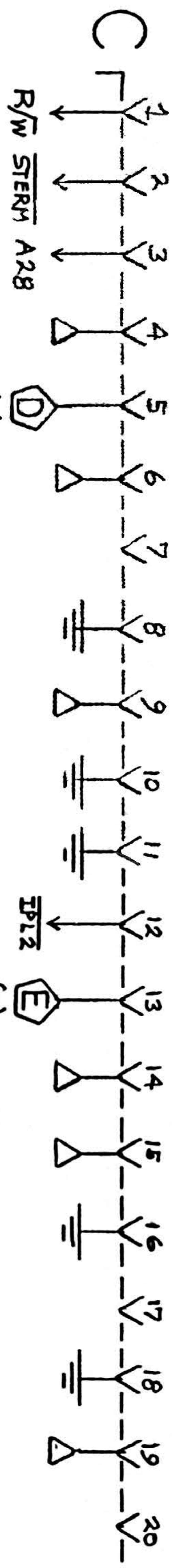
U H 5



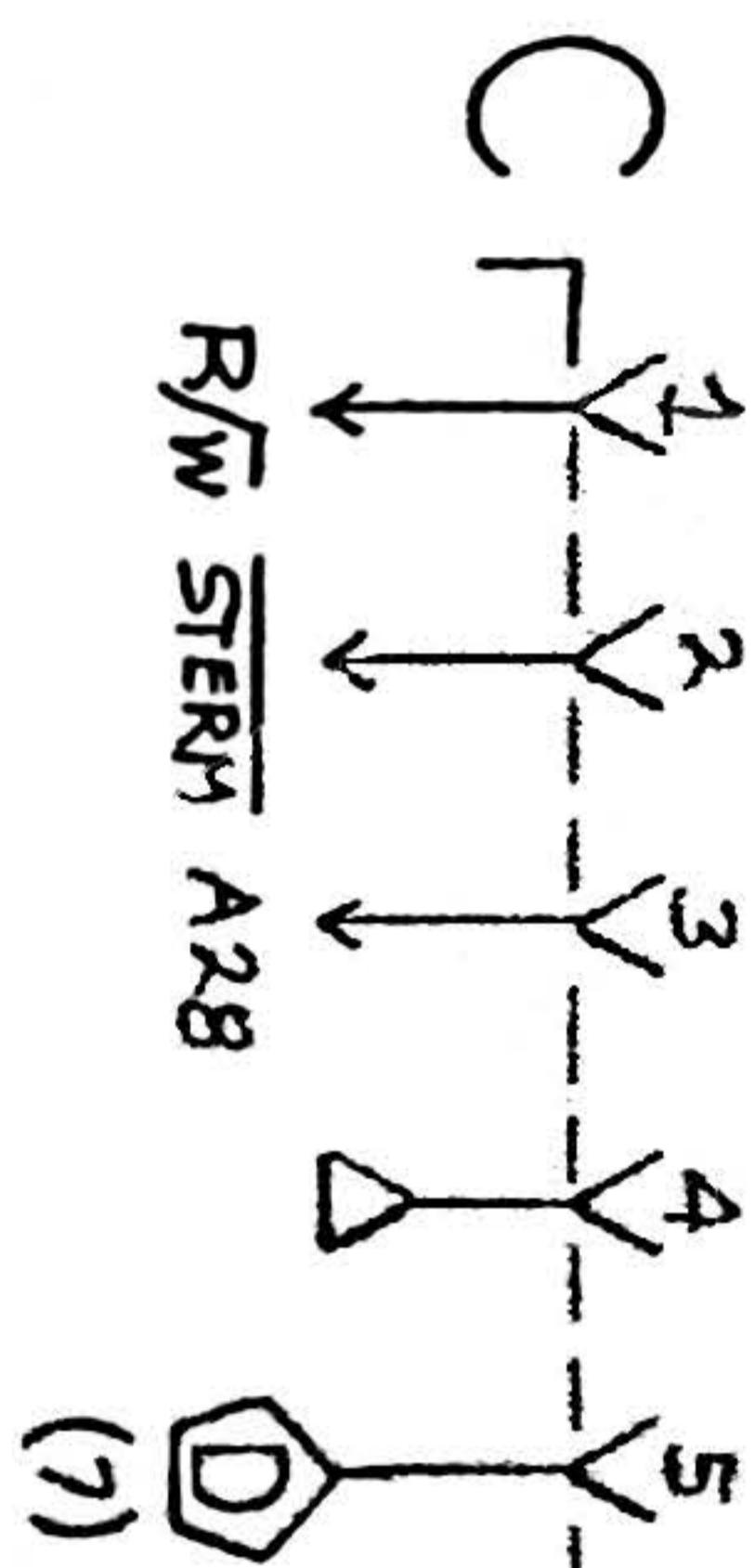
A L

U H 5

4039




PIN VIEW



(7)

J-13



PART OF U K 11

RAII

RAS¹

CAS

18

CAS2

RAS2

CAS26

RPI0

5

RPI4

16

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RPI4

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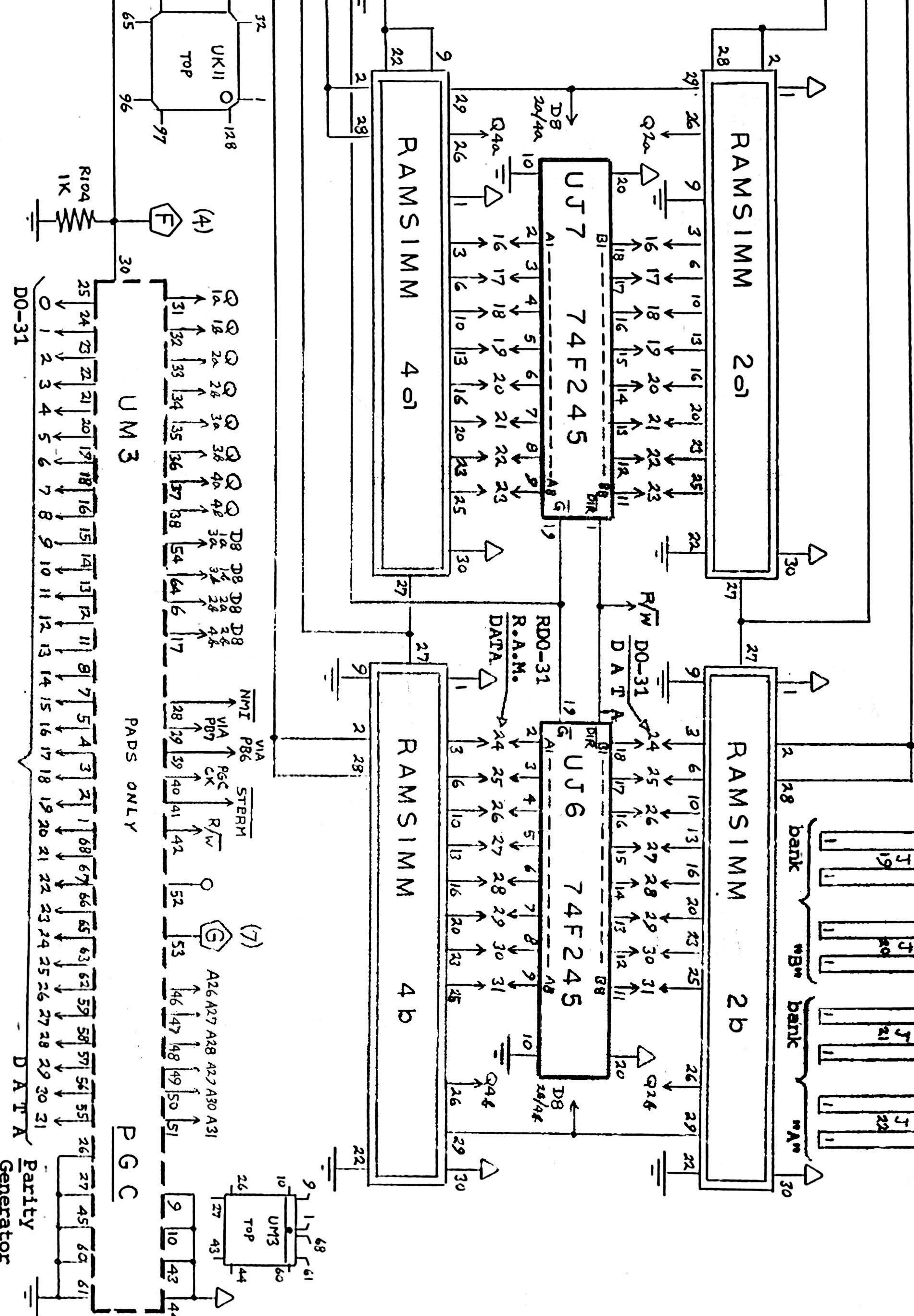
12

12

● = To like pins
 - = of bank "B"
 SIMM sockets

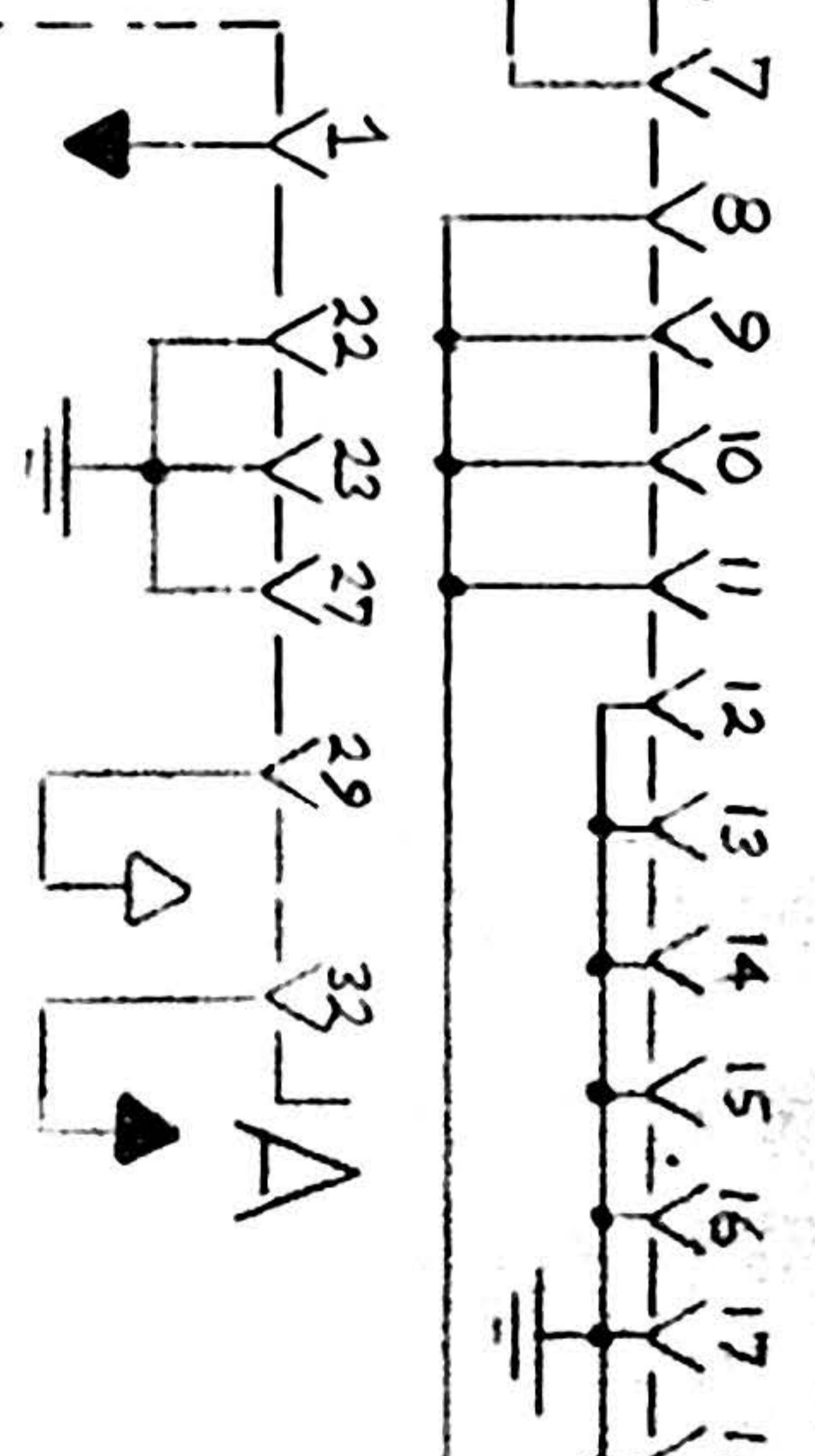
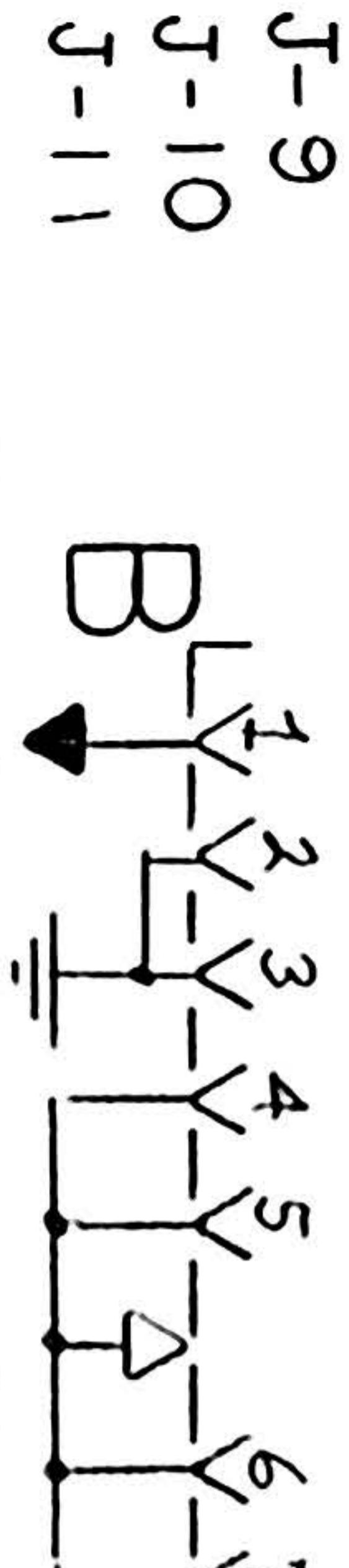
● = to like pins
 - = of bank "A"
 SIMM sockets

RP10-RP14 = SOMC1603-220G =
22 ohm x 15

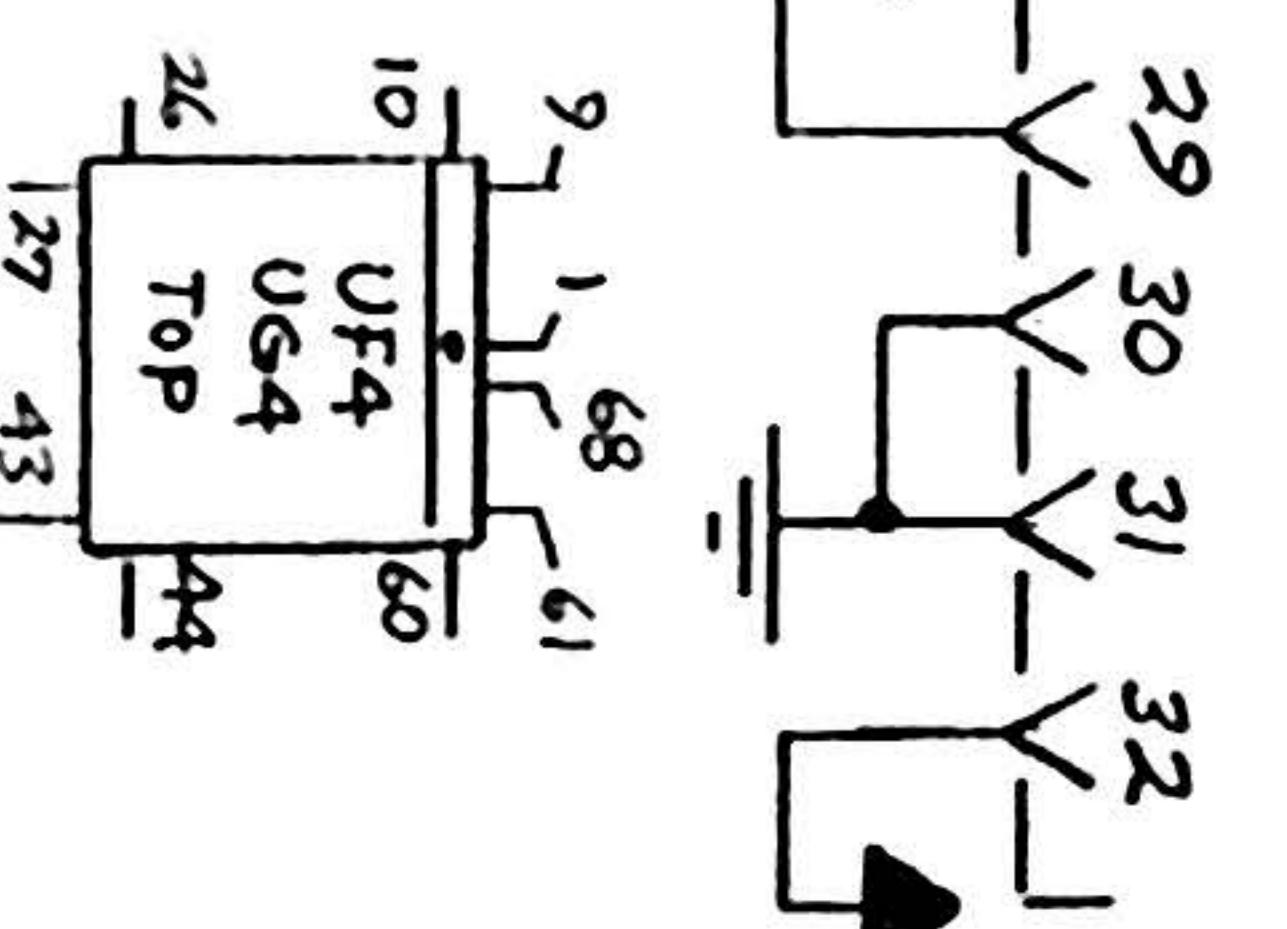


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D T E: All row pins common to all 3 NUBUS sockets EXCEPT those shown in chart at upper right.



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The diagram illustrates the connection between a 16x16 address decoder and a 3-to-8 decoder.

A2-15 ADDRESS

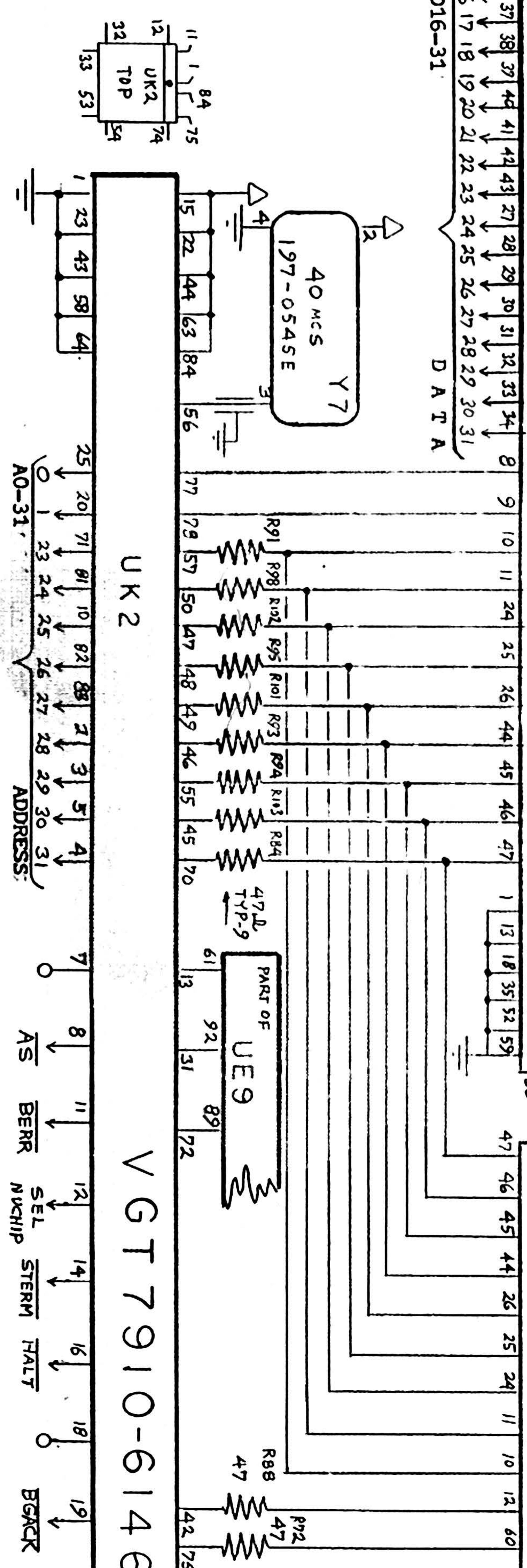
The 16x16 address decoder has 16 inputs labeled 1 through 16. The outputs of the decoder are connected to the inputs of a 3-to-8 decoder. The 3-to-8 decoder has three inputs labeled 3, 4, and 29. The output of the 3-to-8 decoder is connected to a switch labeled C.

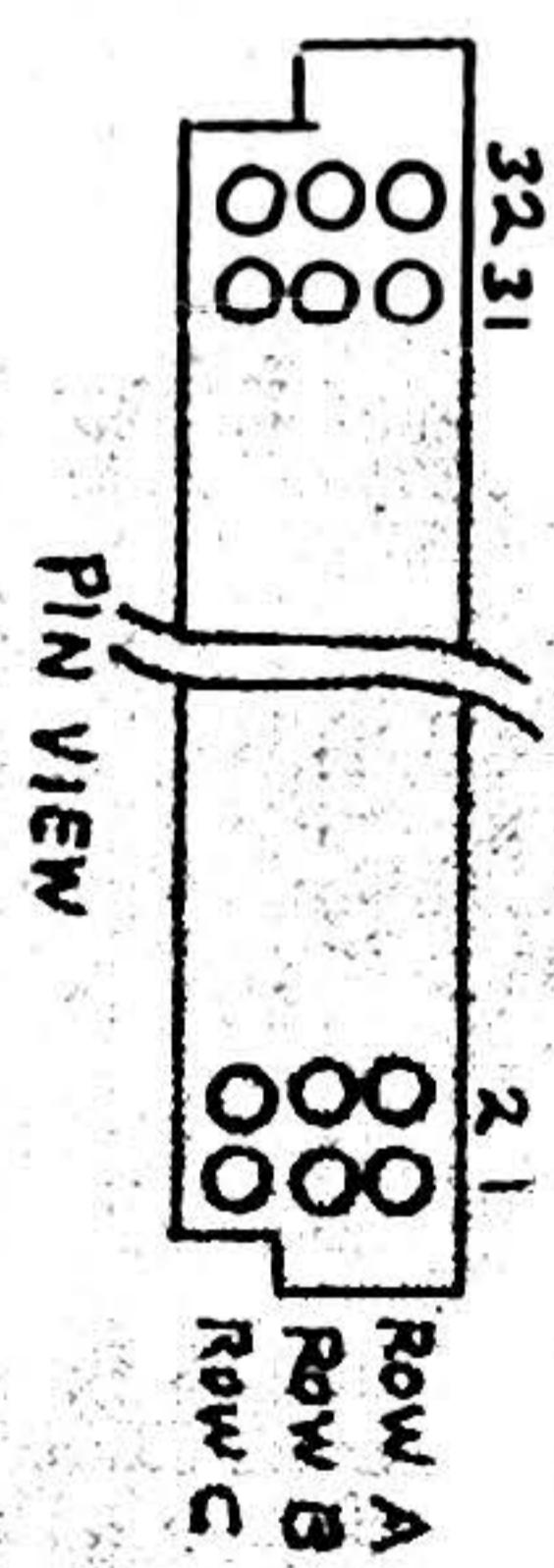
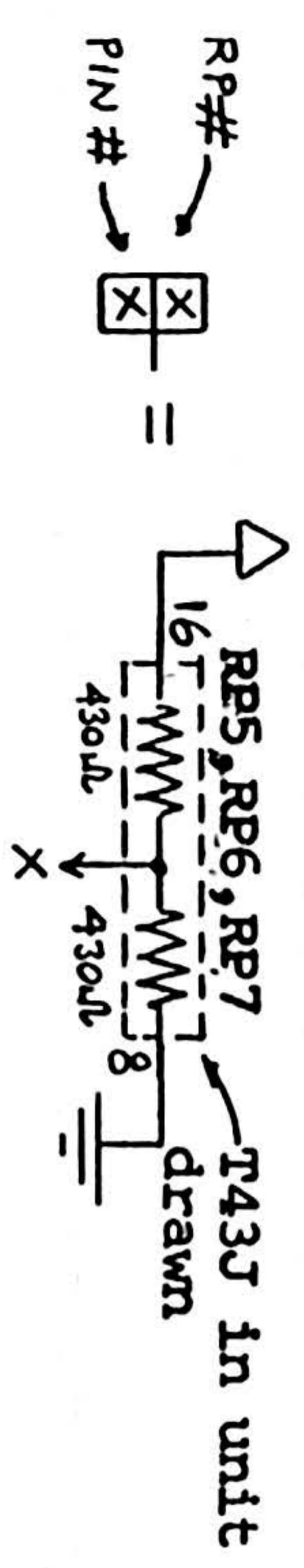
D16-31	DATA	31	32	33	34	35	36	37	38	39	40	41	42	43	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
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X-T-A-14807 (34351027)

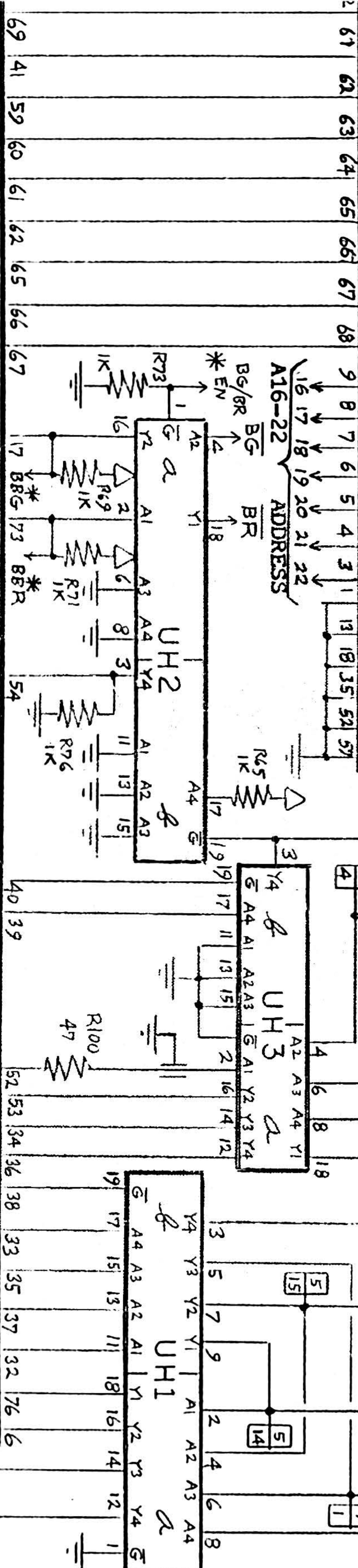
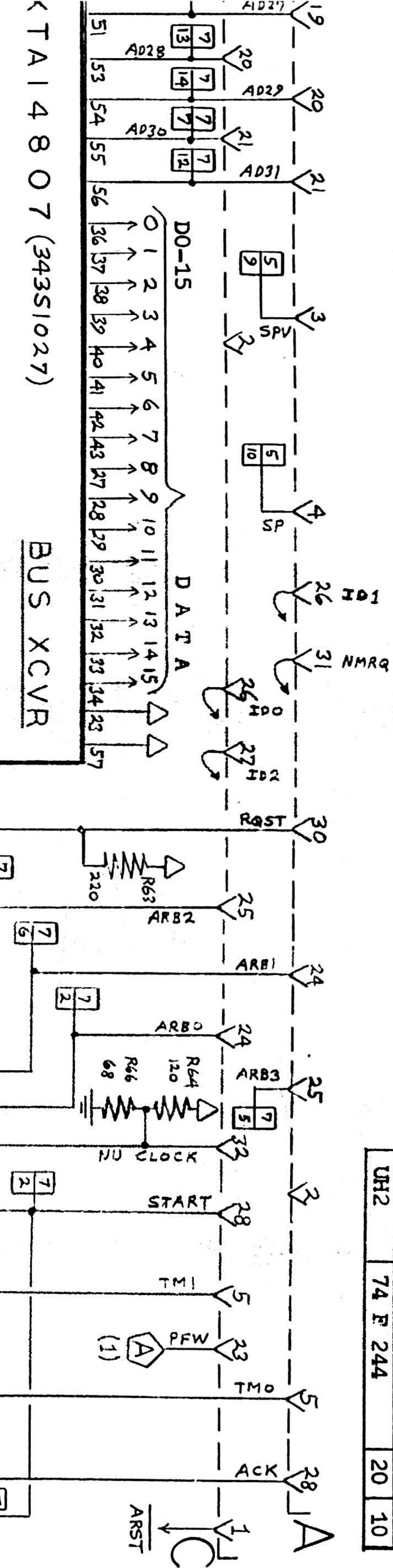
W C S X C < H

C
G
A

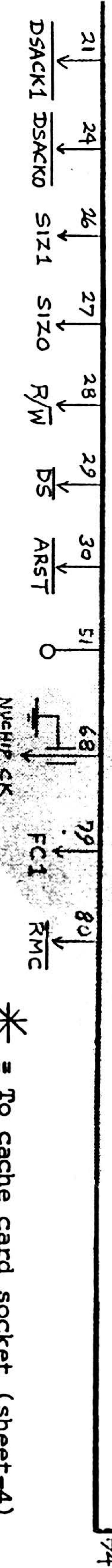





SOCKET	PIN	A26	A31	C26	C27
SLOT C	J9	OPEN	TO RBV	OPEN	GND
SLOT D	J10	OPEN	TO RBV	OPEN	GND
SLOT E	J11	GND	TO RBV	GND	GND
I.C.	△	=			
UH1, UH3	74ALS240A.	20	10		
UH2	74E244	20	10		




(34351020-01)



* = To cache card socket (sheet-4)





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三
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VIDEO PORT / CLOCK / GATE ARRAY SECTIONS

sheet 7 of 7

APPLE

W4740 "McINTOSH IIci"

COMPUTER LOGIC BOARD 820F02A2-0

2/2/92 - 8