

POWER SUPPLY / A.D.B. PORT / CONTROL MICRO

sheet 1 of 12

APPLE Ma

MacINTOSH "QUADRA 840AV"

## **COMPUTER LOGIC BOARD**

9/5/94- 

PART OF U 32

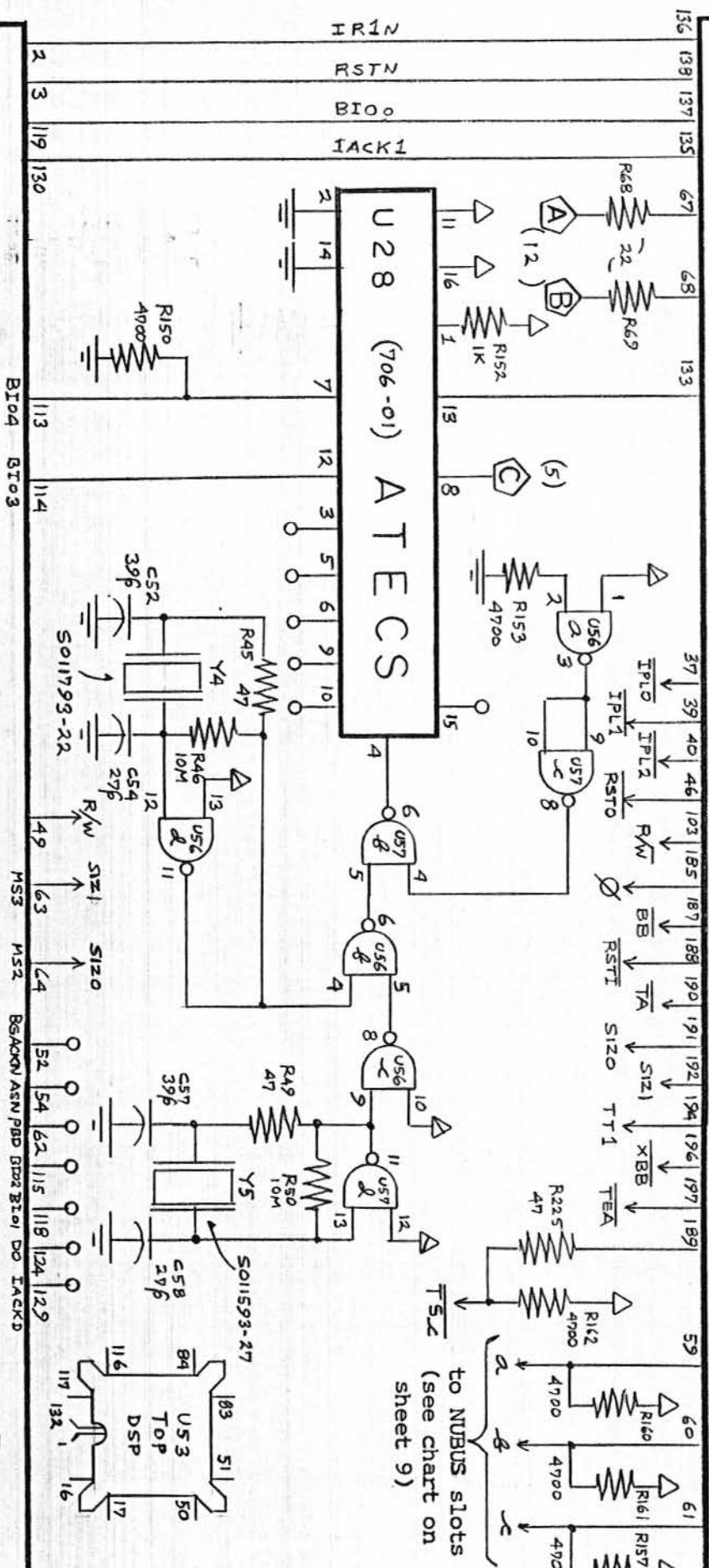
U 32

 $\Delta = 2, 4, 14, 19, 49, 51, 54, 56, 83, 100, 101, 103, 106, 108, 130, 153, 155, 158, 160, 181, 205, 207$ 

V Y

A0-31	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	
	36	38	39	135	67	68	133																										
	36	35	33	32	31	30	28	27	25	24	23	22	21	20	18	17	15	13	12	10	9	8	7	6	5	24	23	22	21	20	19	18	
	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	
	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	

ADDRESS



U 53

 $\Delta = 12, 23, 33, 45, 60, 73, 87, 98, 111, 122, 132$ 

D S P

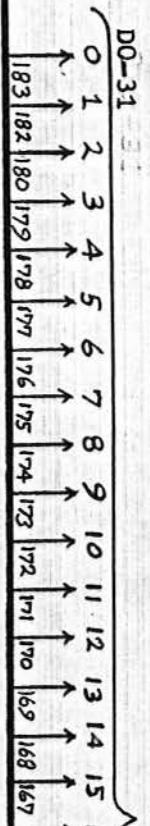
M50	M51	A2 - - -	A31
66	66	107	105
65	65	108	104
64	64	109	103
63	63	110	102
62	62	111	101
61	61	112	100
60	60	113	99
59	59	114	98
58	58	115	97
57	57	116	96
56	56	117	95
55	55	118	94
54	54	119	93
53	53	120	92
52	52	121	91
51	51	122	90
50	50	123	89
49	49	124	88
48	48	125	87
47	47	126	86
46	46	127	85
45	45	128	84
44	44	129	83
43	43	130	82
42	42	131	81
41	41	132	80
40	40	133	79
39	39	134	78
38	38	135	77
37	37	136	76
36	36	137	75
35	35	138	74
34	34	139	73
33	33	140	72
32	32	141	71
31	31	142	70
30	30	143	69
29	29	144	68

--- ADDRESS

A31 - - - ADDRESS

D0-31

DATA

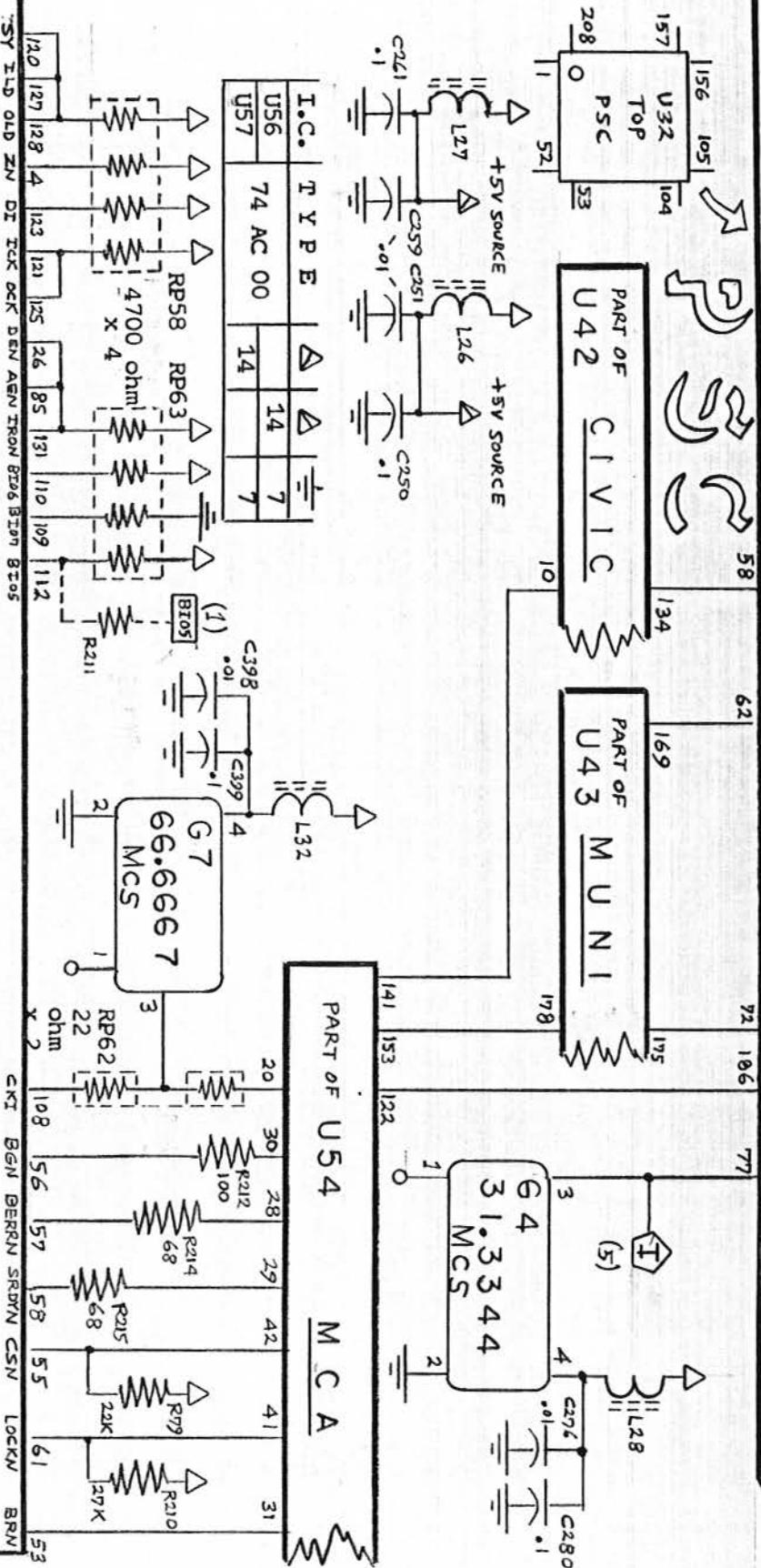
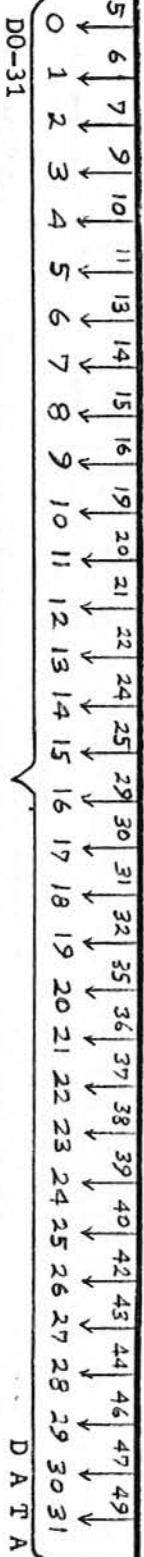


1 6 6 2 4 - 2

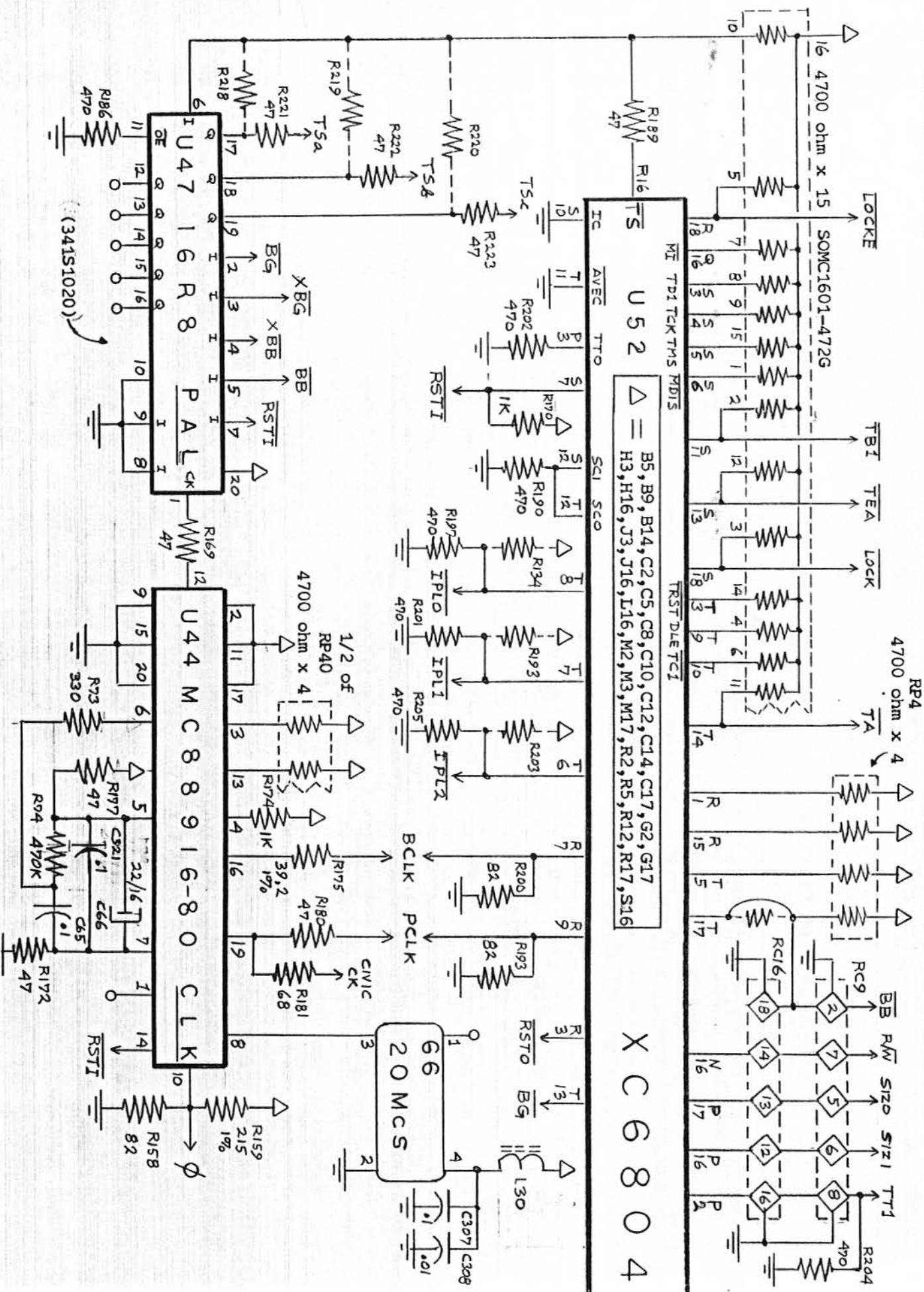
$$\frac{1}{2} = 1, 3, 11, 16, 26, 29, 34, 38, 50, 52, 53, 55, 57, 78, 102, 104, 105, 107, (34351129-a)$$

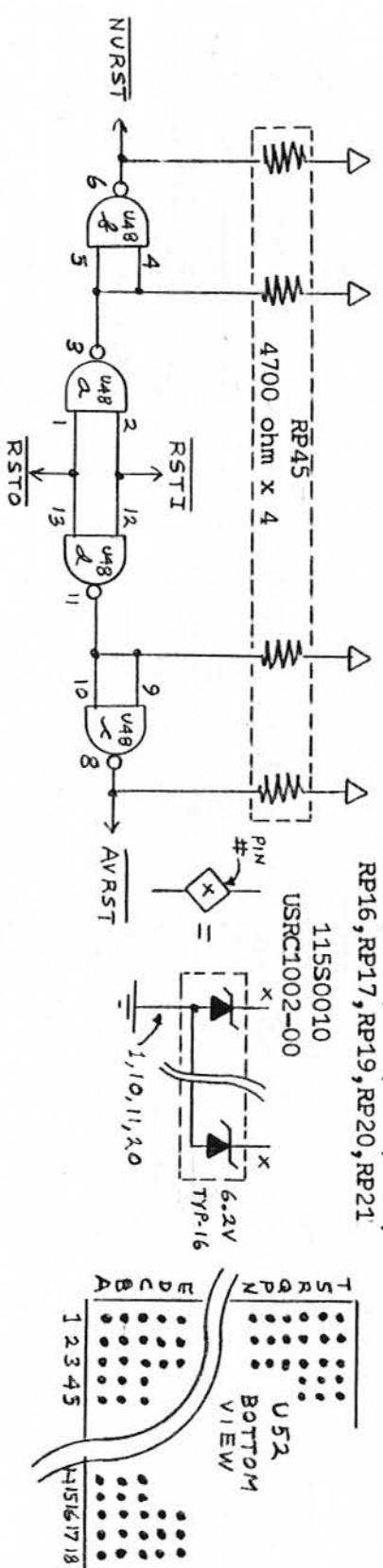
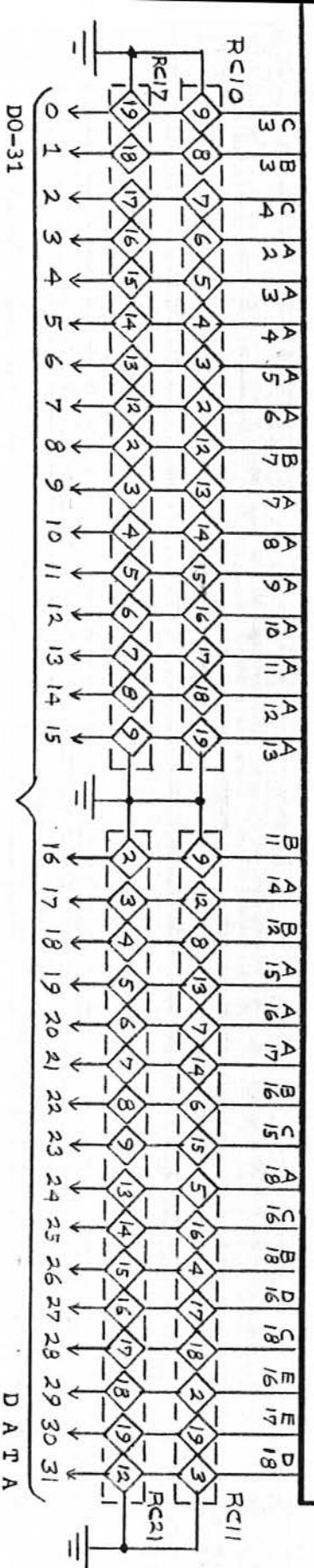
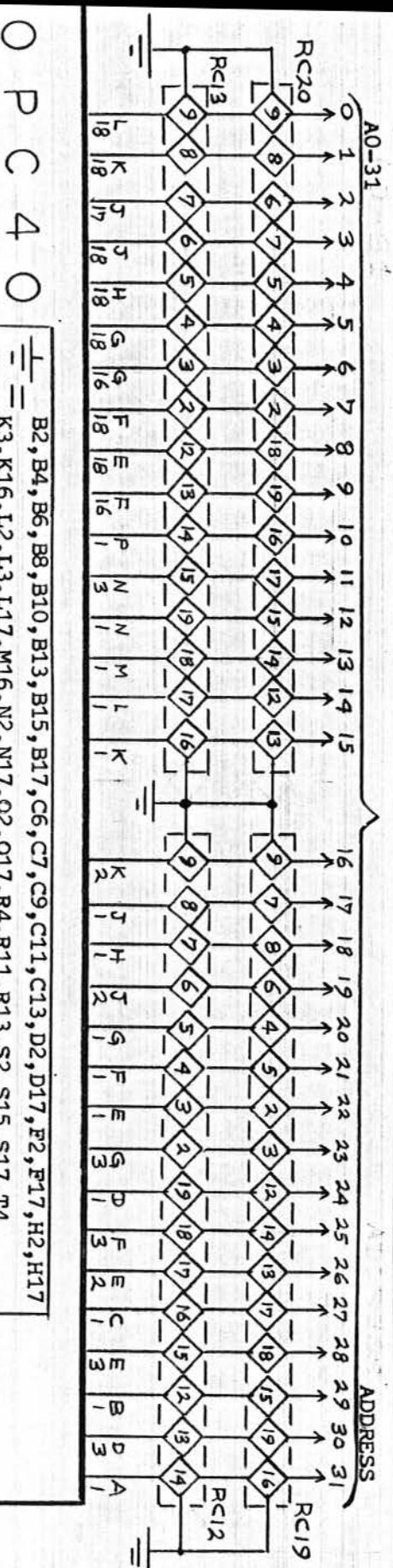
119, 123, 132, 139, 141, 146, 154, 156, 157, 159, 184, 195, 206, 208

SINCK

3 2 1 0  $\frac{1}{2} = 1, 8, 17, 18, 27, 34, 41, 50, 51, 59, 67, 74, 83, 84, 92, 99, 106, 116, 117, 126$ 

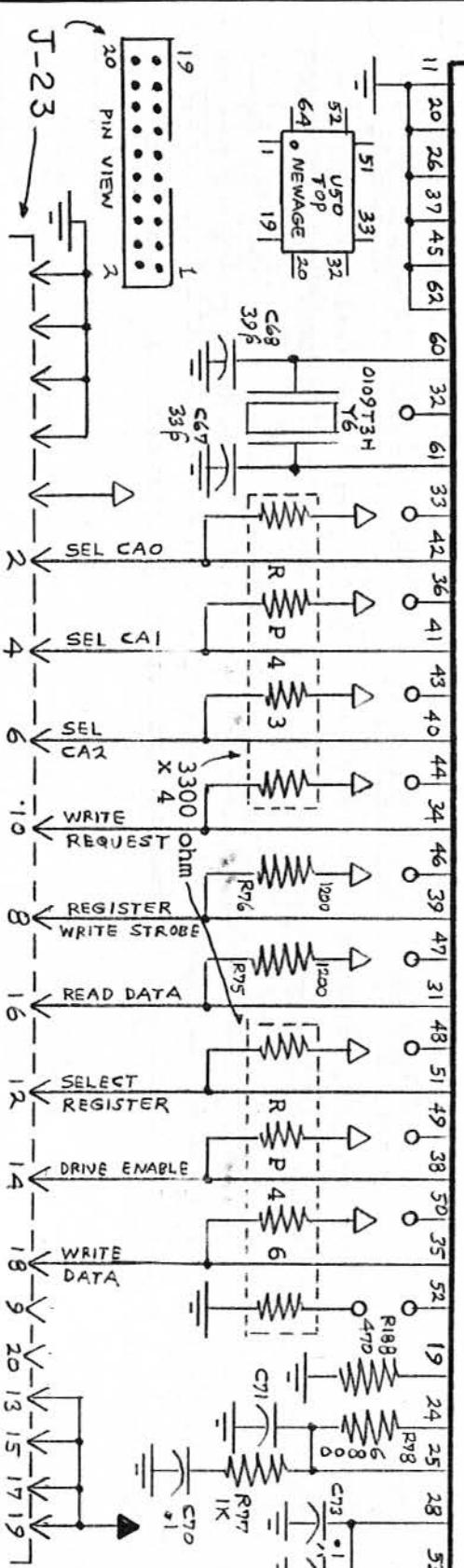
D0-31





part of BOARD: 820-0382-10 / 70 110 0702427 01/01

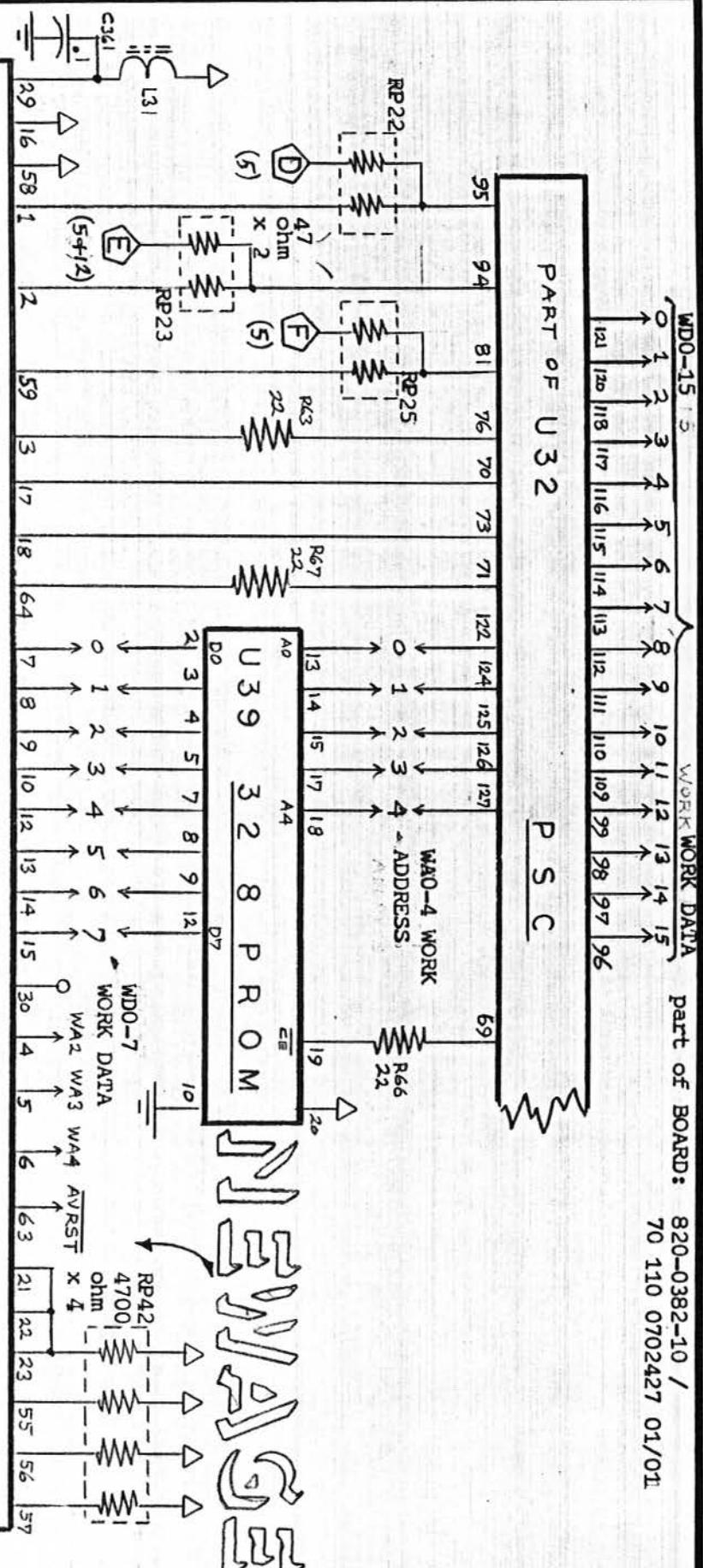
I.C.	T Y P E	$\Delta$	$\frac{1}{2}$
U48	74F38	14	7



U 5 0

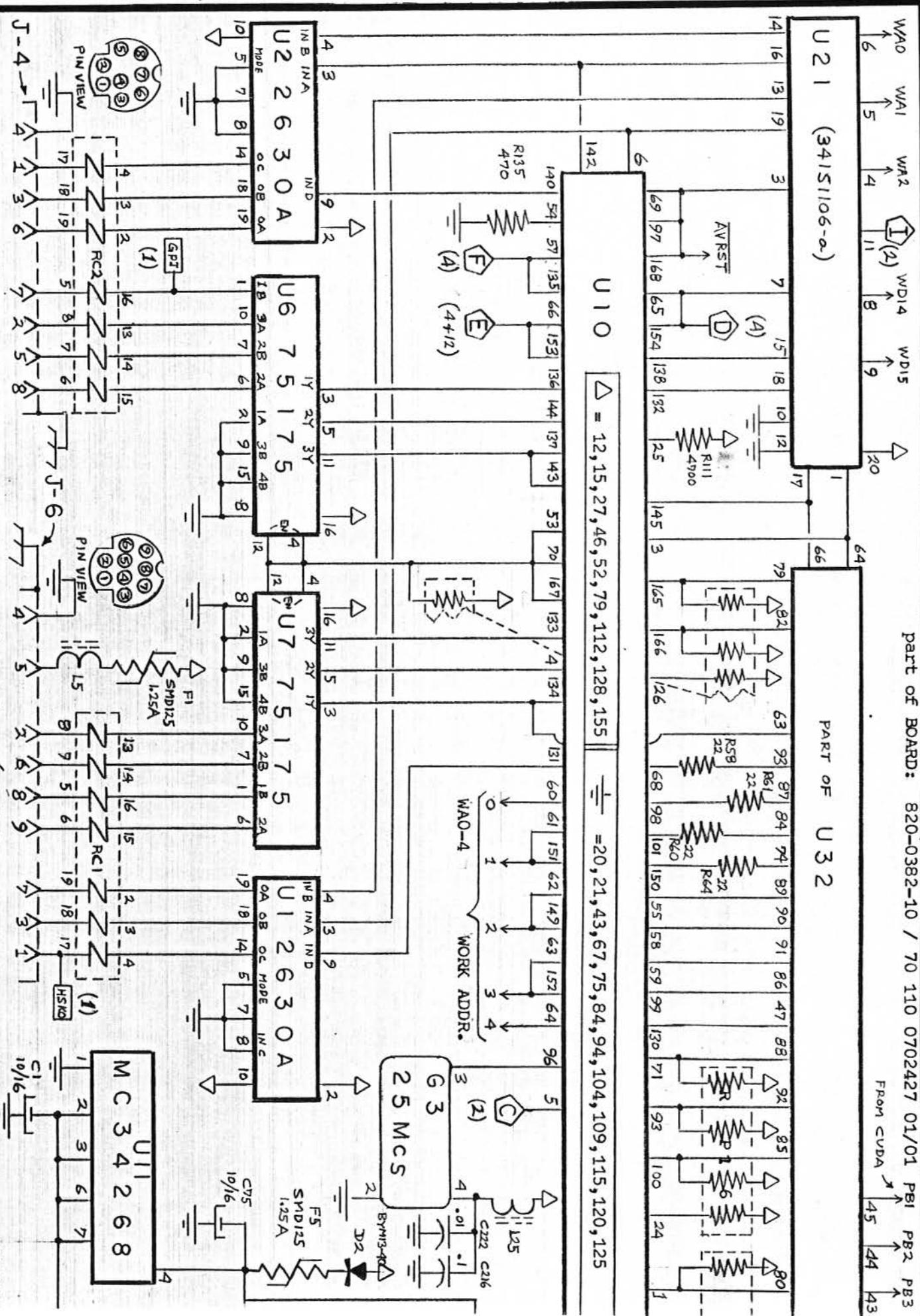
A P D 7 2 0 7 0

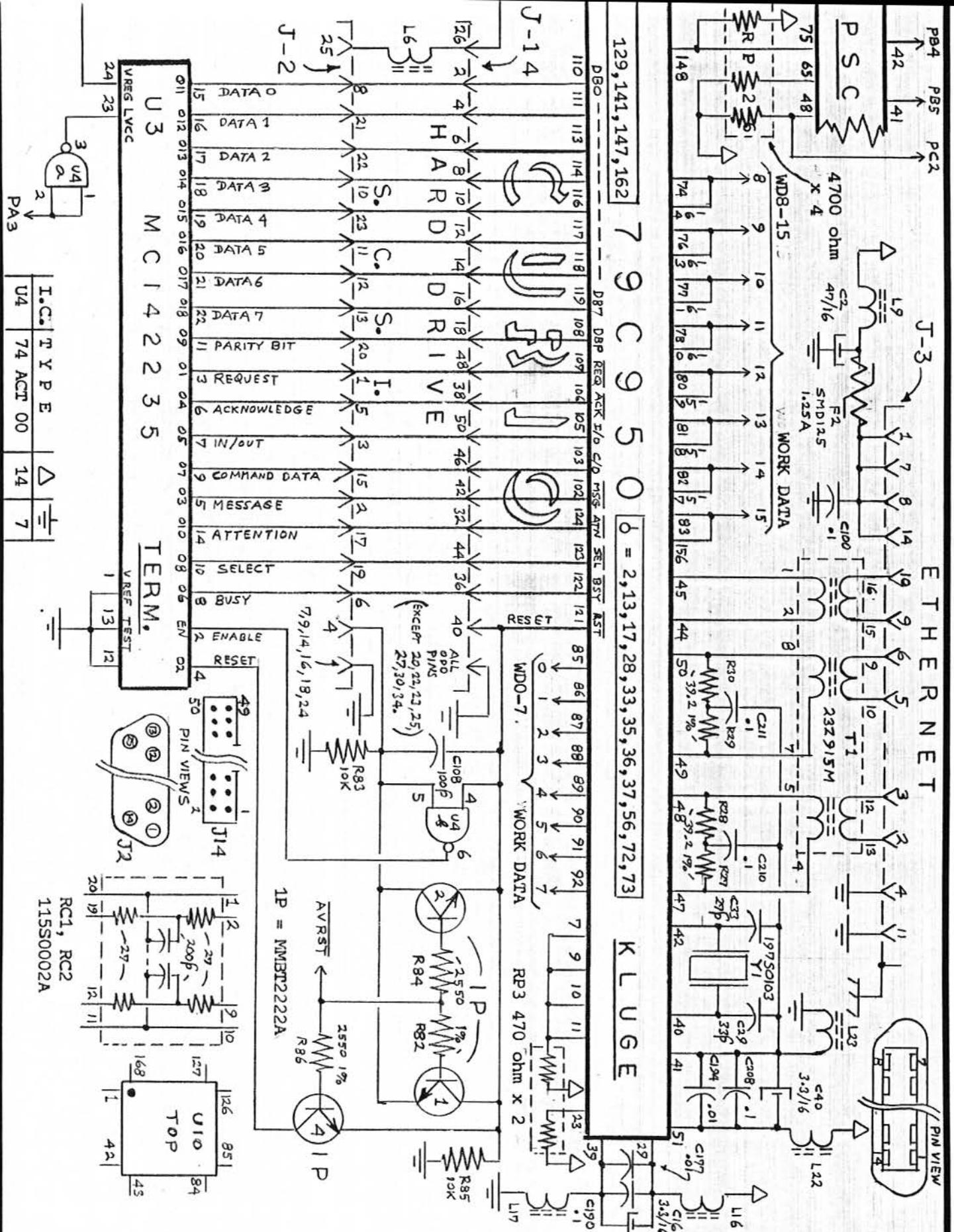
F D C

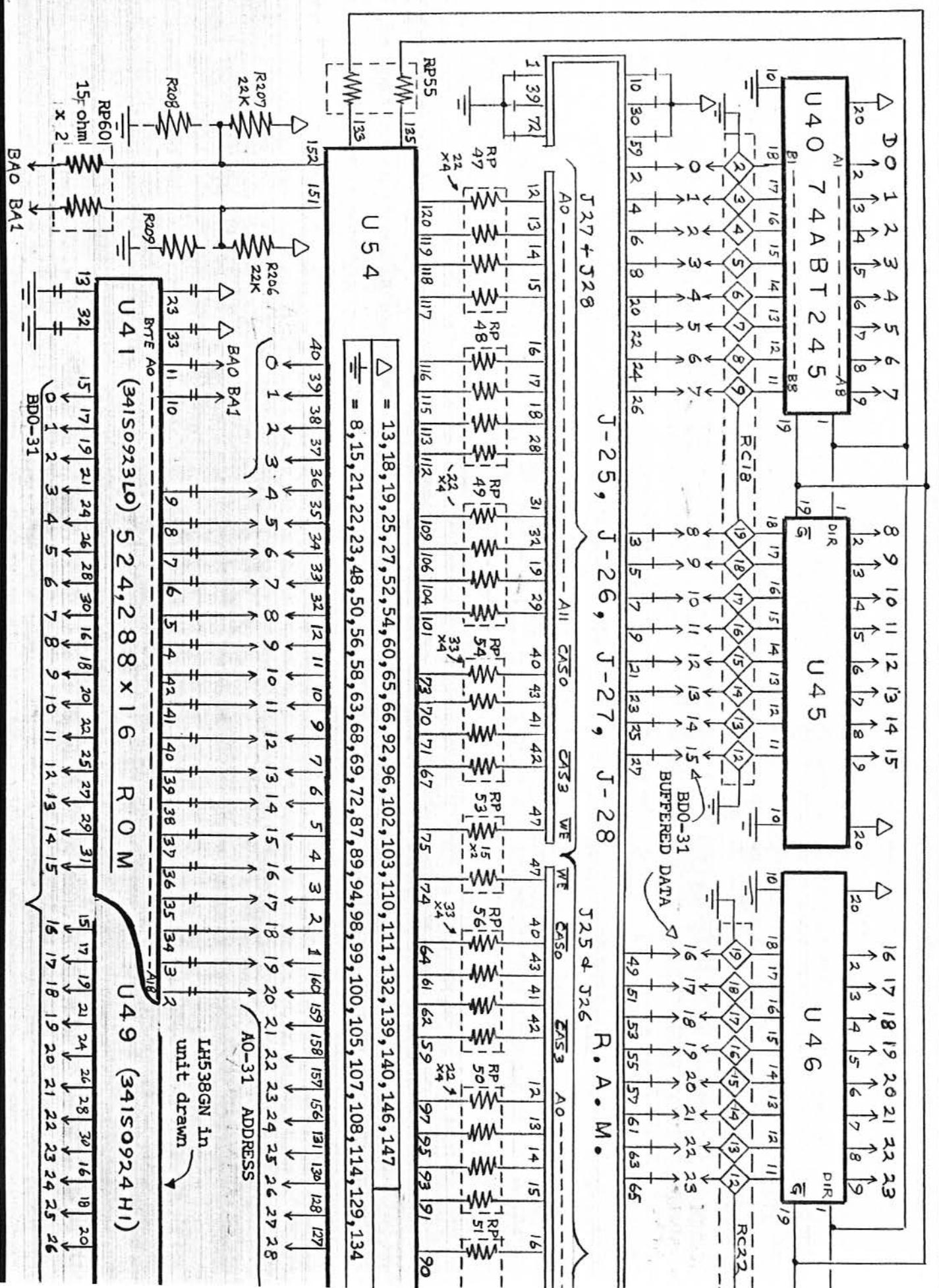


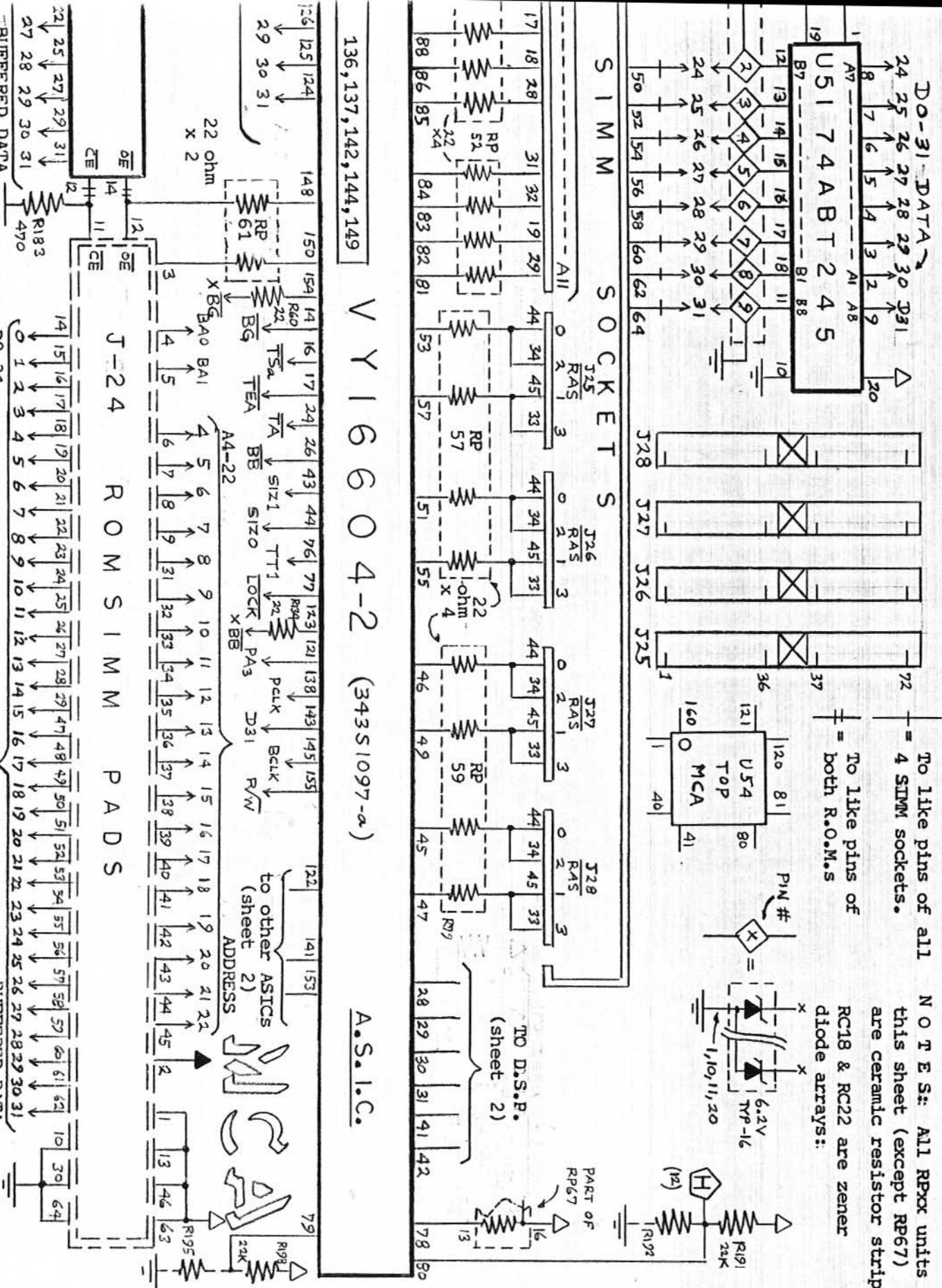
part of BOARD: 820-0382-10 / 70 110 0702427 01/01 PBI

FROM CUDA



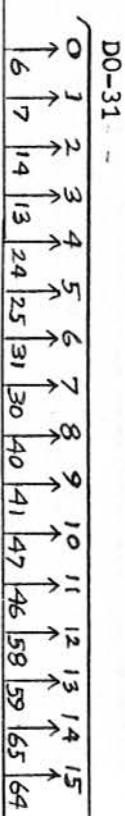






To like pins of both J19a  
= & J19b SIMM sockets.

+ = To like pins of all  
4 DRAM chips.

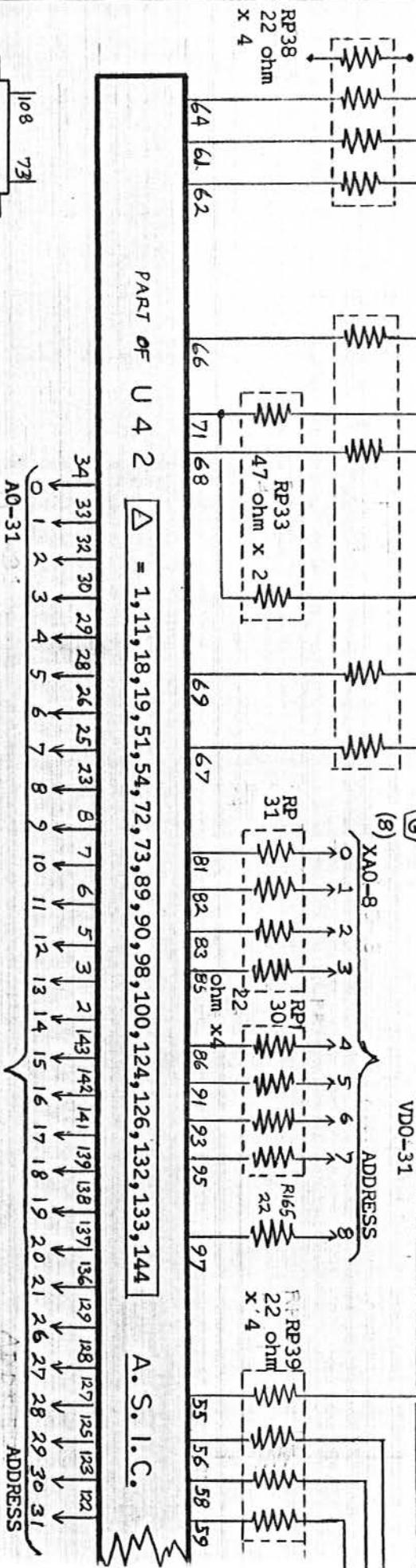


### J-1 9b R A M S | M M

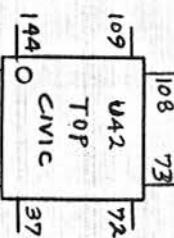


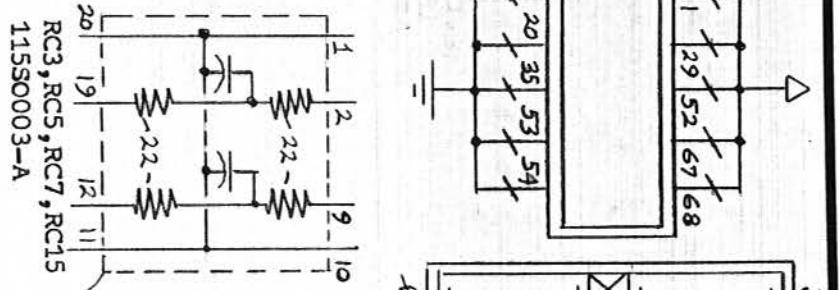
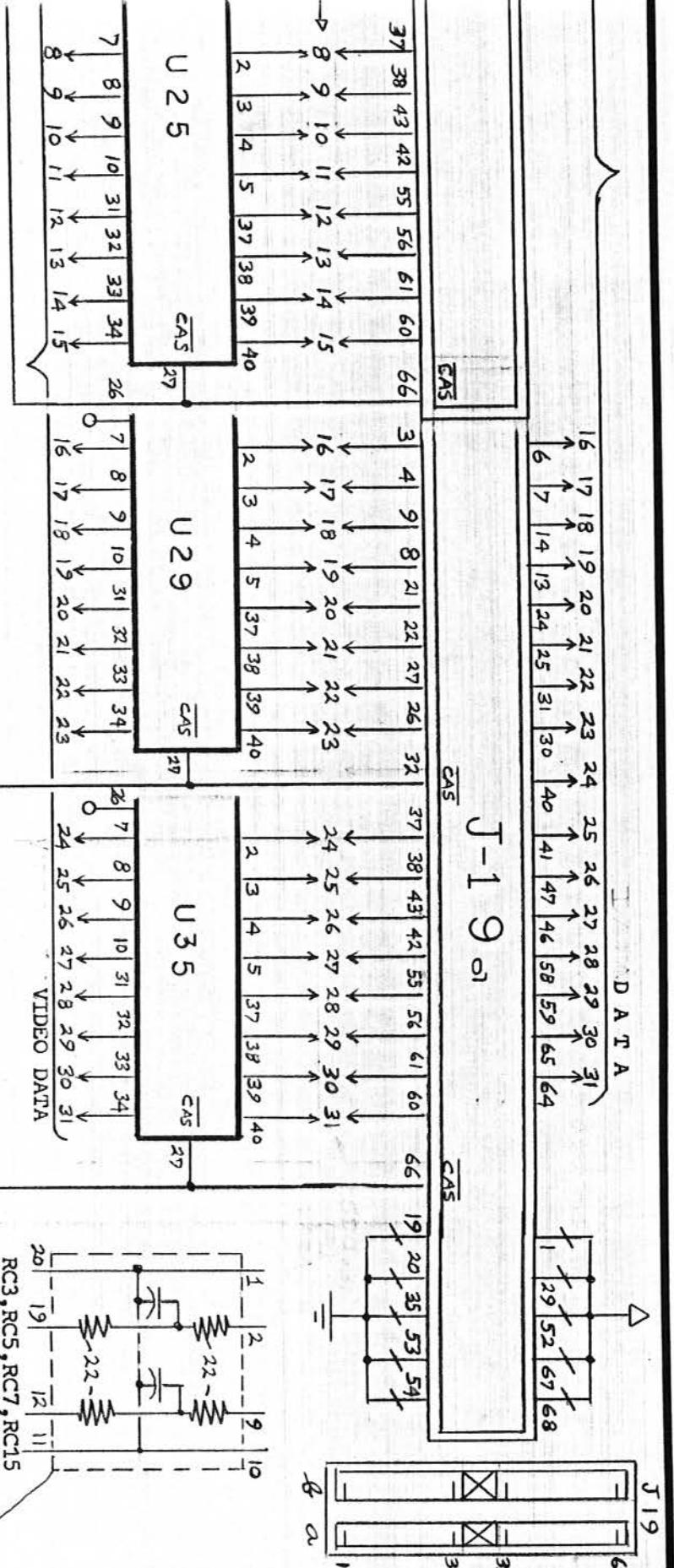
PART OF U 4 2     $\Delta = 1, 11, 18, 19, 51, 54, 72, 73, 89, 90, 98, 100, 124, 126, 132, 133, 144$

A. S. I.C.



NOTE: Dual Port D.R.A.M. chips are  
M5M482128AJ in unit drawn.





#### J19 SIMM & VIDEO D.R.A.M. & SUPPORTING CIRCUITRY

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12

To like pins of both J20a  
& J20b SIMM sockets.

+ = To like pins of all  
4 DRAM chips.

# J 20b RAMSIMM

RAS DSF    WE    SE    SC    OE

A0

CAS

11 2 10 28 44 62 12 45 36 5 22 39 57 15 16 17 18 33 34 48 49 50

YAO-8

ADDRESS

SD0-31(B)

DATA

SD00

SD07

VIDEO DATA  
16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31  
6 7 14 13 24 25 31 30 40 41 47 46 58 59 65 64

J20

68

$\overline{C_{AS}}$

$\overline{C_{AS}}$

$\overline{C_{AS}}$

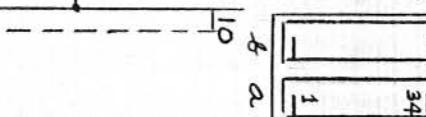
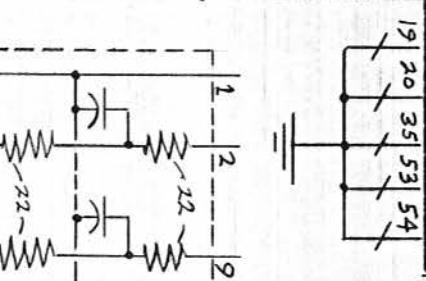
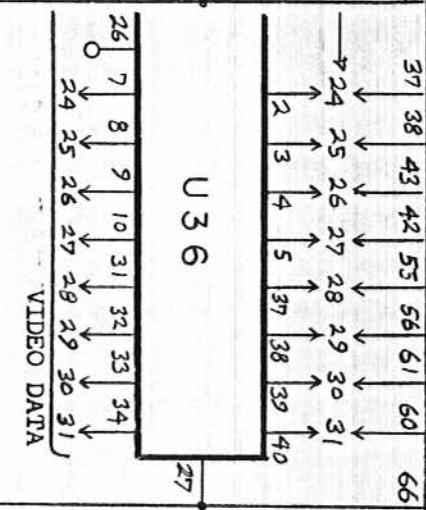
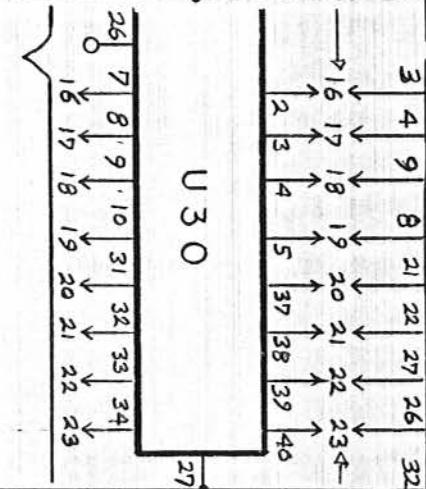
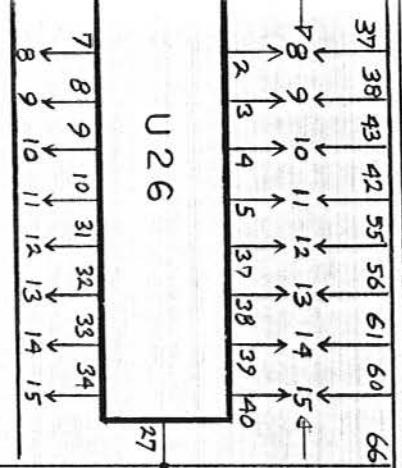
$\overline{C_{AS}}$

$\overline{C_{AS}}$

J-20 a1

$\overline{C_{AS}}$

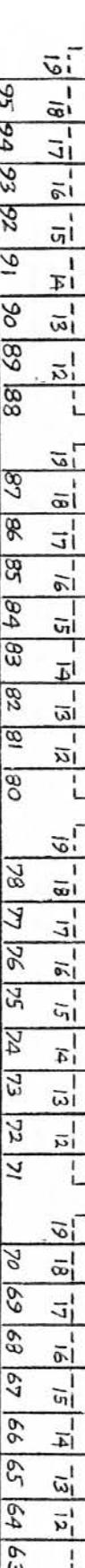
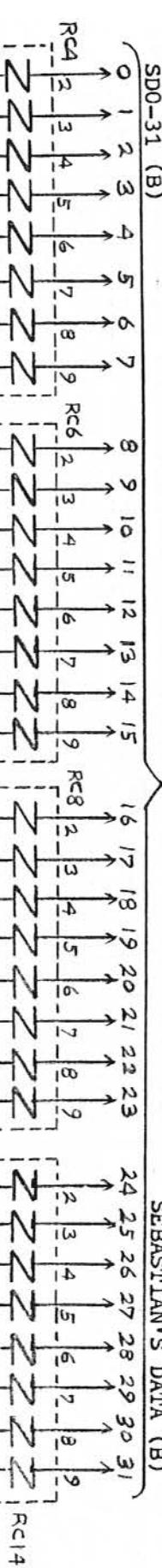
$\overline{C_{AS}}$



RC4, RC6, RCB, RC14  
115S003-A

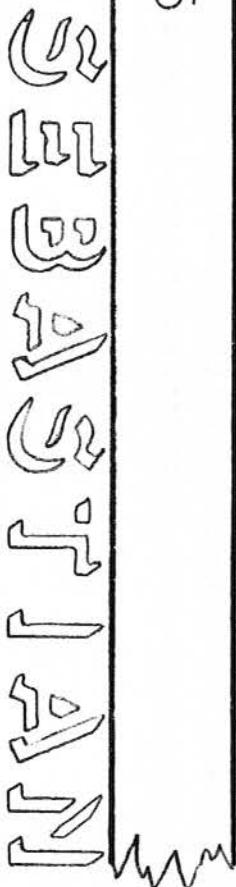
SD0-31 (B)

SEBASTIAN'S DATA (B)



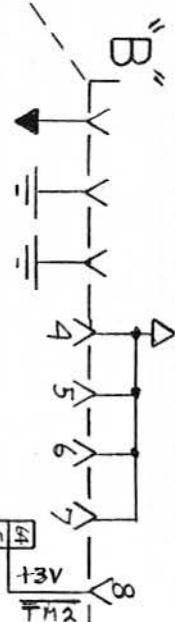
PART OF U16 B T 9845

80 51  
81 U16  
82 TOP  
100 SEBASTIAN  
31 30



NOTE S: : : All "A" pins parallel to J-15,J16,J17 except pins: 26,27,31.  
 All "B" pins parallel to J-15,J16,J17 except pins: 24,25.  
 All "C" pins parallel to J-15,J16,J17 except pins: 26,27,32.

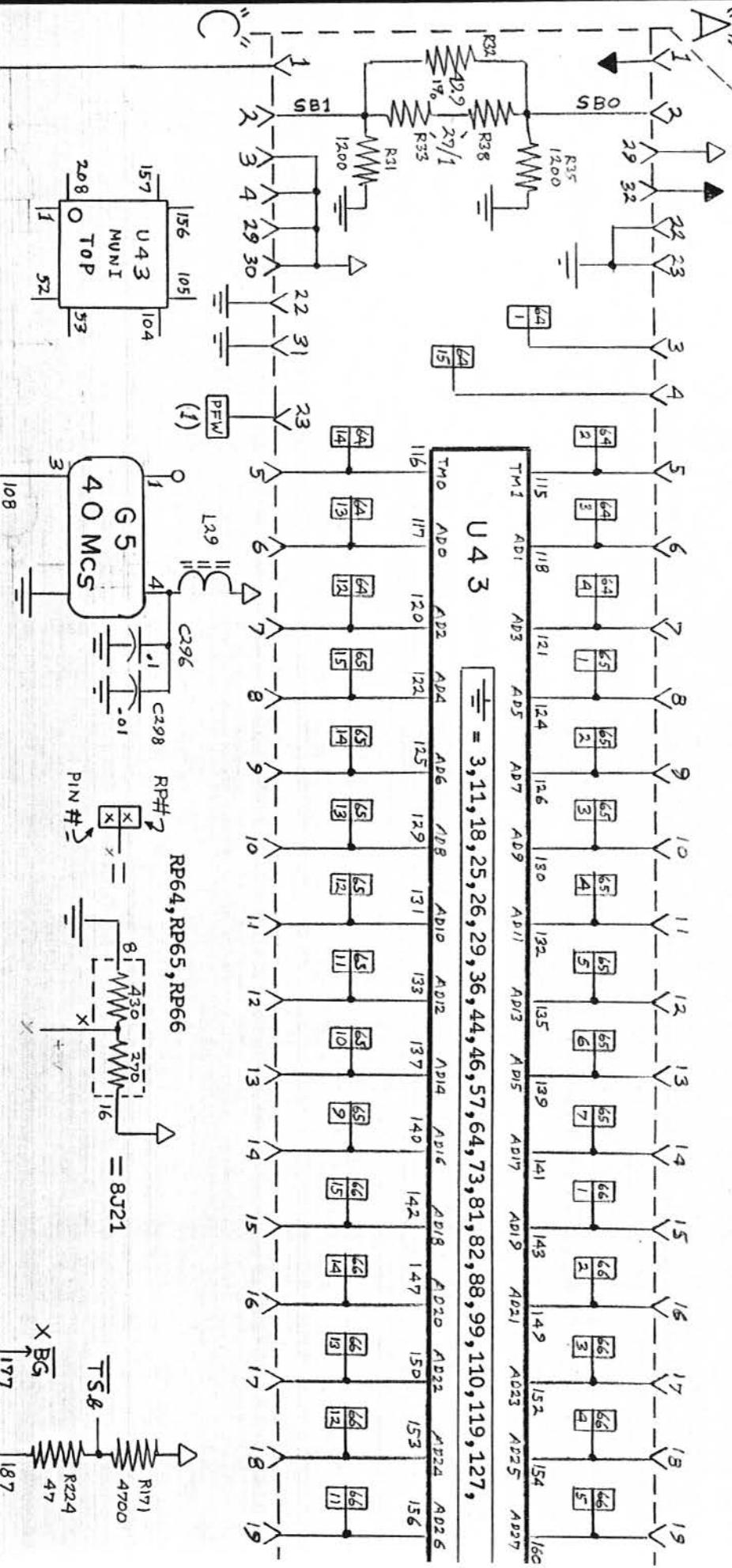
# N U B U S - S O C K E T S



**U 4 3**

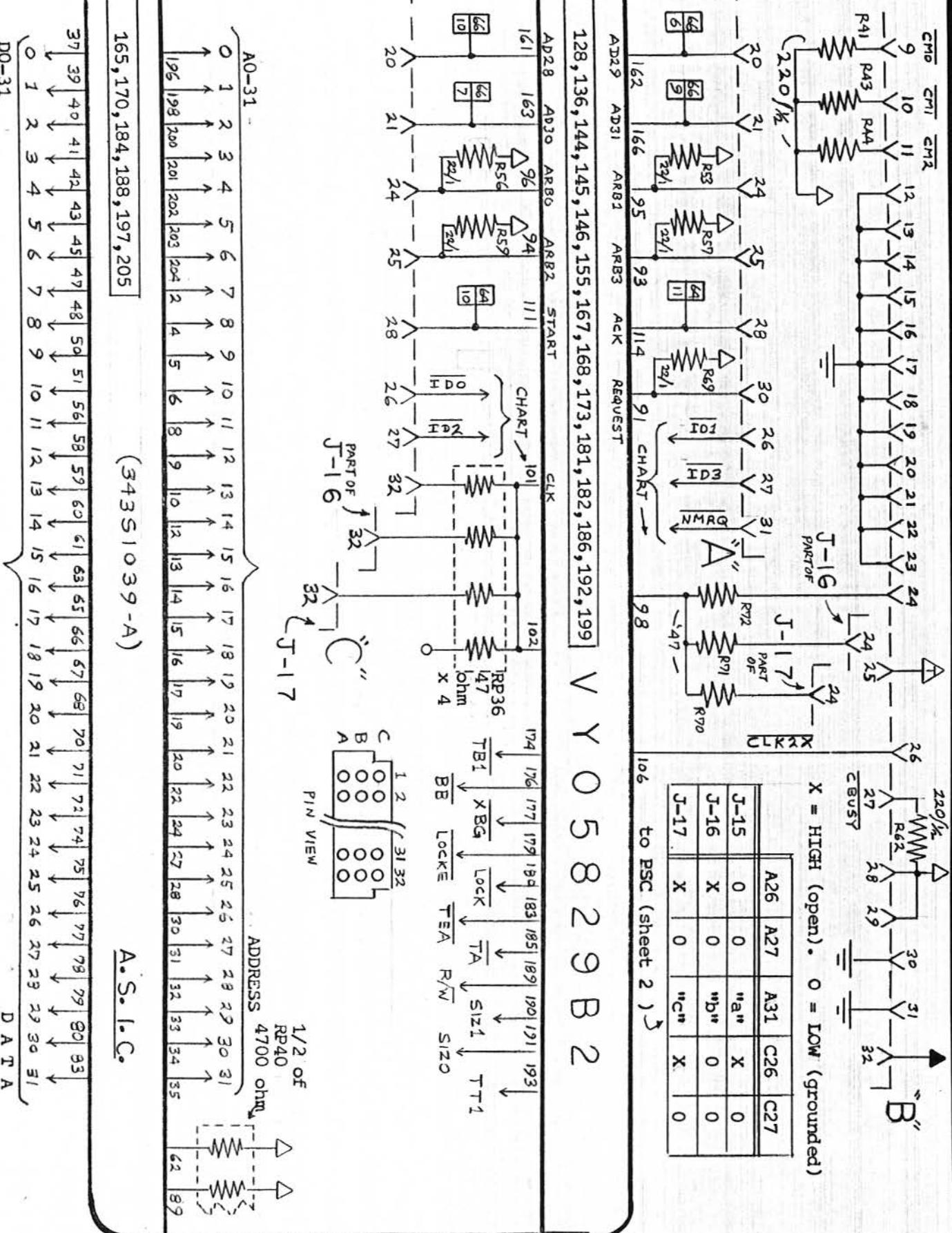
$\overline{\overline{=}} = 3,11,18,25,26,29,36,44,46,57,64,73,81,82,88,99,110,119,127,$

T <sub>1</sub> M <sub>0</sub>	A <sub>D1</sub>	A <sub>D2</sub>	A <sub>D3</sub>	A <sub>D4</sub>	A <sub>D5</sub>	A <sub>D6</sub>	A <sub>D7</sub>	A <sub>D8</sub>	A <sub>D9</sub>	A <sub>D10</sub>	A <sub>D11</sub>	A <sub>D12</sub>	A <sub>D13</sub>	A <sub>D14</sub>	A <sub>D15</sub>	A <sub>D16</sub>	A <sub>D17</sub>	A <sub>D18</sub>	A <sub>D19</sub>	A <sub>D20</sub>	A <sub>D21</sub>	A <sub>D22</sub>	A <sub>D23</sub>	A <sub>D24</sub>	A <sub>D25</sub>	A <sub>D26</sub>
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$\Delta = 7,21,23,38,49,55,69,84,85,86,112,113,123,134,138,148,151,164,$

R184  
68  
to other ASICS  
(sheet 2)



N U B U S SOCKETS & "MUNI" A.S.I.C.

sheet 9 of 12

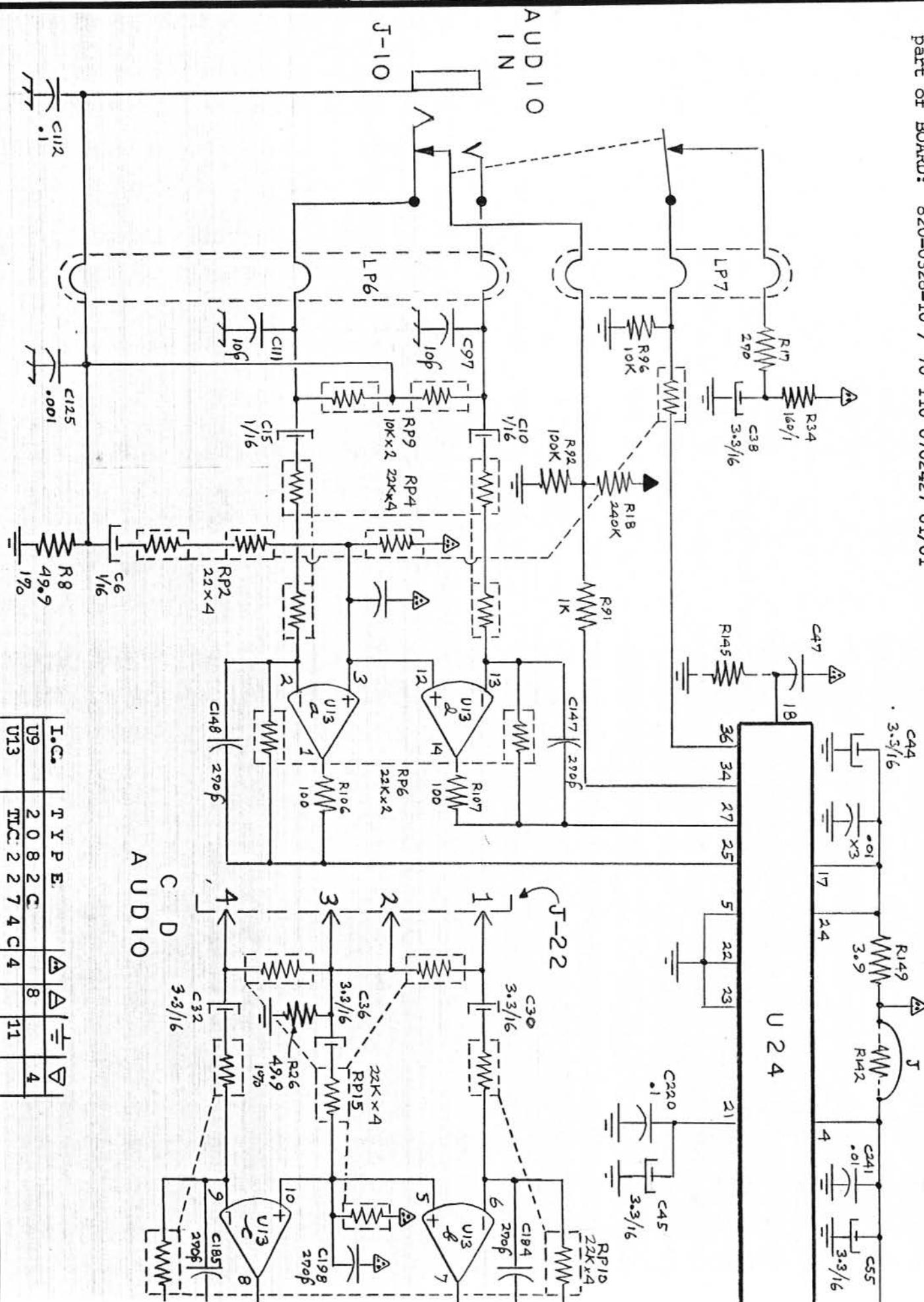
## A P P L E

MacINTOSH "QUADRA" 840AV

## **COMPUTER LOGIC BOARD**

8/27/94-7

part of BOARD: 820-0328-10 / 70 110 0702427 01/01

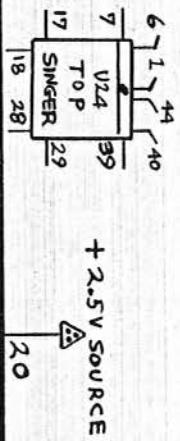


A S C 0 2 3 0 0 (33759622)

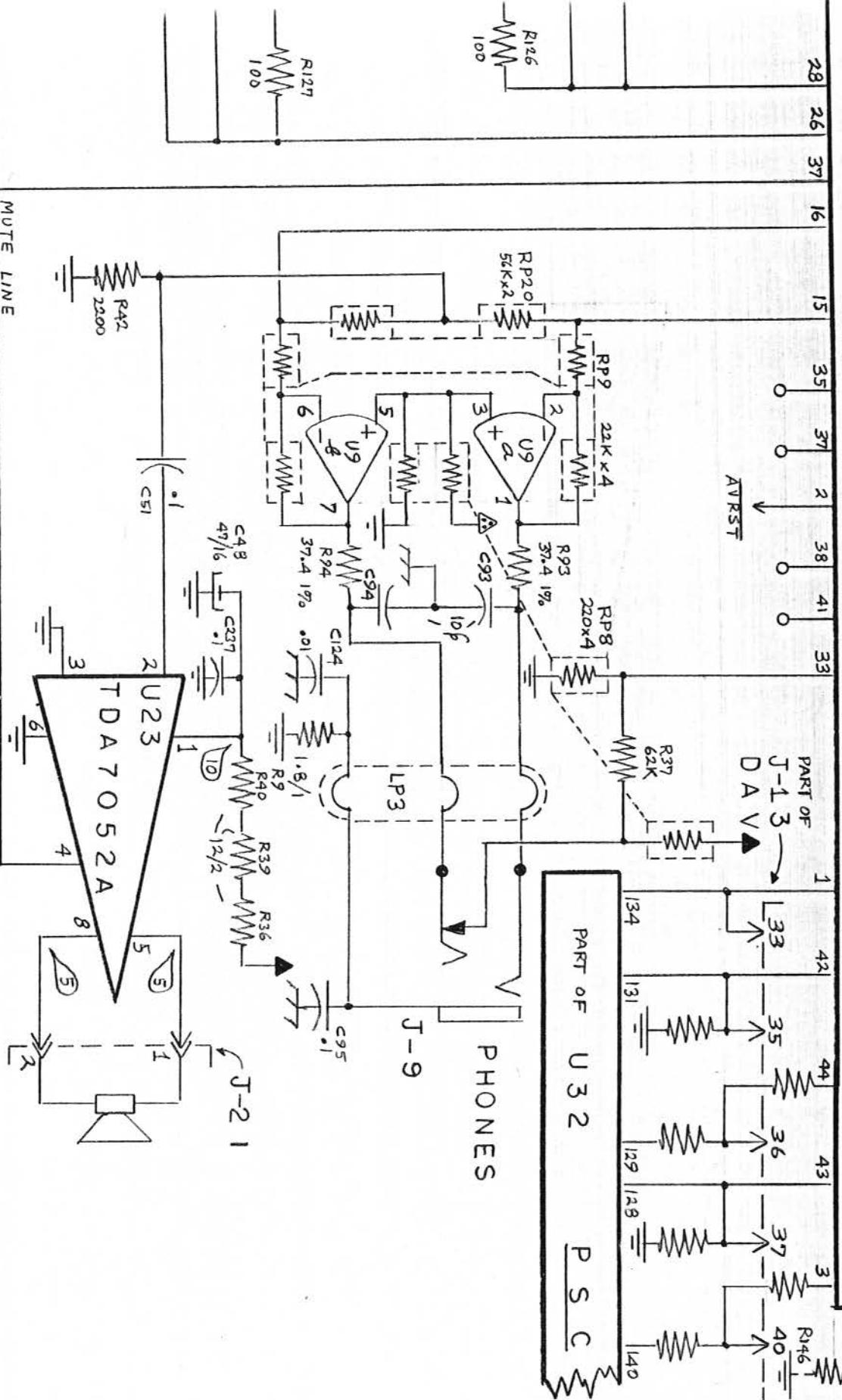
A U D I O

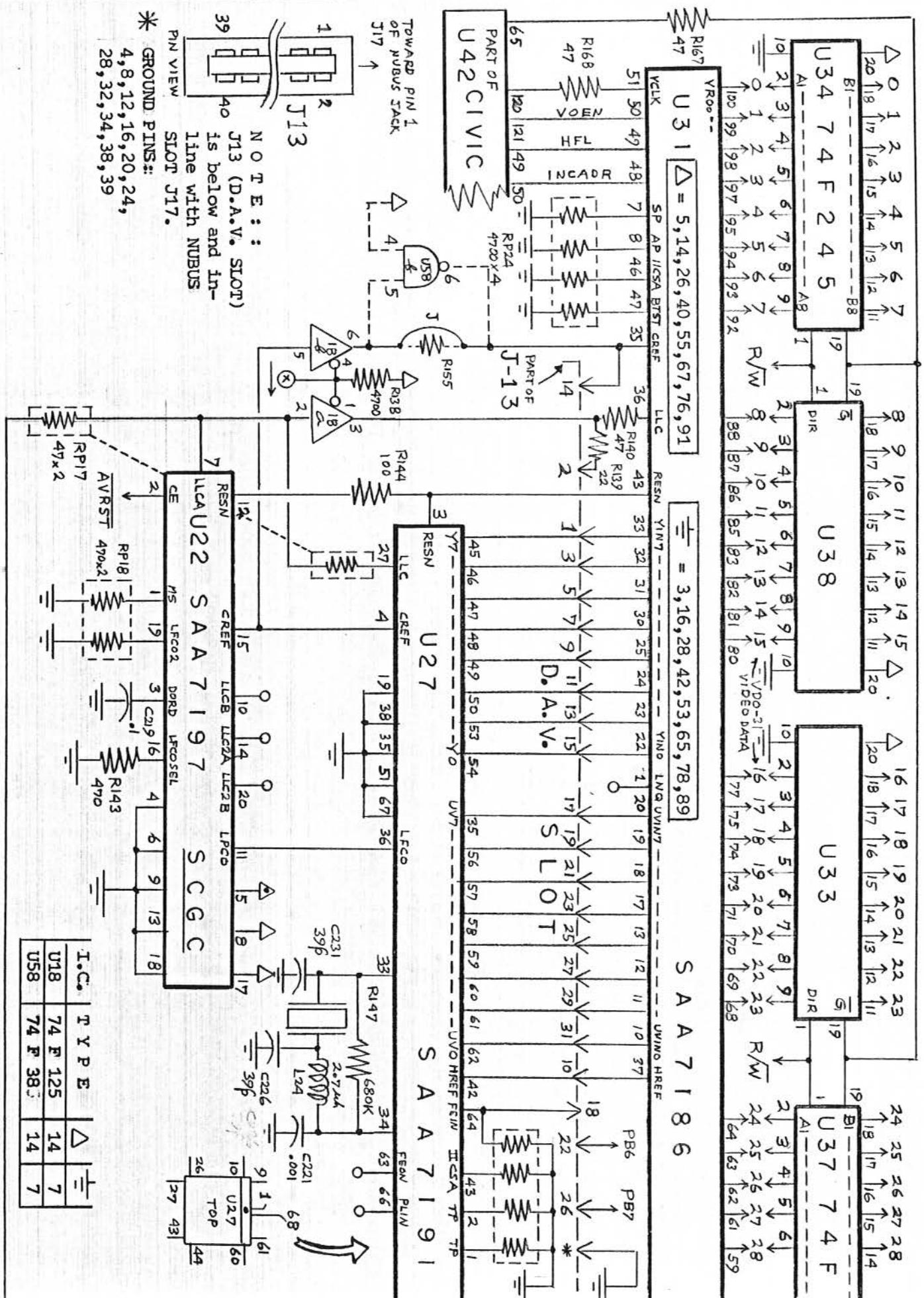
P H O N E S

C<sub>46</sub>

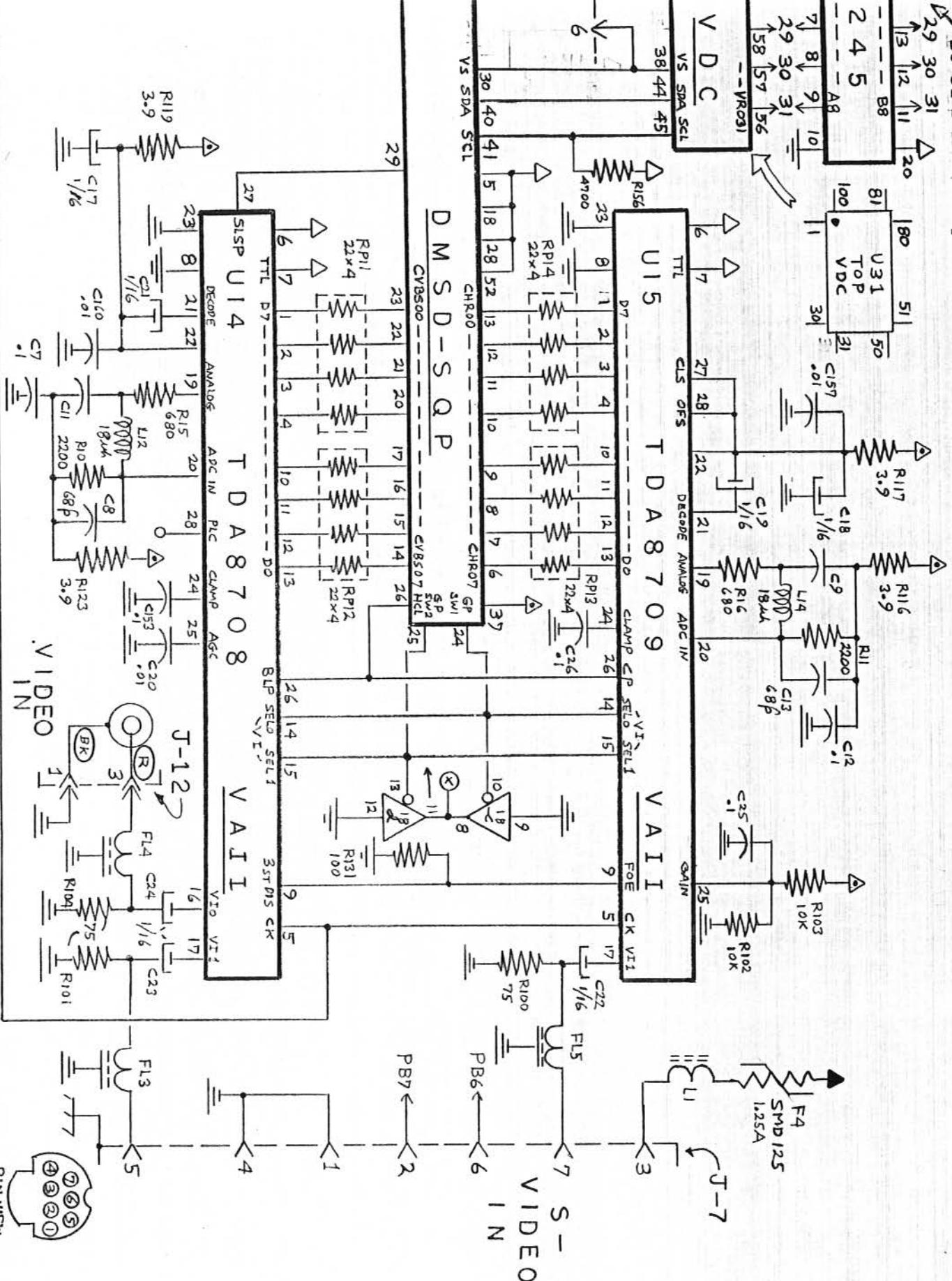


MUTE LINE





D0-31 DATA part of BOARD: 820-0382-10 / 70 110 0702427 01/01



## VIDEO INPUTS & DIGITAL VIDEO SIGNAL PROCESSING

sheet 11 of 12

APPLE

MacINTOSH "QUADRA" 840AV

## **COMPUTER LOGIC BOARD**

9/9/94- 

