

BOARD NUMBER: 820-0228-A

## POWER SUPPLY & READ ONLY MEMORY SECTIONS

sheet 1 of 6


APPLE


#### M5000 "MacINTOSH II"


# **COMPUTER LOGIC BOARD**

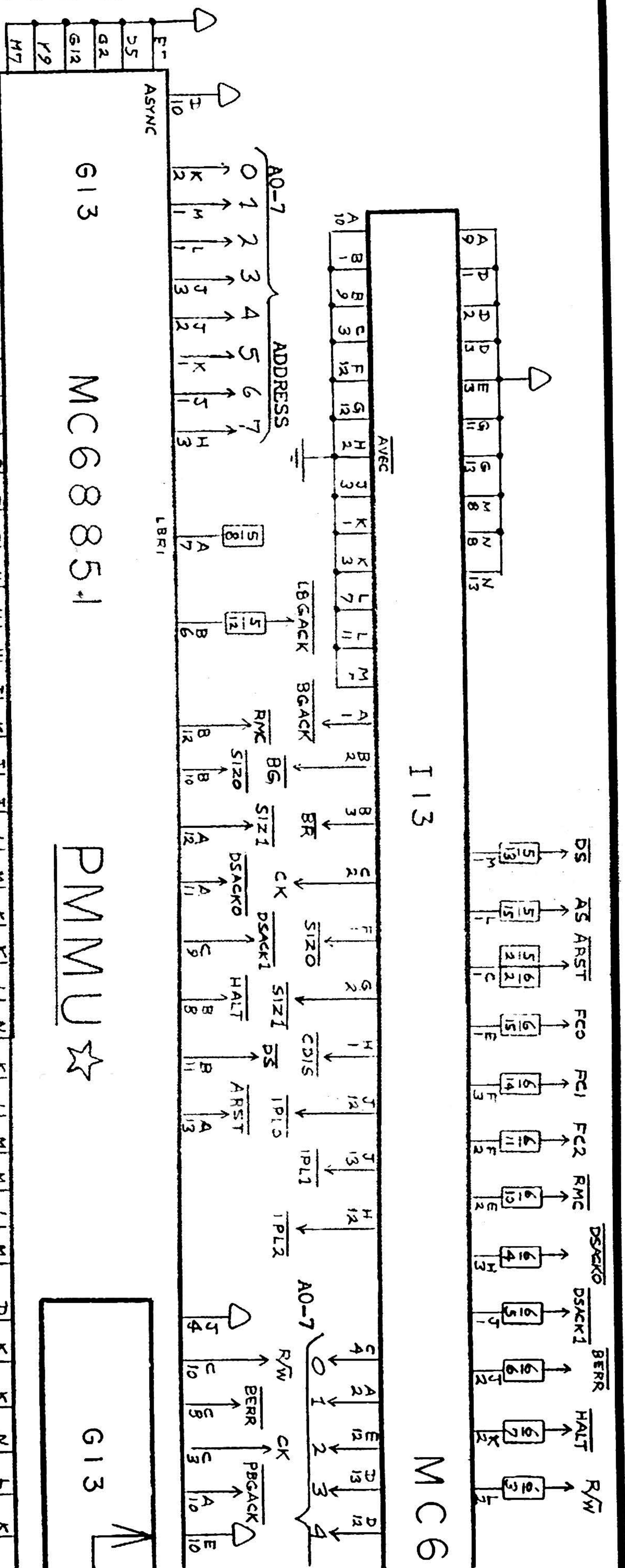
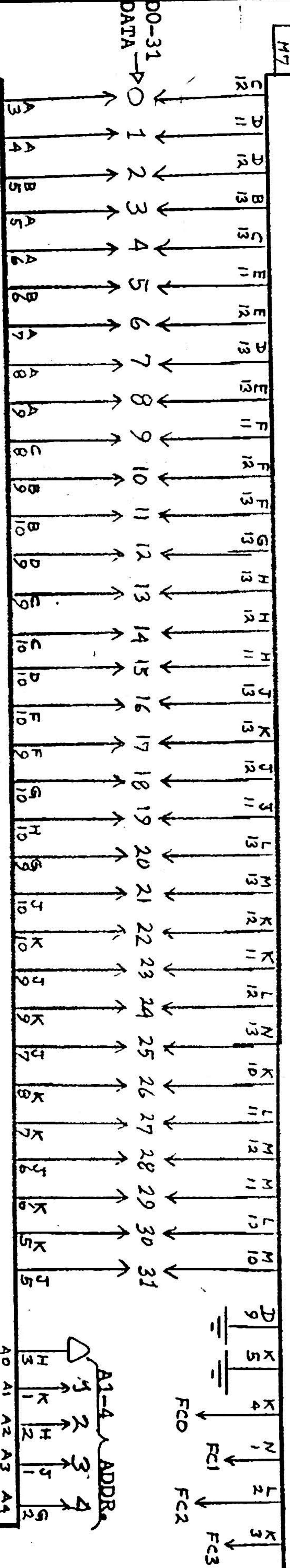
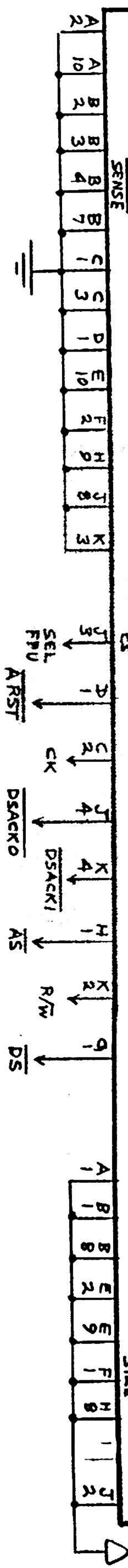
820-0228-A

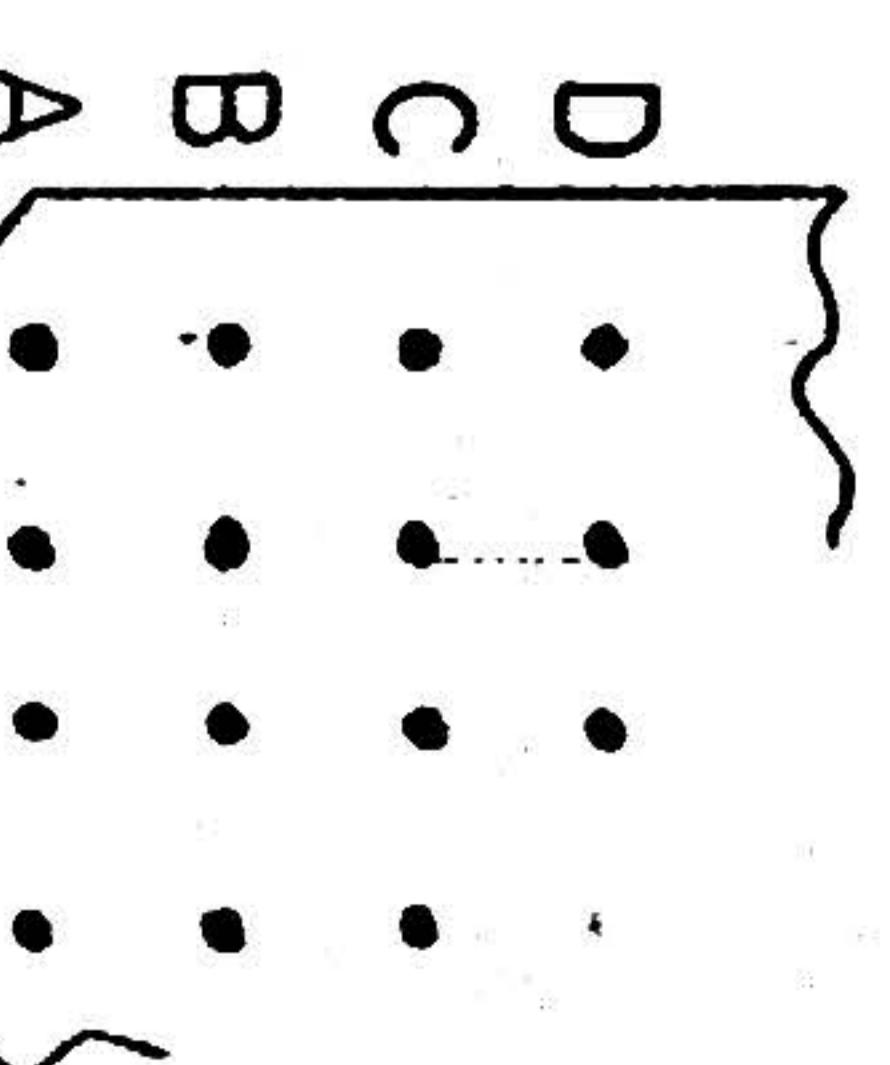
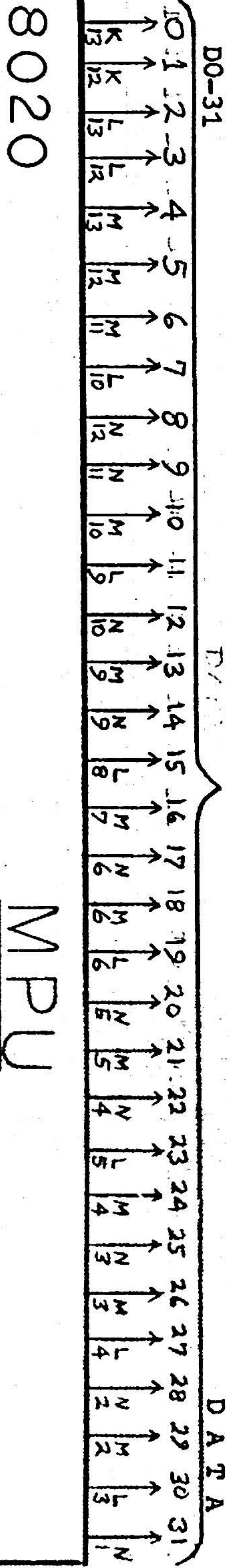
8/28/89- 



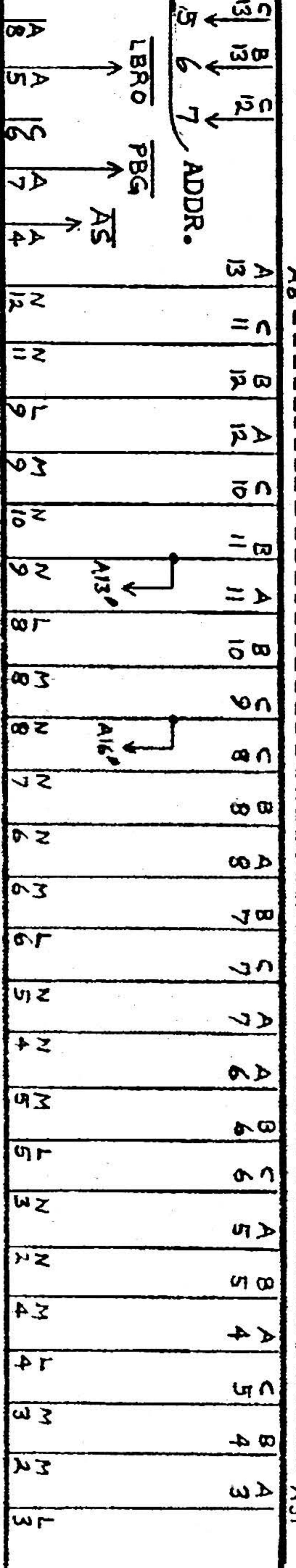




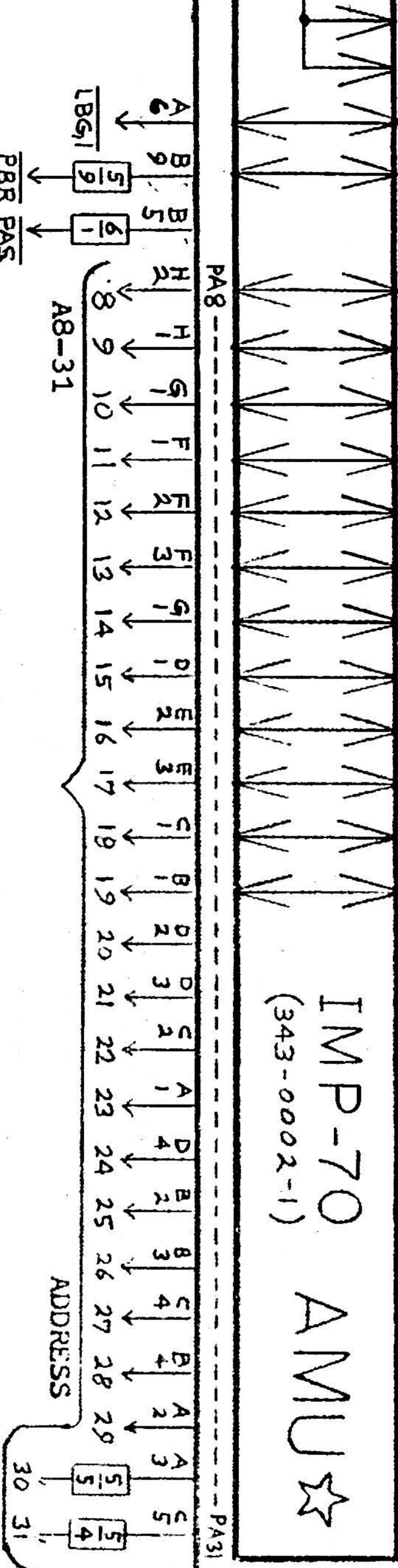




**8020**

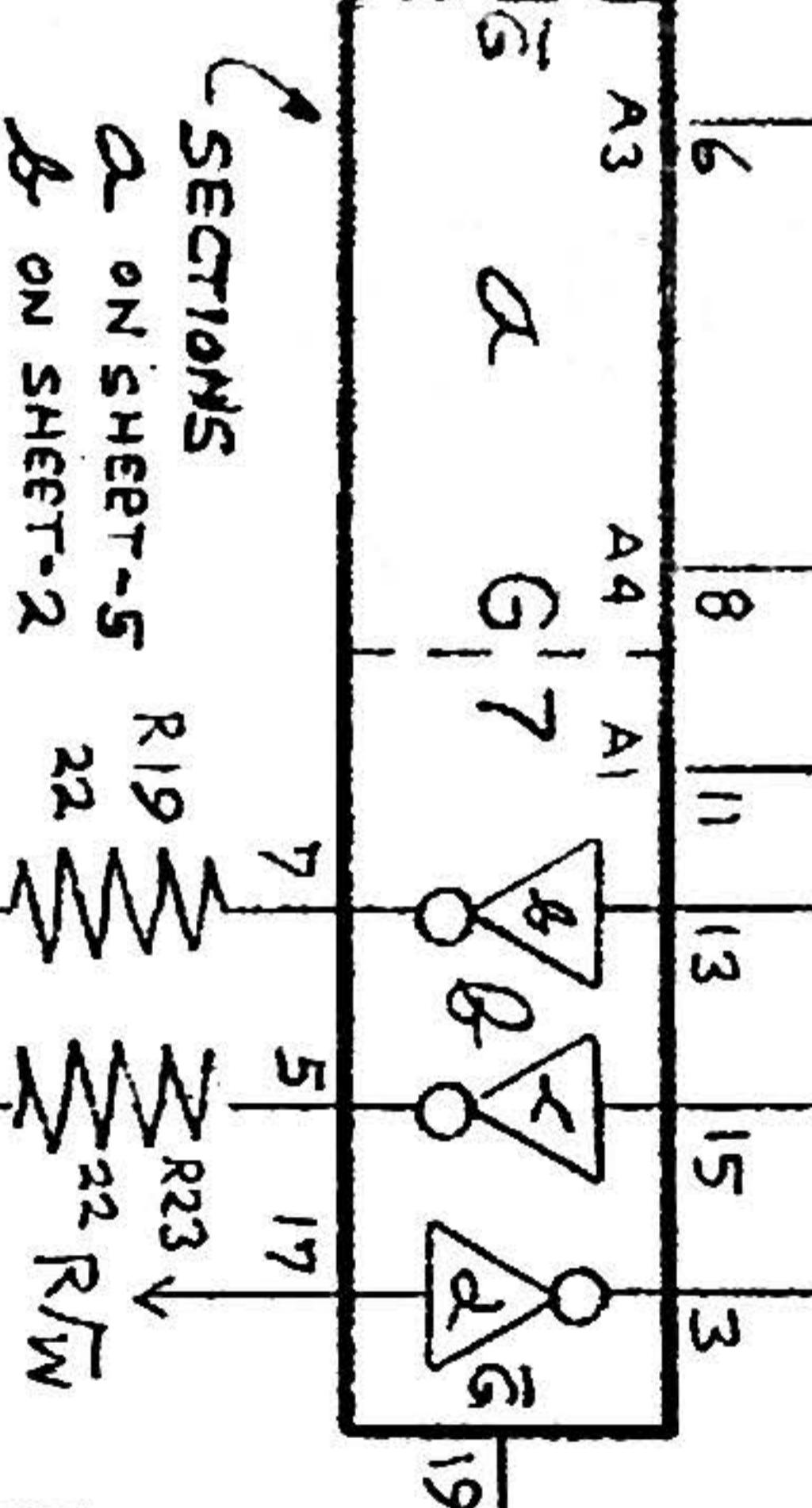
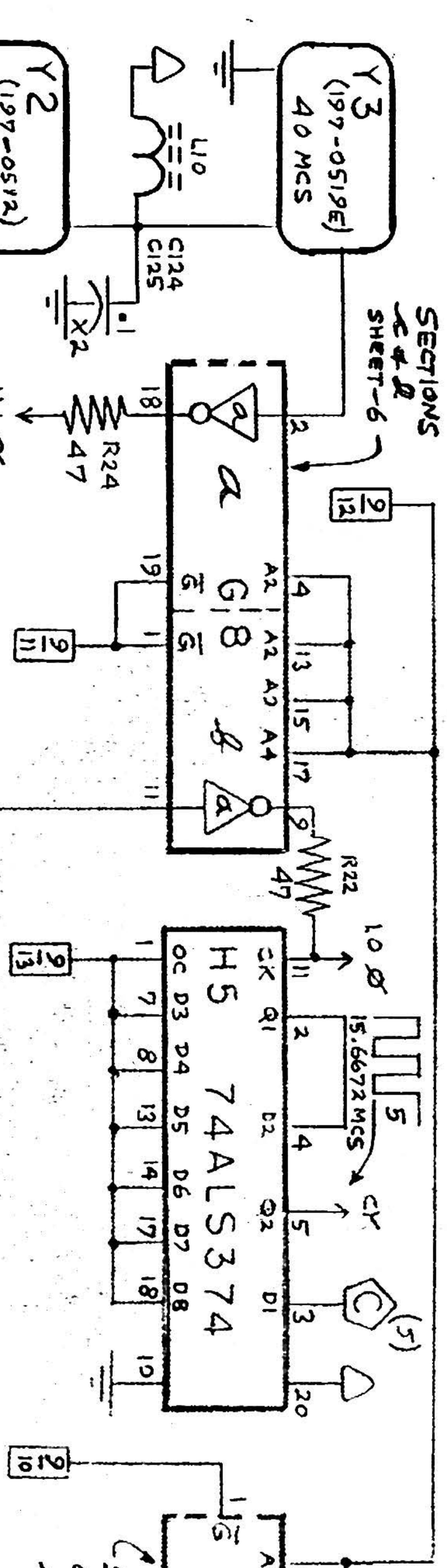
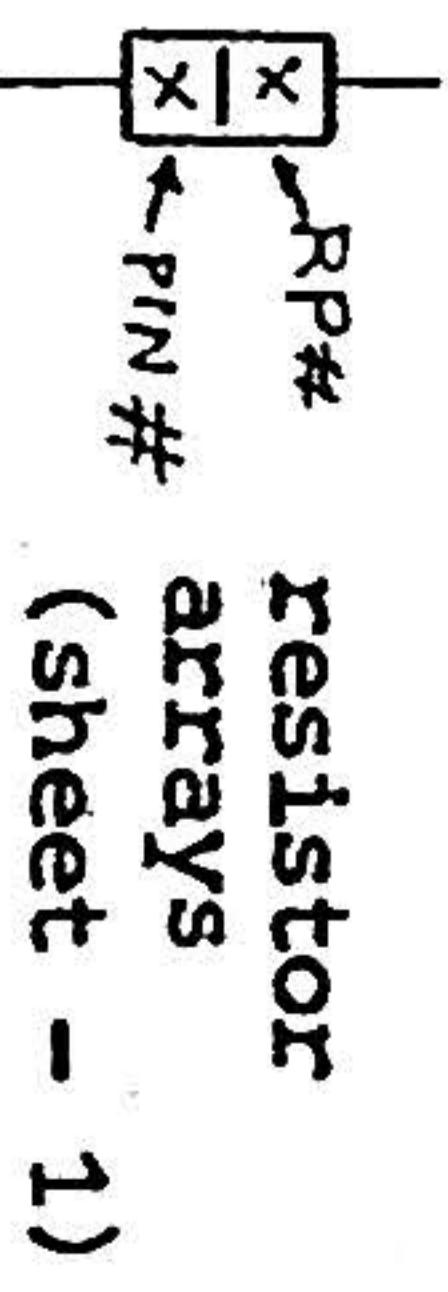


**IMP-70 AMU**  $\star$   
(343-0002-1)




$\star$  A.M.U. is an alternate hybrid chip and jumper array. If P.M.M.U. is used, outer ring applies. If A.M.U. is used, inner ring applies and jumpers are shown as:  $\leftrightarrow$

P.G.A. CHIP  
(pin view)  
(sheet - 1)



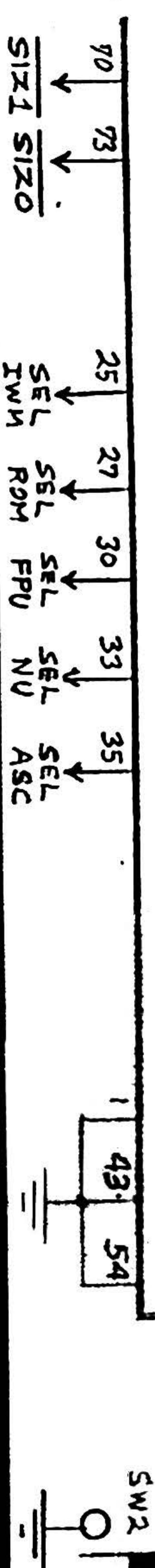
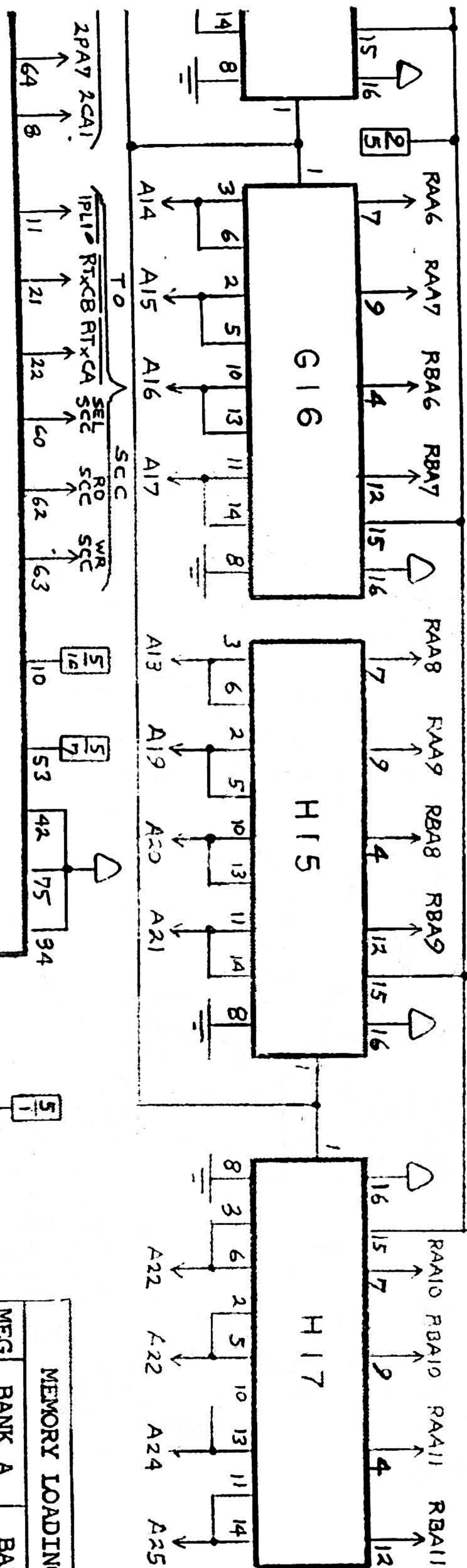
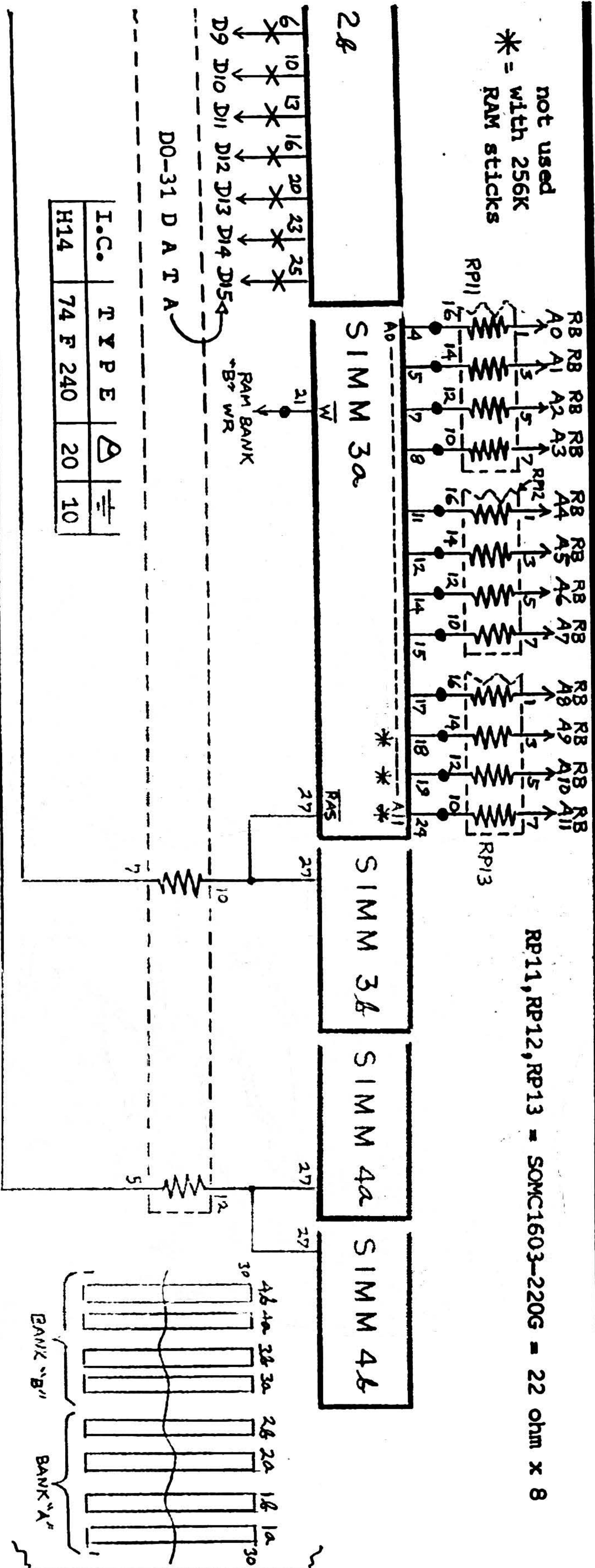
RAM BANK "A"  
WR

RAM BANK "B"  
WR



\* = not used  
with 256K RAM sticks

RP11, RP12, RP13 = SMC1603-220G = 22 ohm x 8



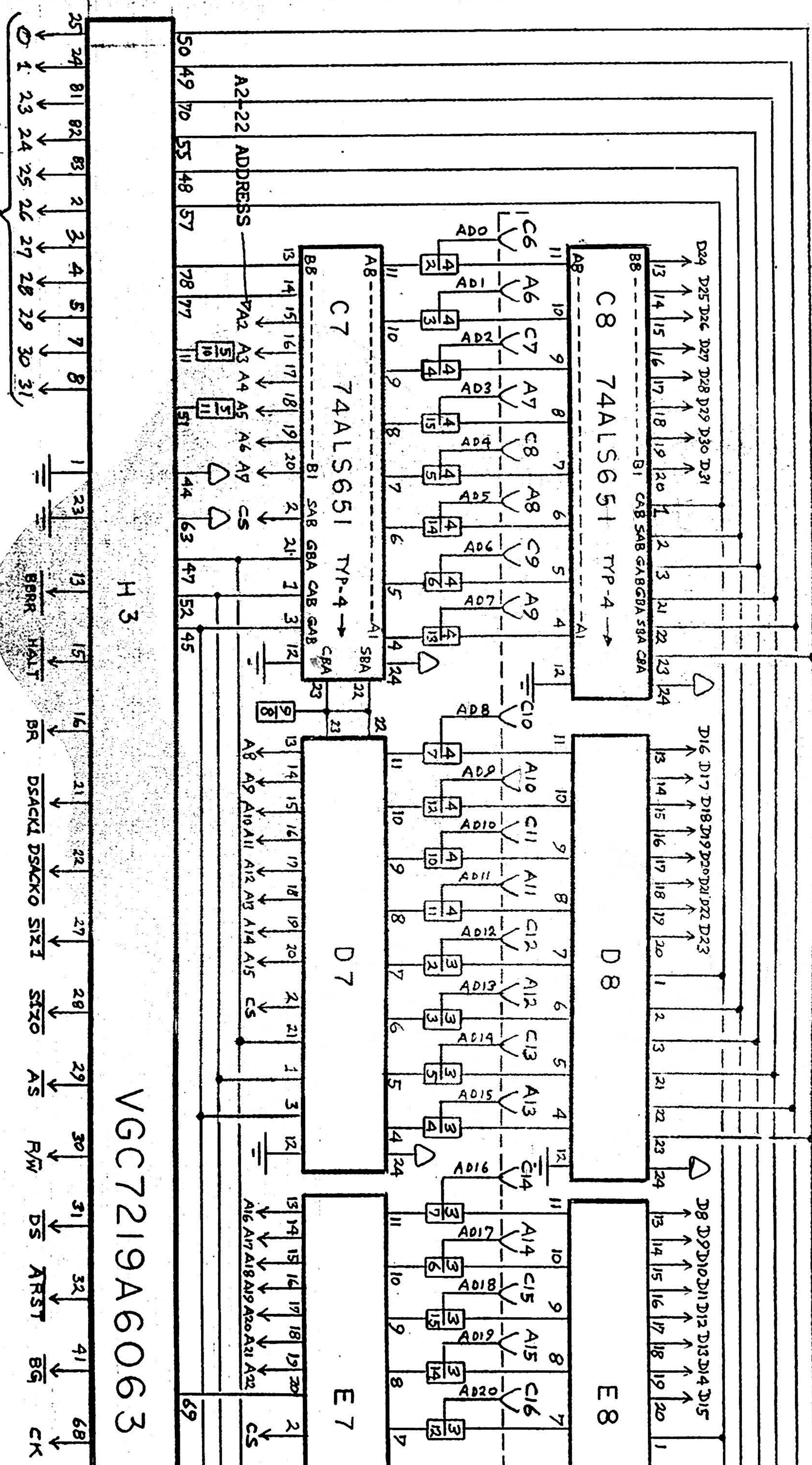
## MEMORY LOADING:

MEG	BANK A	BANK B
1	4	
2	4	
4	4	256K
5	4	256K
8	4	1M
1M		
4	4	256K
4	4	256K
1M		


J10-J15

B BANK

all pins same for  
all six sockets



36064-27C6V



## NuBUS SOCKETS & SUPPORTING CIRCUITRY SECTION

sheet 6 of 6

APPLE

M5000 "MacINTOSH II"

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