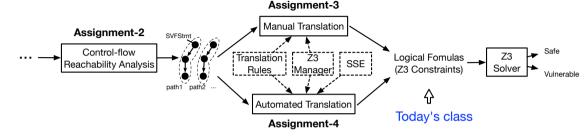
Code Verification and Predicate Logic

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Today's class

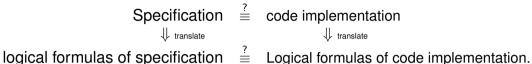


- In this week, we will learn the (first-order) logical formulas, which are the outputs translated from code for verification.
- We will take a look at manual and automated translation from code to formulas for verification from next class.

Formal Verification For Code

Specification $\stackrel{?}{\equiv}$ code implementation

Formal Verification For Code



Formal Verification For Code

Specification
$$\stackrel{?}{\equiv}$$
 code implementation \downarrow translate

logical formulas of specification $\stackrel{?}{\equiv}$ Logical formulas of code implementation.

- Proving the correctness of your code given a specification (or spec) using formal methods of mathematics
- Make the connection between specifications and implementations rigid. reliable and secure by translating specification and code into logical formulas.
- The application of theorem proving tools to perform satisfiability checking of logical formulas.

Specification²

- Specifications independent of the source code
 - Formal specification in a separate file from source code and written in a specification language and accepted by theorem provers
- Specifications embedded in the source code (This subject)
 - assert embedded in the program following the Hoare triple form.
- Hoare logic¹: P {prog} Q.
 - P is the pre-condition (assume), expressed by predicate logic (first-order logic) formula
 - Q is the **post-condition** (assert)
 - prog is the target program
 - The Hoare triple describes that when the precondition is met, executing the program prog establishes the postcondition.

¹https://en.wikipedia.org/wiki/Hoare_logic

²https://www.hillelwayne.com/post/why-dont-people-use-formal-methods

Assertion-Based Specification and Satisfiability

Prove whether the post-condition (assert) holds after executing the program given the pre-condition (assume).

```
assume(100 > x > 0); // P
    foo(x){
         if(x > 10) {
             v = x + 1:
                                     translate
                                                                  feed into
         else {
                                             \phi(P\{\text{foo}\}Q)
                                                                           SAT/SMT
             v = 10:
                                                                             Solver
                                                logical formula
assert(y >= x + 1); // Q
```

Will the assertion hold?

Assertion verification as satisfiability checking. The assertion holds if the formula $\phi(P\{\text{foo}\}Q)$ is satisfiable (SAT).

• ϕ is satisfiable if a program *prog* is correct for all valid inputs.

$$\forall \mathtt{x} \ \forall \mathtt{y} \ P(\mathtt{x}) \wedge \mathcal{S}_{prog}(\mathtt{x},\mathtt{y}) \rightarrow \mathcal{Q}(\mathtt{x},\mathtt{y})$$

- P(x) is the pre-condition formula over variables x, i.e., 100 > x > 0.
- $S_{prod}(x, y)$ is the formula representing prog which accepts x as its input, and terminates with output v.
- Q(x, y) is the post-condition formula over variables x, y, i.e., y >= x + 1.

³https://en.wikipedia.org/wiki/Logical_equivalence

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- P(x) is the pre-condition formula over variables x, i.e., 100 > x > 0.
- $S_{proa}(x, y)$ is the formula representing prog which accepts x as its input, and terminates with output v.
- Q(x, y) is the post-condition formula over variables x, y, i.e., y >= x + 1.
- How to prove correctness for all inputs x? Search for counterexample x where ϕ does not hold, that is

$$\exists \mathbf{x} \; \exists \mathbf{y} \; \neg (P(\mathbf{x}) \land S_{prog}(\mathbf{x}, \mathbf{y})) \to Q(\mathbf{x}, \mathbf{y}))$$

$$\Rightarrow \; \exists \mathbf{x} \; \exists \mathbf{y} \; P(\mathbf{x}) \land S_{prog}(\mathbf{x}, \mathbf{y}) \land \neg Q(\mathbf{x}, \mathbf{y}) \qquad \text{(simplification}^3)$$

Note that P(x) is always true if a program has no pre-condition.

³https://en.wikipedia.org/wiki/Logical_equivalence

Checking whether the logical formula ϕ is satisfiable by an SAT/SMT solver.

```
assume(100 > x > 0);
foo(x){
     if(x > 10) {
                                   translate
                                                                                        feed into
           y = x + 1;
                                                                                        \Longrightarrow SAT/SMT
                                   \implies \exists x \exists y \ P(x) \land S_{prog}(x,y)) \land \neg Q(x,y)
                                                                                                  Solver
                                                        logical formula \phi
     else {
           v = 10:
assert(y >= x + 1);
```

Checking whether the logical formula ϕ is satisfiable by an SAT/SMT solver.

Translating Code into Logical Formulas

- Logical formulas
 - The formulas of predicate logic are constructed from propositional, predicate and object variables by using logical connectives and quantifiers (This class)
- Translation
 - Translating SVFStmts of **each program path** (from Assignment-2) into a logical formula ϕ , and then check the satisfiablity for each path.
 - $\forall path \in prog \quad checking(\phi(path))$ $\phi(path_1)$: $\exists x \ P(x) \land ((x > 10) \land (y \equiv x + 1)) \land \neg Q(x, y)$ (if branch of foo) $\phi(path_2)$: $\exists x \ P(x) \land ((x \le 10) \land (y \equiv 10)) \land \neg Q(x, y)$ (else branch of foo)

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 - $\phi(path_2)$: has a counterexample x = 10!!

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 - $\phi(path_2)$: has a counterexample x = 10!!
 - Manual translation via theorem prover APIs (e.g., Z3 APIs) (Assignment-3)
 - Automatic translation during symbolic execution (Assignment-4)

Proving each $\phi(path)$ formula

Exhaustively by not finding counterexamples via mathematical proofs.

- Direct Proof
- Proof by contradiction
- Proof by induction
- Proof by construction
- . . .

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Manual proof is impractical for software verification!

- Too many variables and logical relations between them (state exploration)
- Too many assertions as specifications.

This subject **does not** focus on formal mathematical proof, but rather the application of automated theorem prover tools

- Translating code into logical formulas
- Feeding the logical formulas into a theorem prover to check the satisfiability.

Theorem Prover Tools 4

- Interactive theorem provers (proof assistant)
 - Formal proofs by human-machine collaboration via expressive specification language and may not work directly on source code.
 - For example, ACL2, Coq, Isabelle and HOL provers
- Automated theorem provers
 - Proof automation (but less expressive than interactive provers) and can work on real-world source code.
 - For example, Z3 and CVC

⁴https://en.wikipedia.org/wiki/Theorem_prover

Automated Theorem Provers

A prover/solver checks if a formula $\phi(P\{\text{foo}\}Q)$ is satisfiable (SAT).

- If yes, the solver returns a **model** m, a valuation of x, y, z of foo that satisfies ϕ (i.e., m makes ϕ true).
- Otherwise, the solver returns unsatisfiable (UNSAT)

SAT vs. SMT solvers

- SAT solvers accept propositional logic (Boolean) formulas, typically in the conjunctive normal form (CNF).
- **SMT** (satisfiability modulo theories) solvers generalize the Boolean satisfiability problem (SAT), and accept more expressive predicate logic formulas, i.e., propositional logic with predicates and quantification.
 - Z3 Automated Theorem Prover⁵, a cross-platform satisfiability modulo theories (SMT) solver developed by Microsoft (This subject).
 - More details next week

⁵https://github.com/Z3Prover/z3/wiki#background