

```
260 I2C_Base::mStateMachineStatus_t I2C_Base::i2cStateMachine()  
261 {  
262     enum {  
263         // General states :  
264         busError          = 0x00,  
265         start              = 0x08,  
266         repeatStart       = 0x10,  
267         arbitrationLost   = 0x38,  
268  
269         // Master Transmitter States:  
270         slaveAddressAked   = 0x18,  
271         slaveAddressNacked = 0x20,  
272         dataAckedBySlave   = 0x28,  
273         dataNackedBySlave  = 0x30,  
274  
275         // Master Receiver States:  
276         readAckedBySlave   = 0x40,  
277         readModeNackedBySlave = 0x48,  
278         dataAvailableAckSent = 0x50,  
279         dataAvailableNackSent = 0x58,  
280  
281         // Slave Receiver States  
282         slaveAddressReceived = 0x60,  
283         slaveDataReceived    = 0x80,  
284         slaveStoporRptStartRecv = 0xA0,  
285  
286         // Slave Transmitter States  
287         slaveDataSend       = 0xA8,  
288         dataAckedByMaster   = 0xB8,  
289         masterNackRecv      = 0xC0  
290     };  
291 }
```