

VLSI Lab 5

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Abstract

This report contains solutions for the problems described in Assignment L5 for the course VLSI Programming.

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1 Problem Specification and Requirements

2 Solution

In this section we describe the key ideas behind our design, and the decisions we made during the design process.

3 Results

4 Appendix A: Answers to inline questions

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5 Appendix B: Verilog source code

This appendix includes Verilog source code for the `filter.v` file in the ISE project.