REDUX-V: Algorítmo Base

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1 Assembly

```
; int i;
; int a = 3;
 int b = 42;
; int *c = 10;
 for (i = 0; i < a; i++) {
    c[i] = b + i;
; }
_setup:
0x00
     xor r0, r0
                       ; r0 = 0
      addi 3
0x01
                       ; r0 = 3
0x02
      mov r3, r0
                       ; r3 = 0
                       ; r3 := i + 1
0x03
      xor r0, r0
                       ; r0 = r0
0x04
      addi 4
                       ; r0 = 4
      mov r1, r0
0x05
                       ; r1 = r0
                       ; r1 := 4
0x06
      xor r0, r0
                       ; r0 = 0
0x07
      addi 12
                       ; r0 = 1111 \ 1100
      slr r0, r1
                       ; r0 = r0 << r1
0x08
                       ; r0 := 1111 \ 1100 << 4 = 1100 \ 0000
0x09
                       ; r0 = r0 >> r1
      srr r0, r1
                       ; r0 := 1100 \ 0000 >> 4 = 0000 \ 1100
                       ; r2 = r0
0x0A
      mov r2, r0
                       ; r2 := 12
_loop:
      xor r0, r0
0x0B
                       ; r0 = 0
0x0C
      addi 1
                       ; r0 = 0000 0001
0x0D
      slr r0, r0
                       ; r0 = 0000 0010
0x0E
      slr r0, r0
                       ; r0 = 0010 \ 0000 = 0x20
0x0F
      addi 2
                       r0 = 0x22
                       ; r0 := loop\_end
0x0F
      mov r1, r0
                       ; r1 = r0
                       ; r1 := loop\_end
0x10
      brzr r3, r1
                       ; PC = r3 = 0 ? r1 : PC + 1
                       ; PC = i = 0 ? loop_end : PC + 1
```

```
0x11 \mod r0, r3
                       ; r0 = r3
0x12
      addi -1
                       ; r0 = 1
0x13
      mov r3, r0
                        ; r3 = r0
                        ; r3 := i - 
0x14
      xor r0, r0
                       ; r0 = 0
                        ; r0 = 2
0x15
      addi 2
      mov r1, r0
                       ; r1 = r0 = 0000 0010
0x16
      slr r0, r1
                       ; r0 = 0000 1000
0x17
                       ; r0 = 0000 \ 1010
0x18
      add r0, r1
0x19
      slr r0, r1
                       ; r0 = 0010 \ 1000
      add r0, r1
                        ; r0 = 0010 \ 1010
0x1A
                        ; r0 := 42
                        ; r0 = r0 + r3
0x1B
      add r0, r3
                       ; r0 := b + i
      mov r1, r0
0x1C
                       ; r1 = r0
                       ; r1 := b + i
0x1D
      st r1, r2
                        ; M[r2] = r1
                       ; M[c] := b + 1
0x1E
      mov r0, r2
                       ; r0 = r2
0x1F
      addi -1
                       ; r0 = 1
                       ; r2 = r0
0x20
      mov r2, r0
                       : r2 := c - 1
                       ; PC = 0010 \ 0001 + 1111 \ 1001 = 0001 \ 1010 = 0x0B
0x21
      jі —7
                        ; PC := _loop
loop_end:
0x22
```

2 Código de Máquina