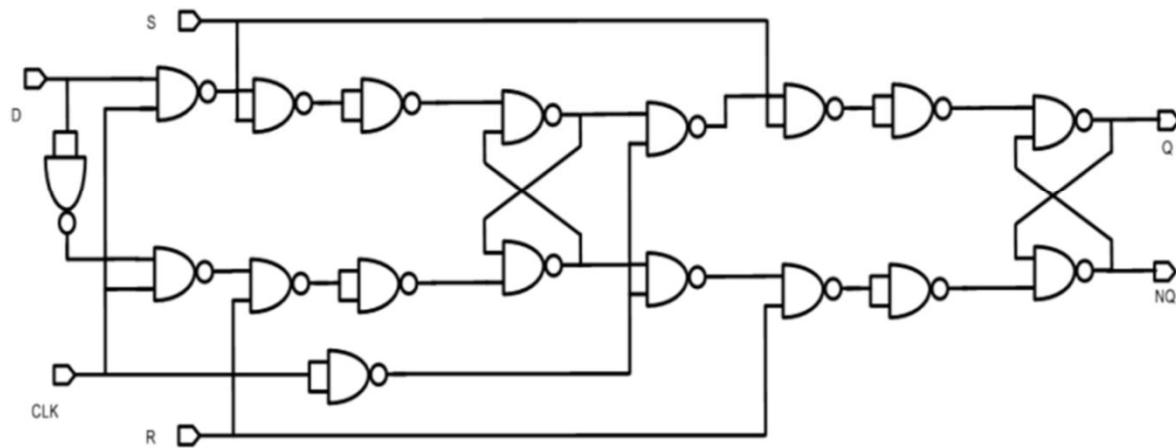


PROIECT BTME1

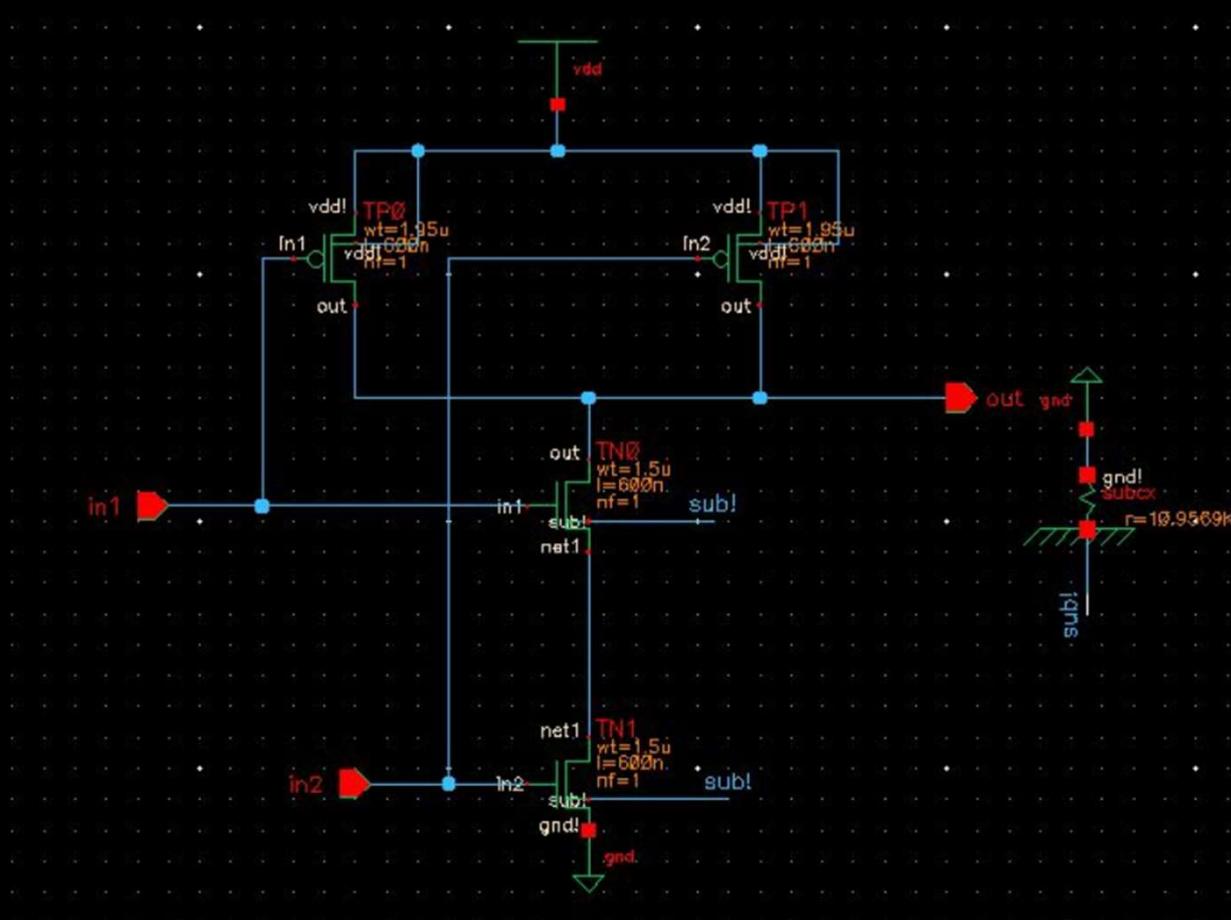
NUMARATOR BINAR ASINCRON CU 12 STARI CU BISTABIL D_v1

BALINT ANTONELA-NICOLA

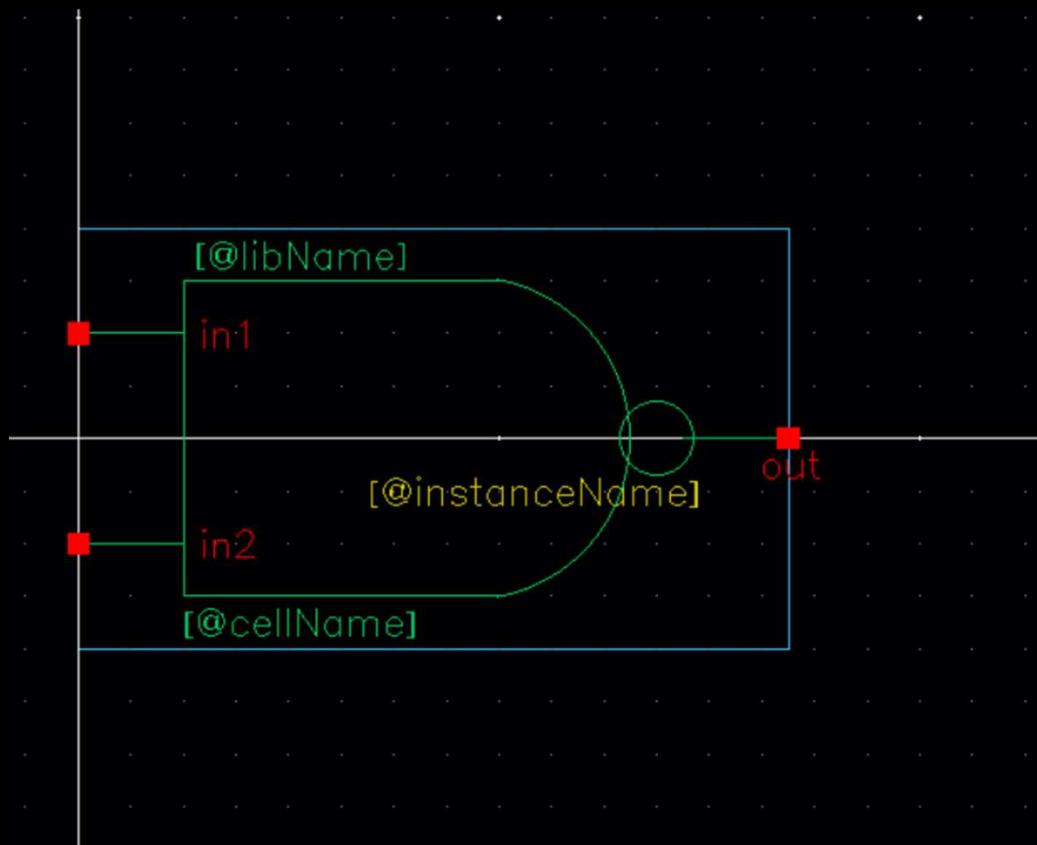
Bistabil de tip D_v1



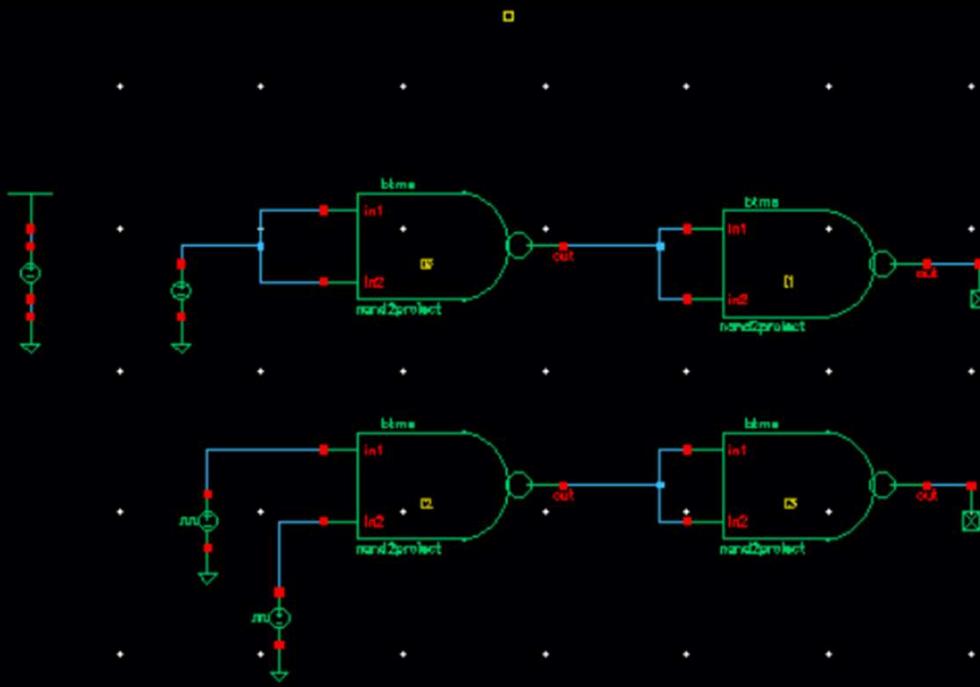
Schematic NAND2(L=0.6u, Wn=1.5u, Wp=1.95u)



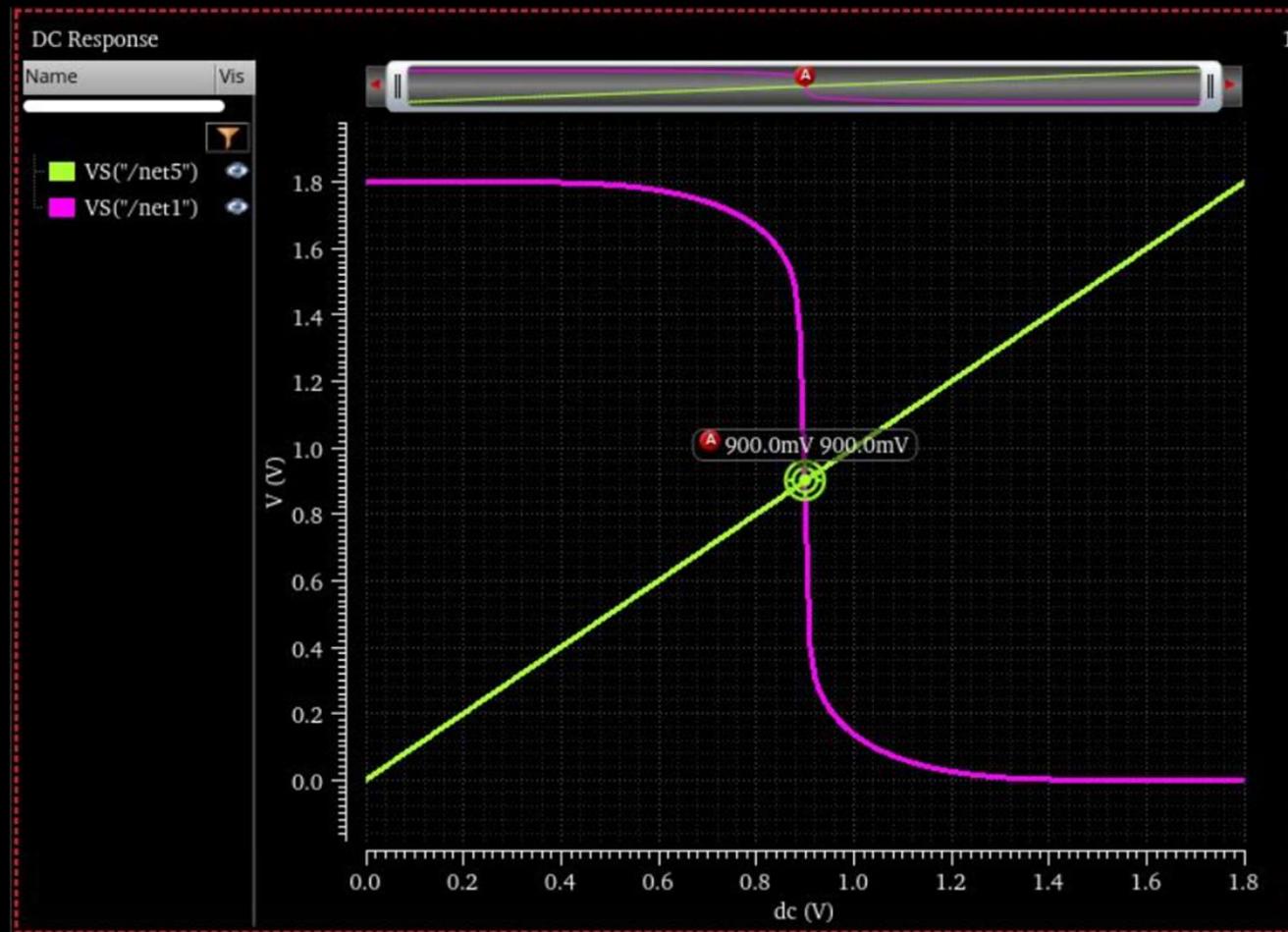
Simbol NAND2



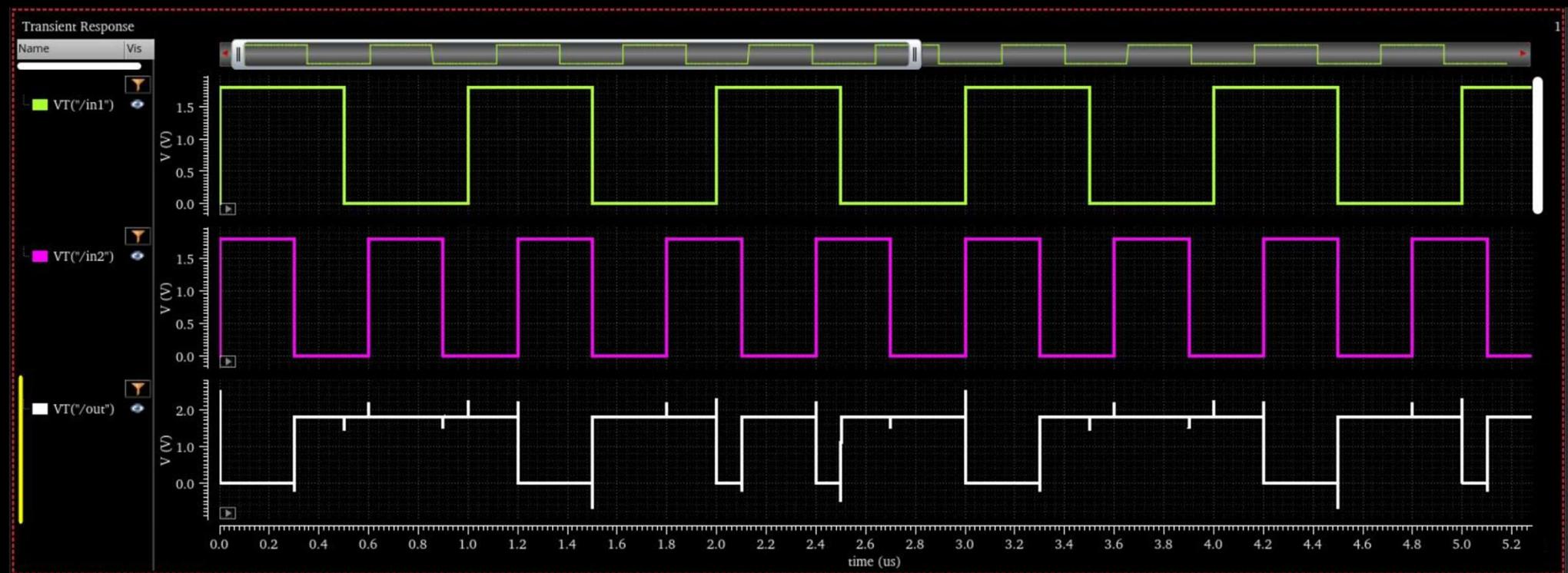
Simulari NAND2

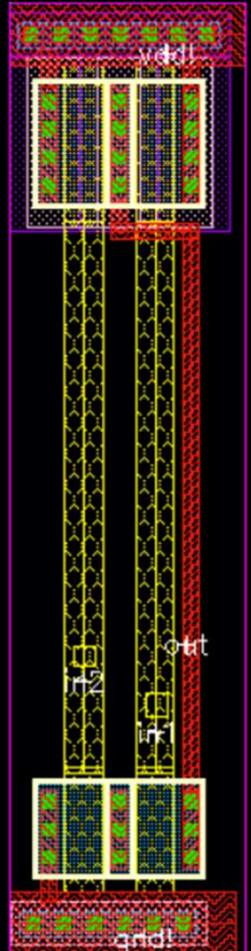


Analiza DC:



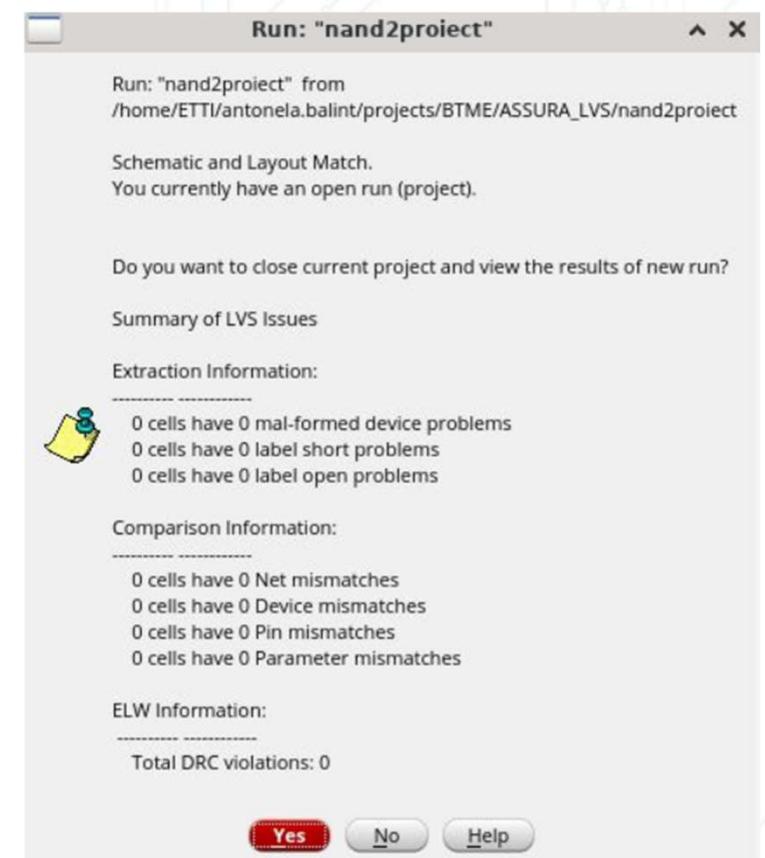
Analiza TRAN:



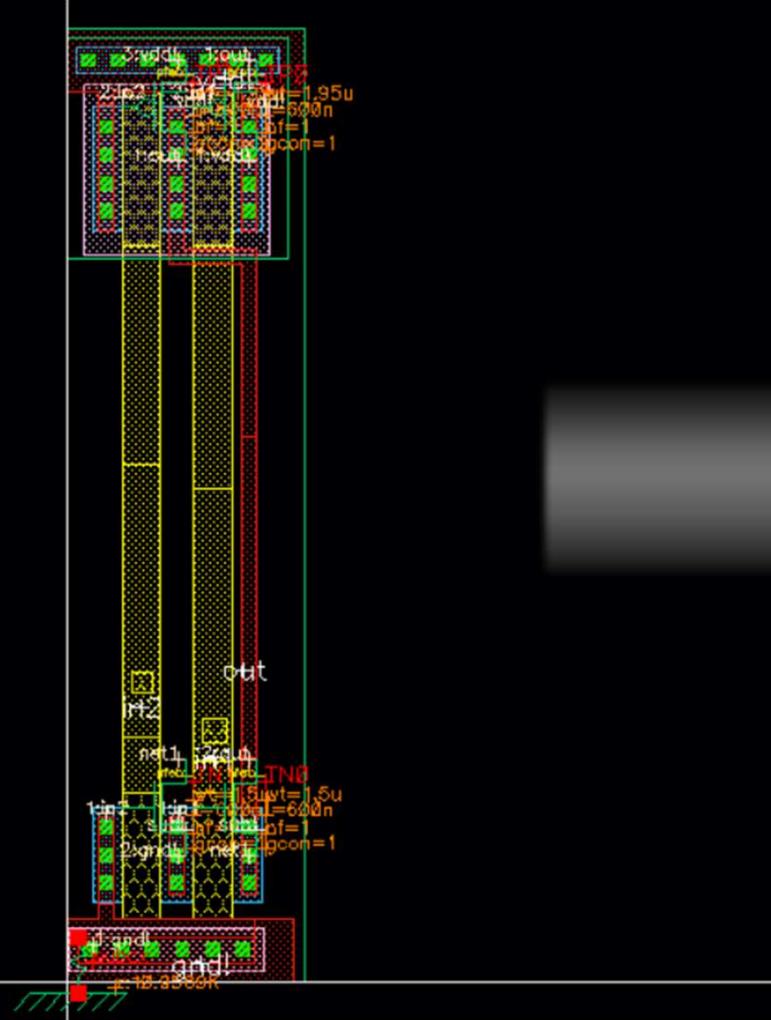


Layout NAND2

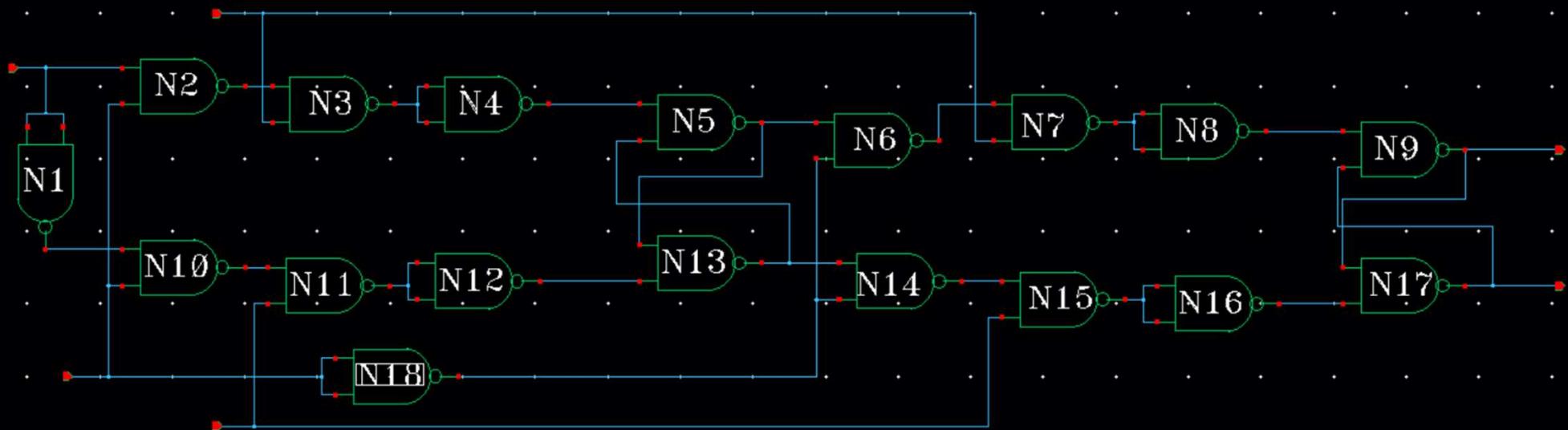
DRC & LVS



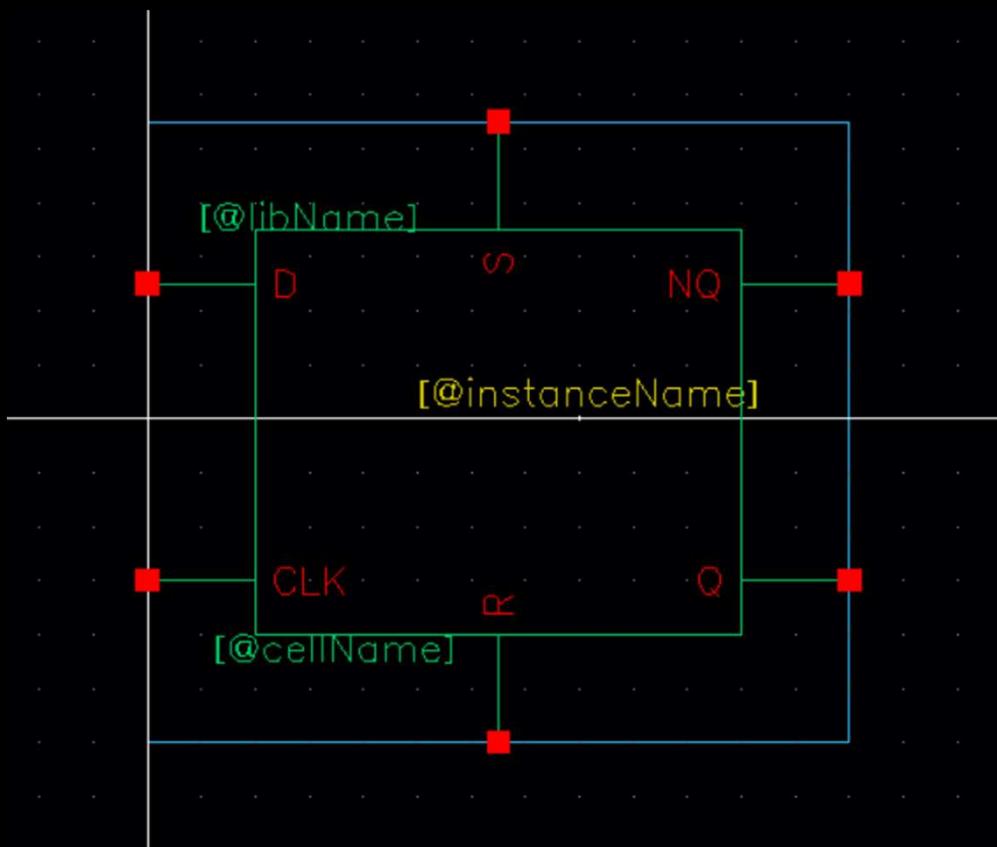
AV extracted



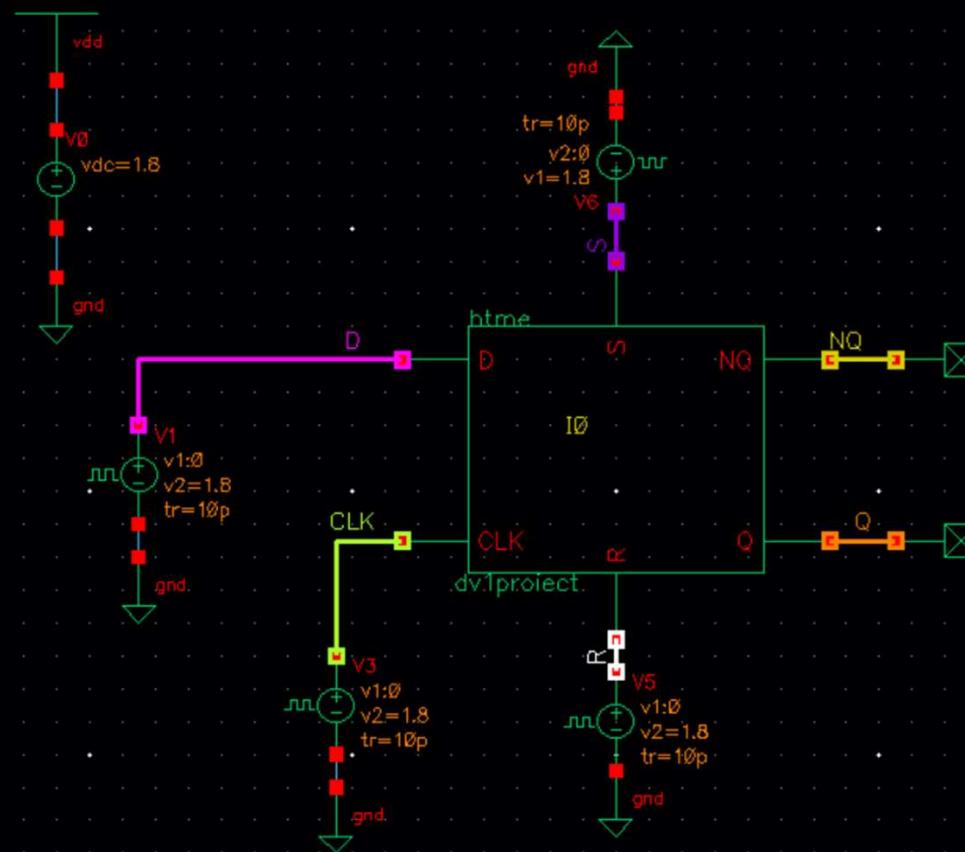
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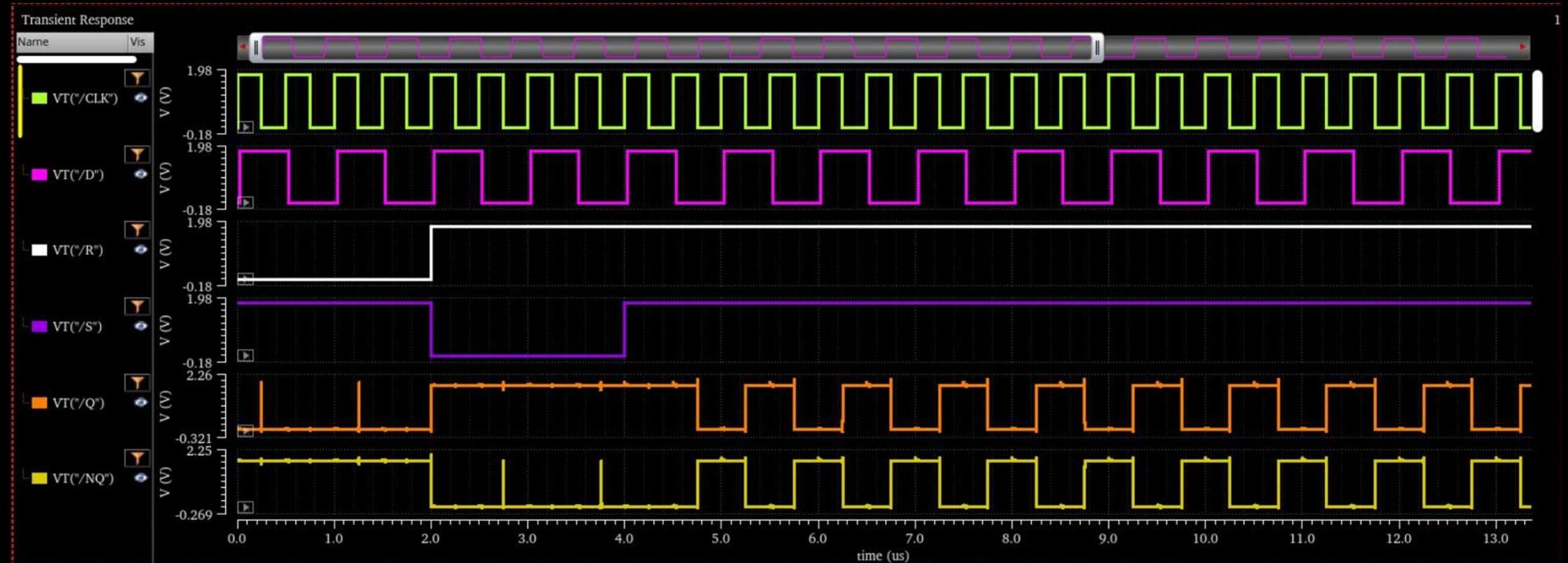
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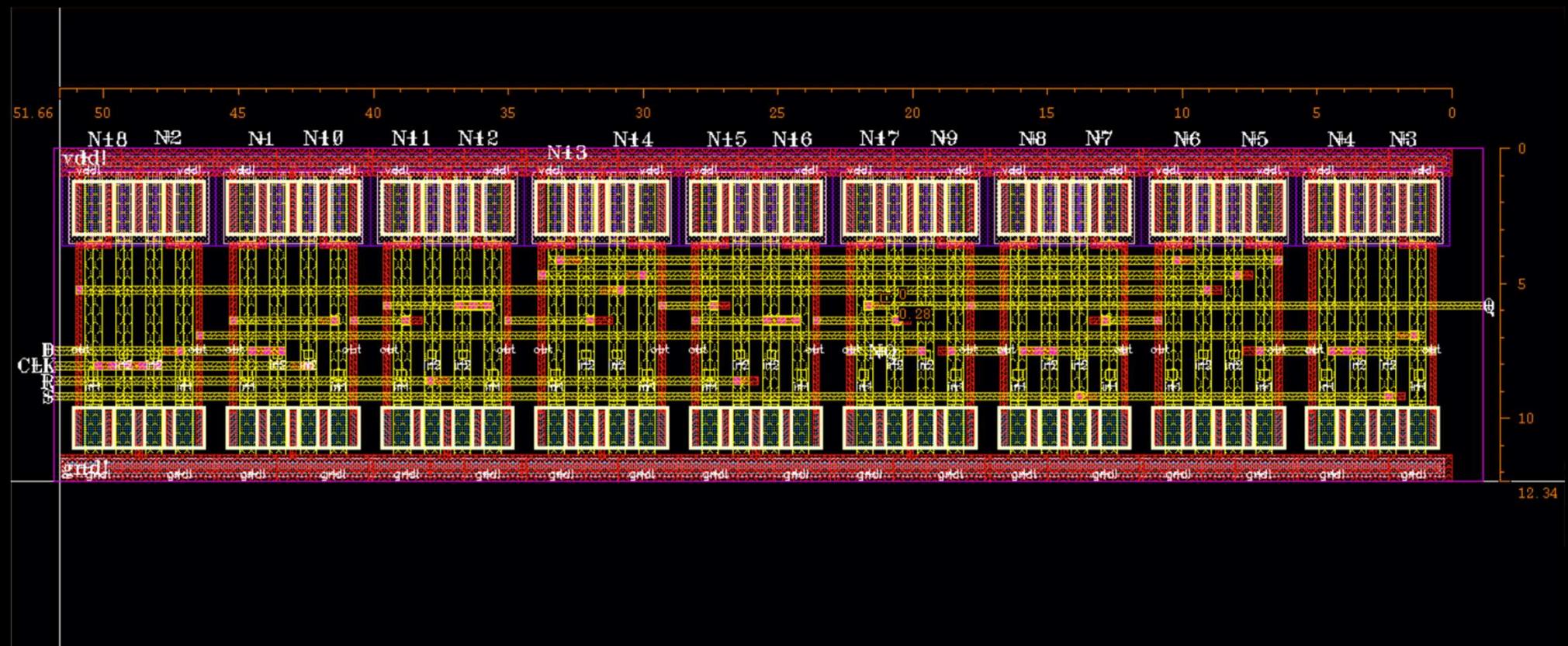
Simulare bistabil:



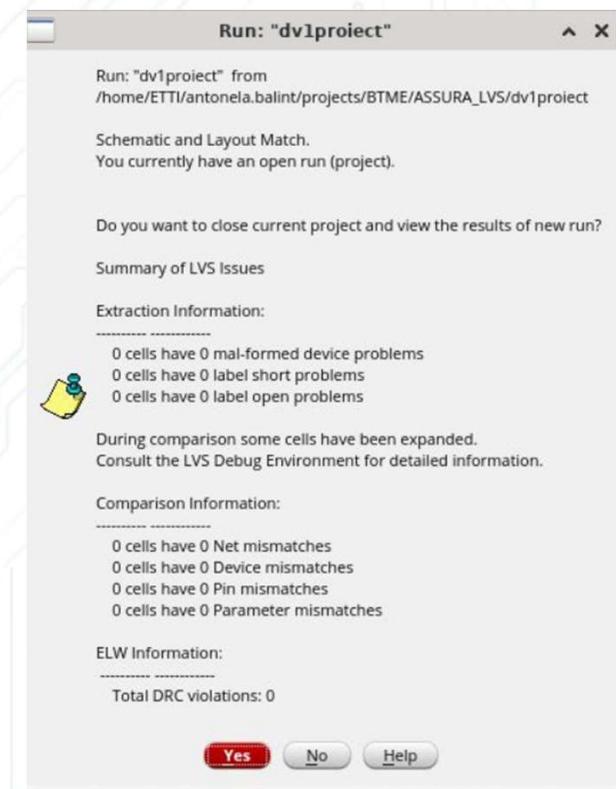
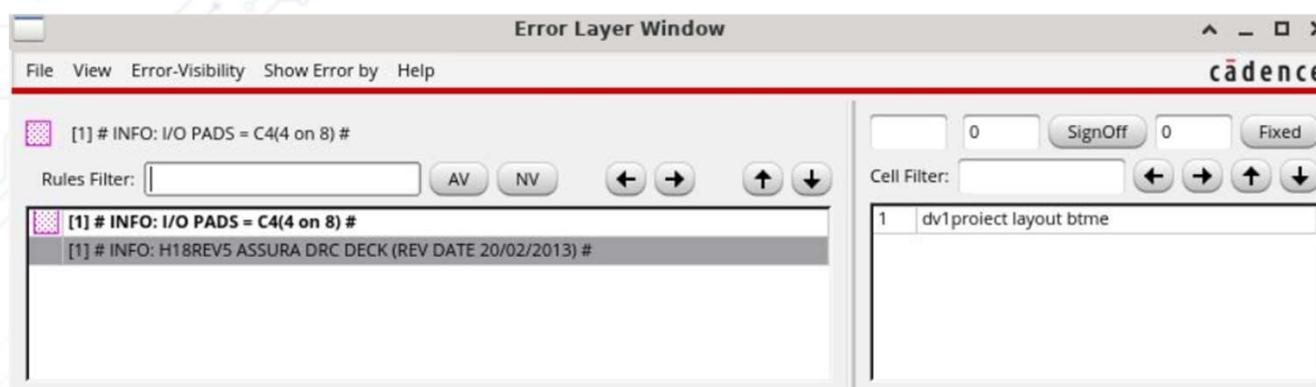
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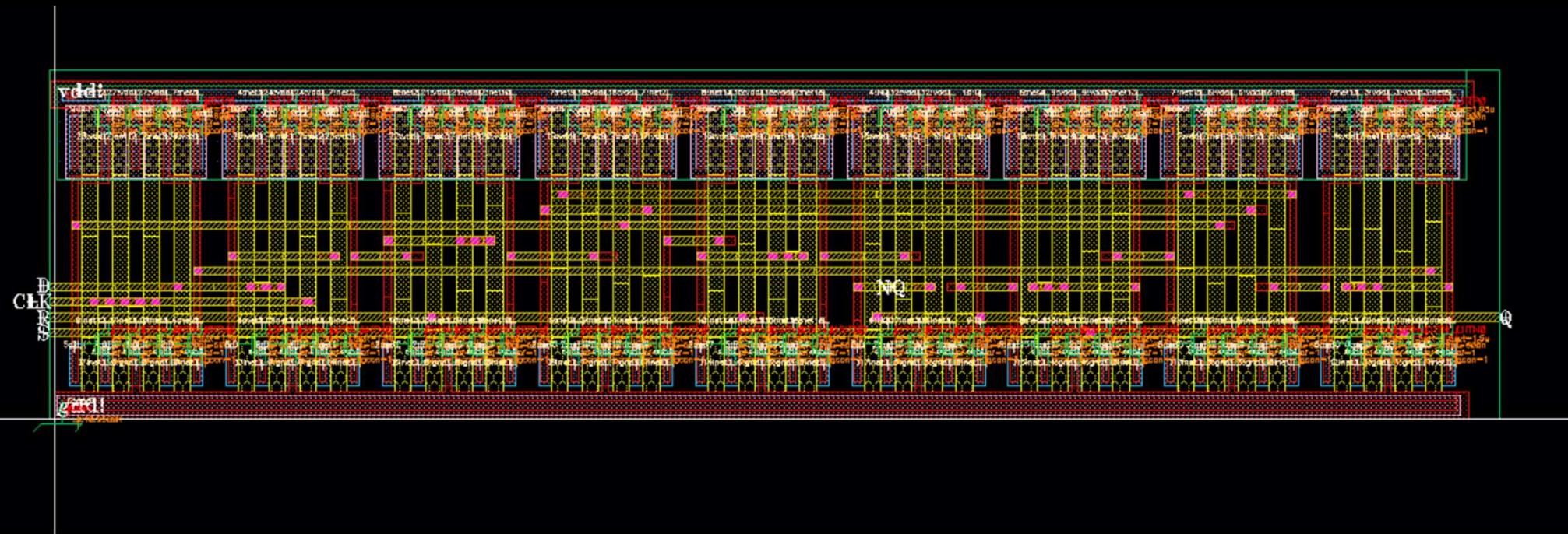
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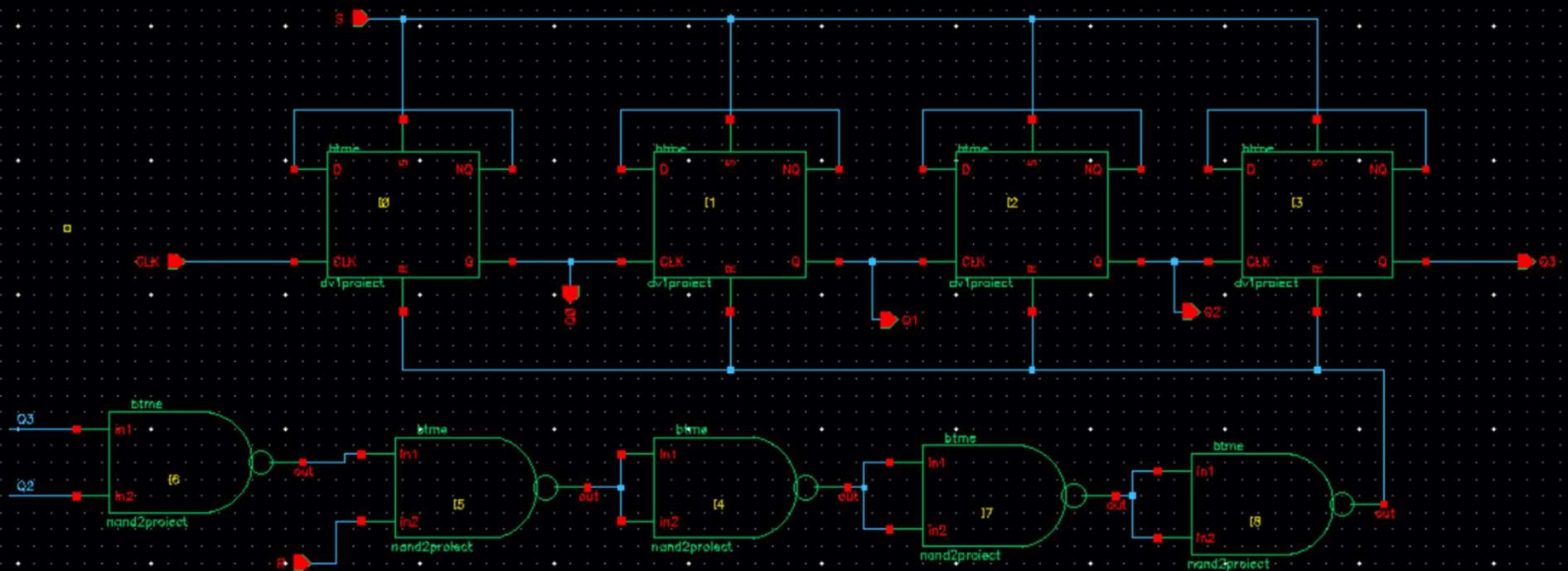
DRC & LVS



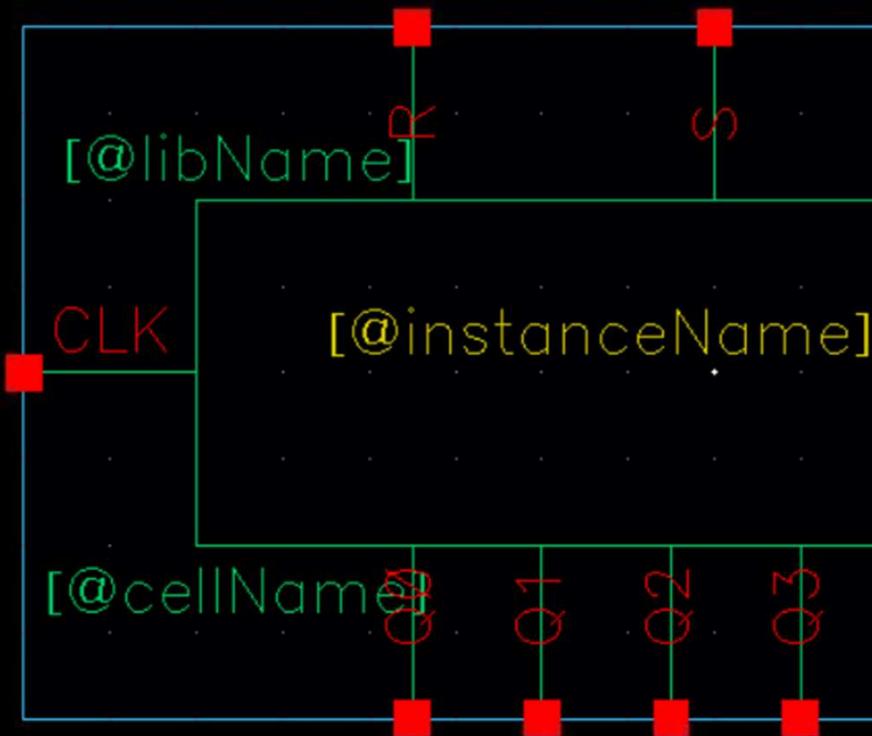
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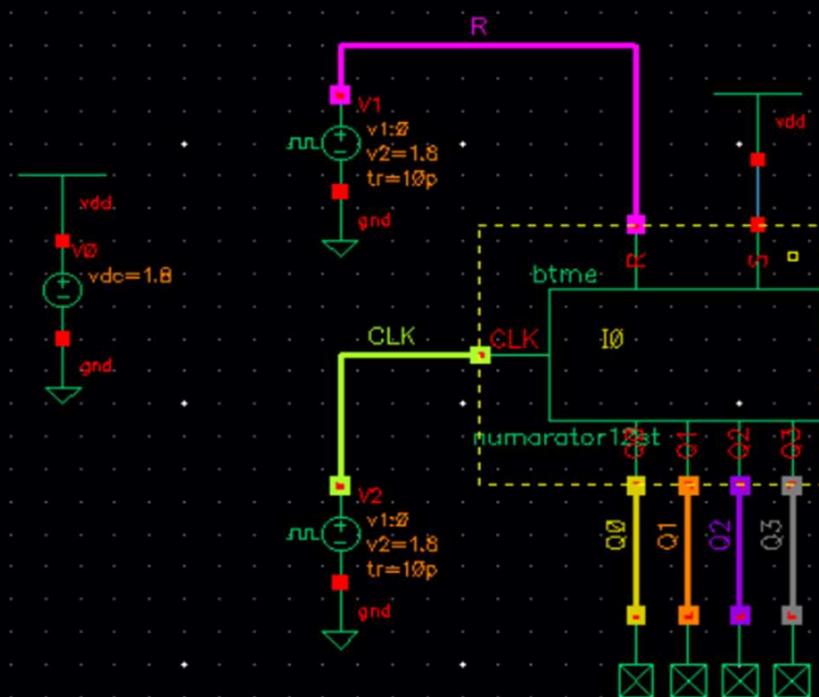
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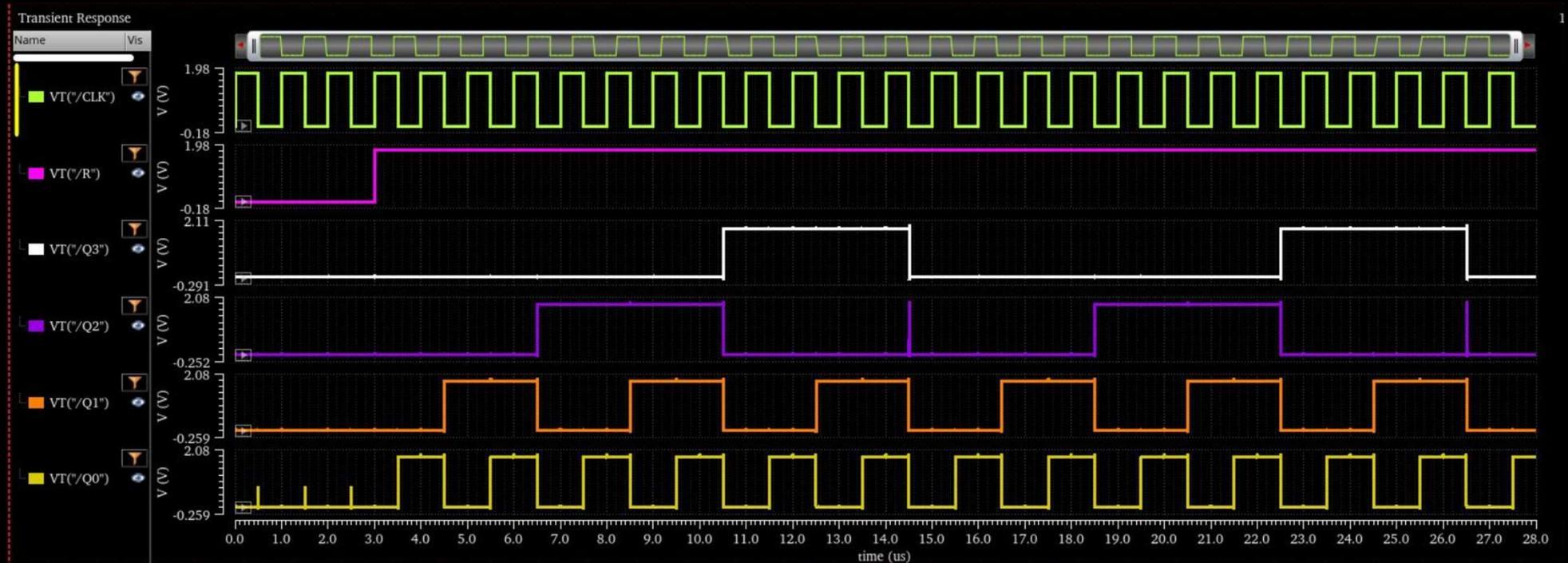
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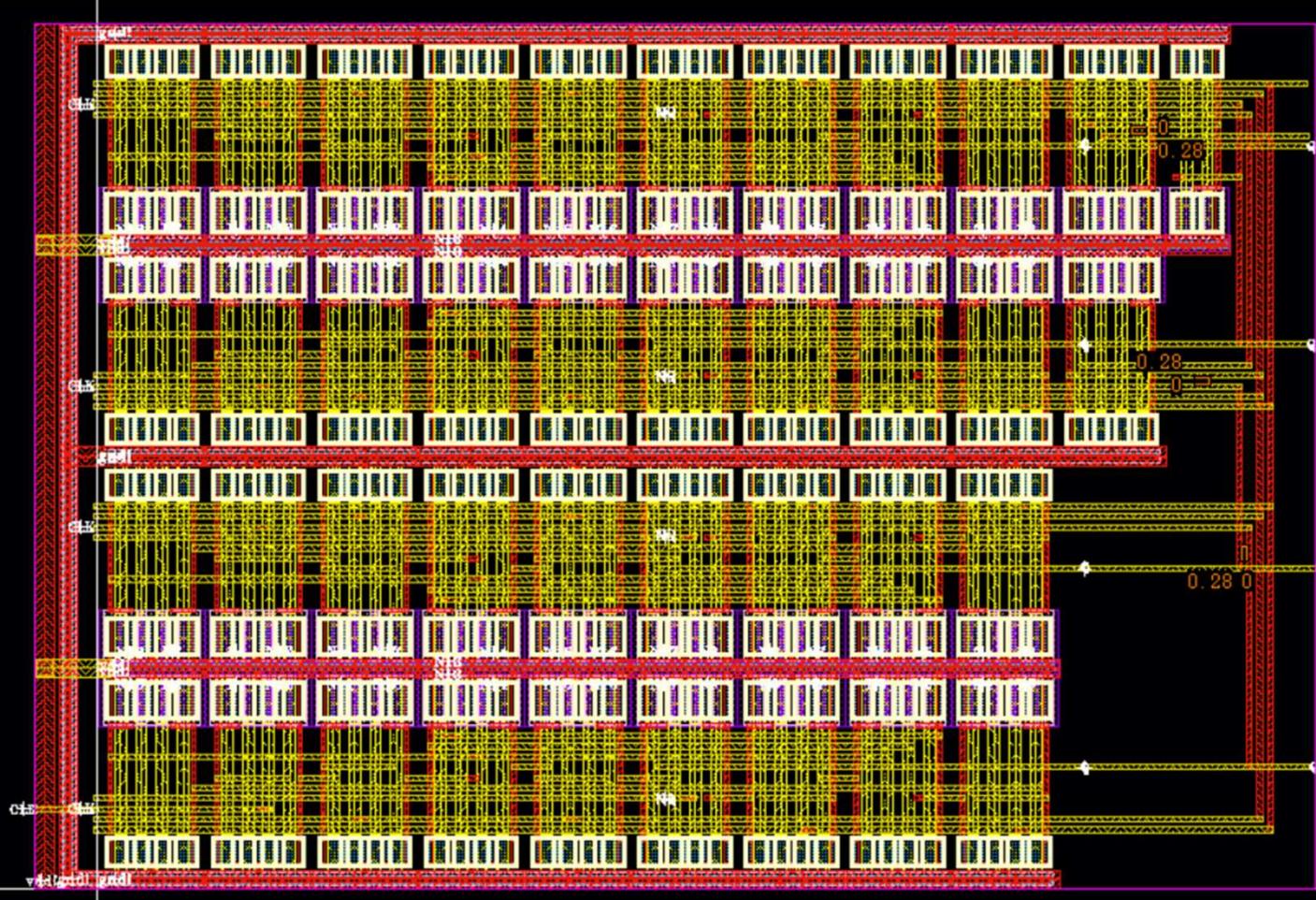
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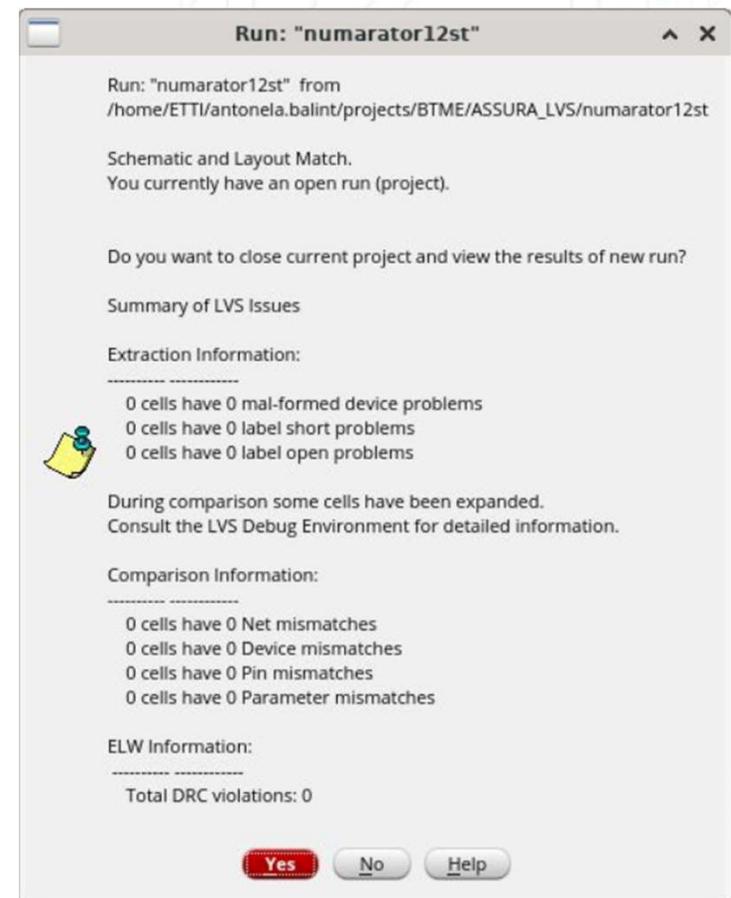
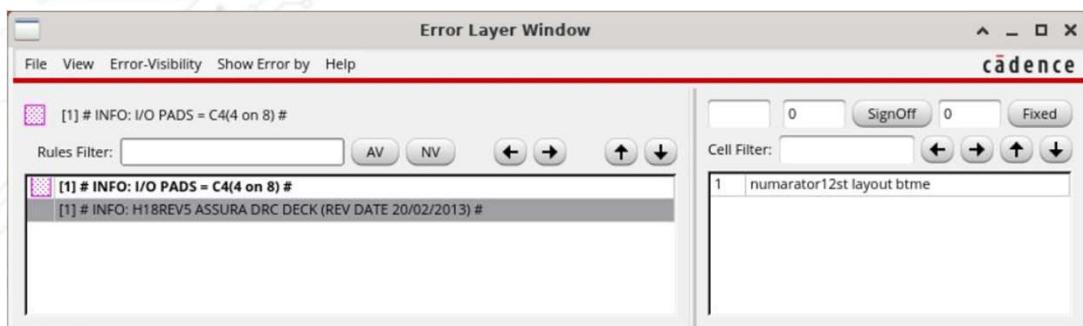
Forme de unda numarator:



Layout numarator:



DRC & LVS



AV extracted:

