

PROIECT 7

Bazele tehnologice ale microelectronicii 2

BALINT ANTONELA-NICOLA

GRUPA 5314

CUPRINS

1

Planificare
matching

2

Schematic

3

Layout

4

DRC & LVS

PARTEA 1

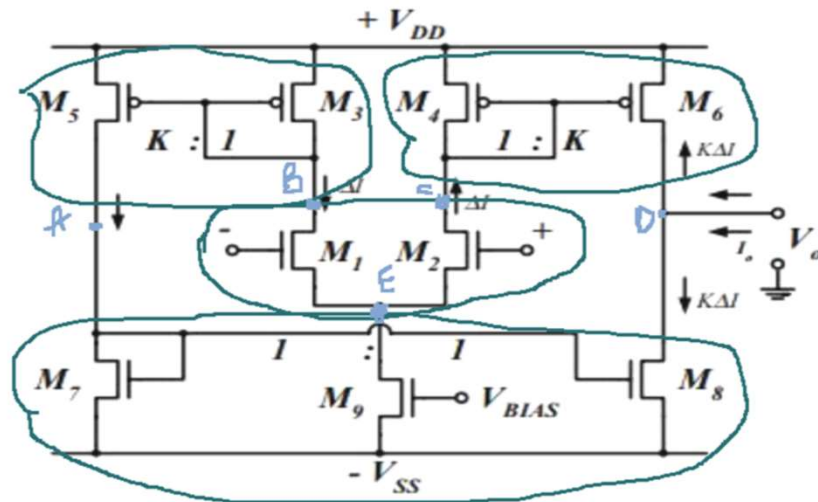
Planificare matching



1. Planificare matching

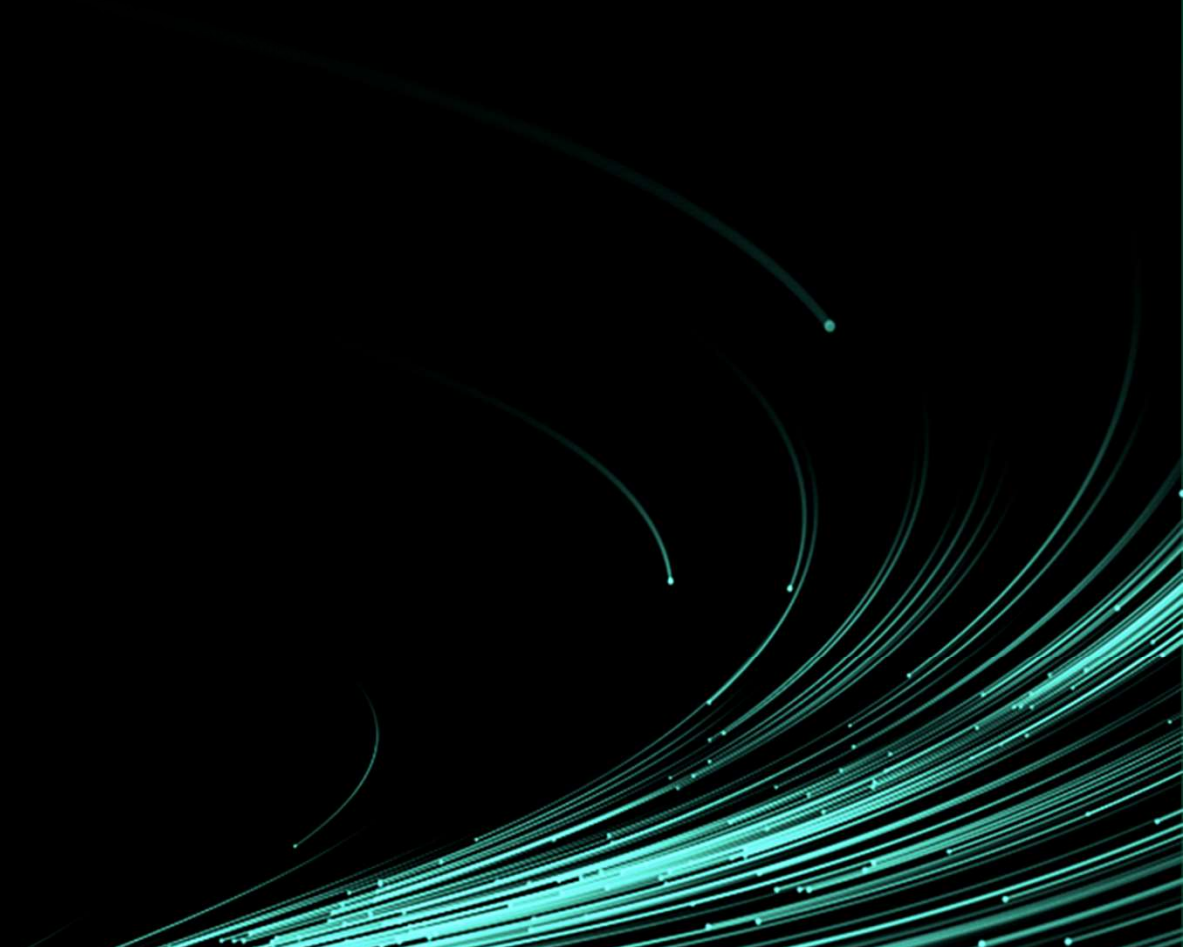
Proiect 7.

$L=0.5\mu$ și $w_1=w_2=24\mu$, $w_3=w_4=8\mu$, $w_5=w_6=16\mu$, $w_7=w_8=w_9=12\mu$

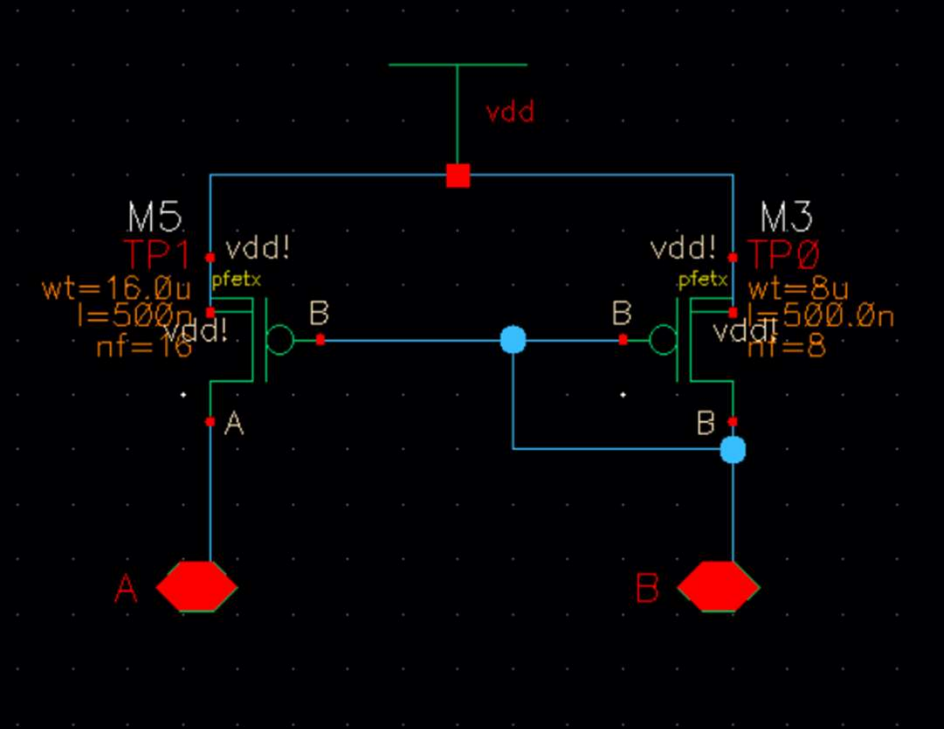


PARTEA 2

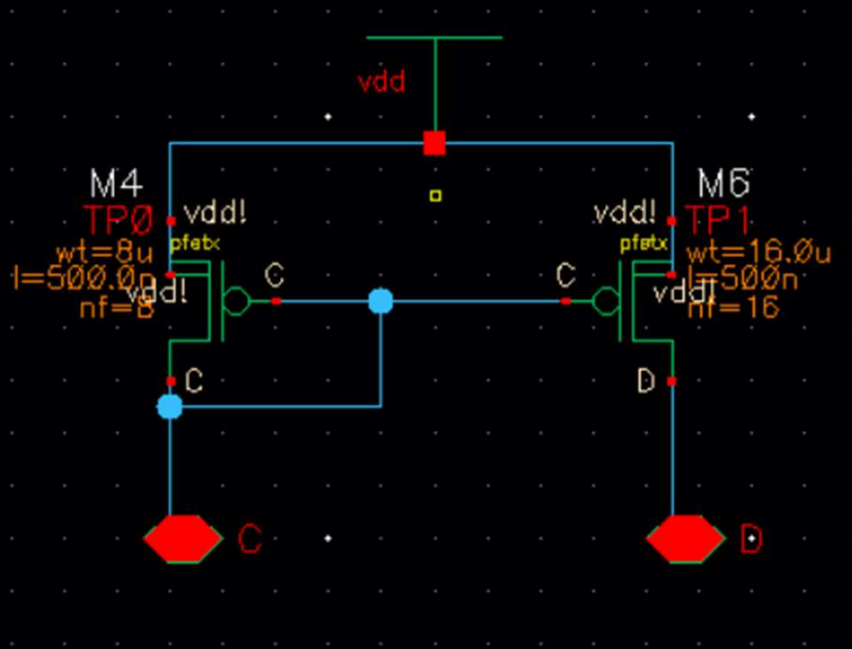
Schematic



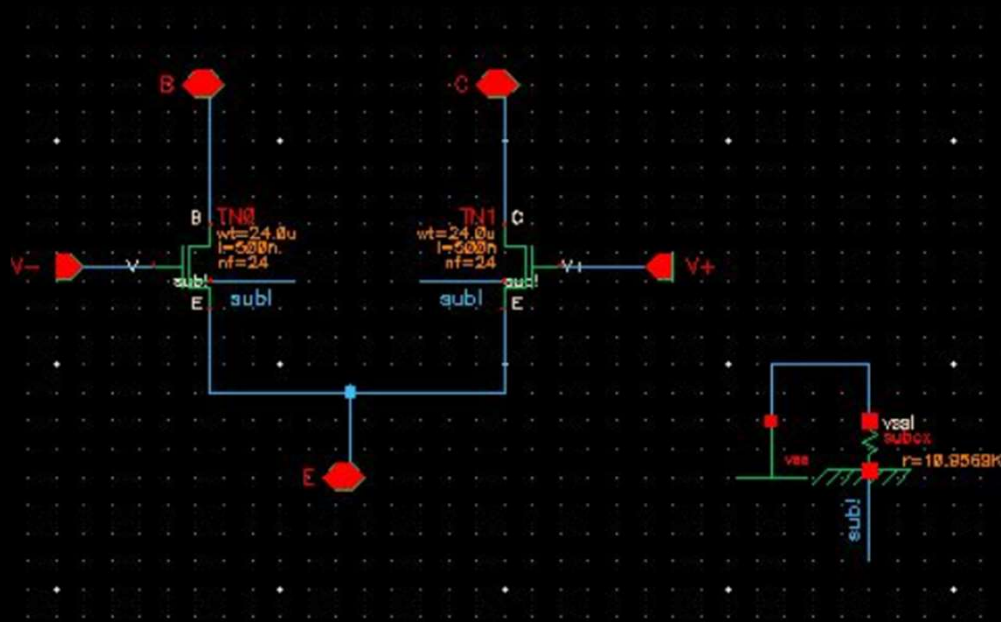
2.1 Schematic oglinda de curent M5-M3



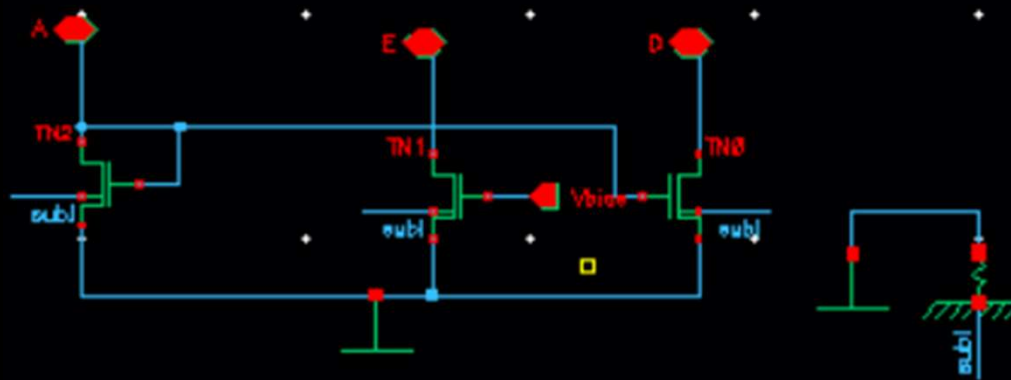
2.2 Schematic oglinda de curent M4-M6



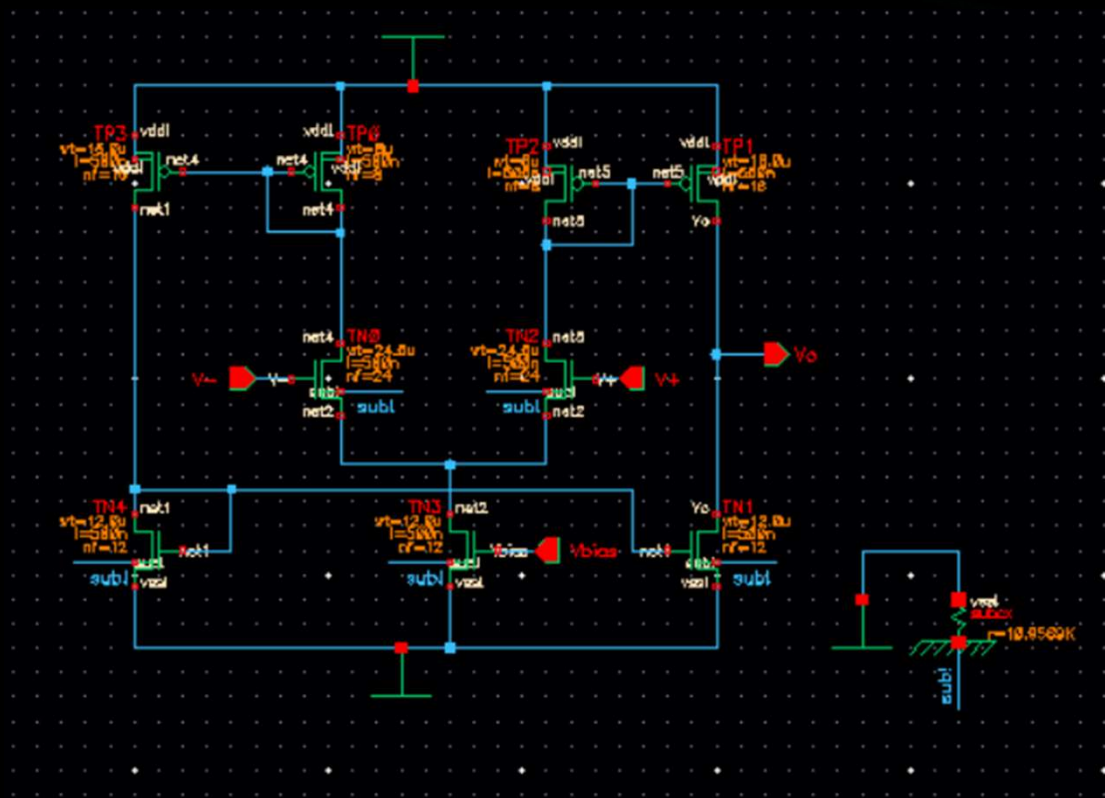
2.3 Schematic oglinda de curent M1-M2



2.4 Schematic gruparea M7-M8-M9

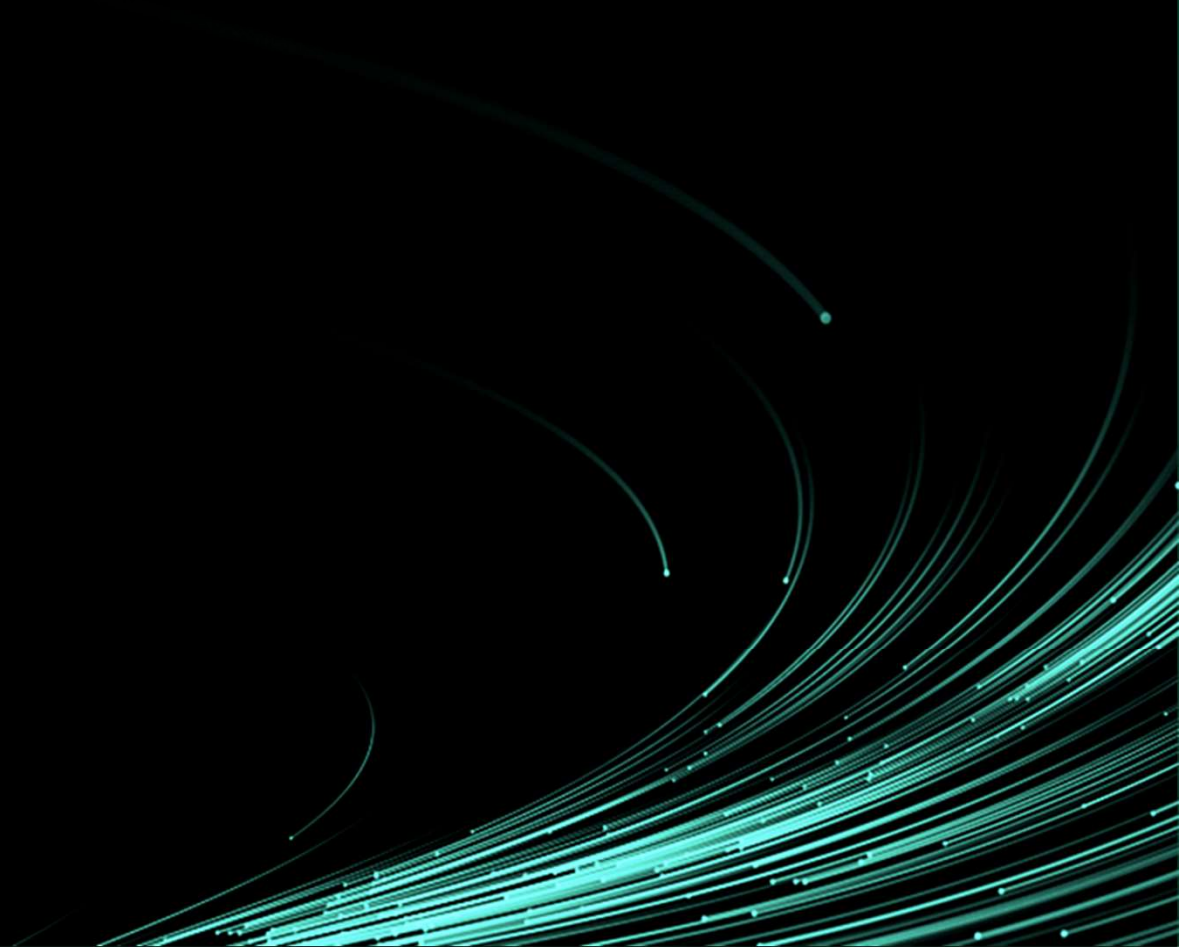


Schematic final

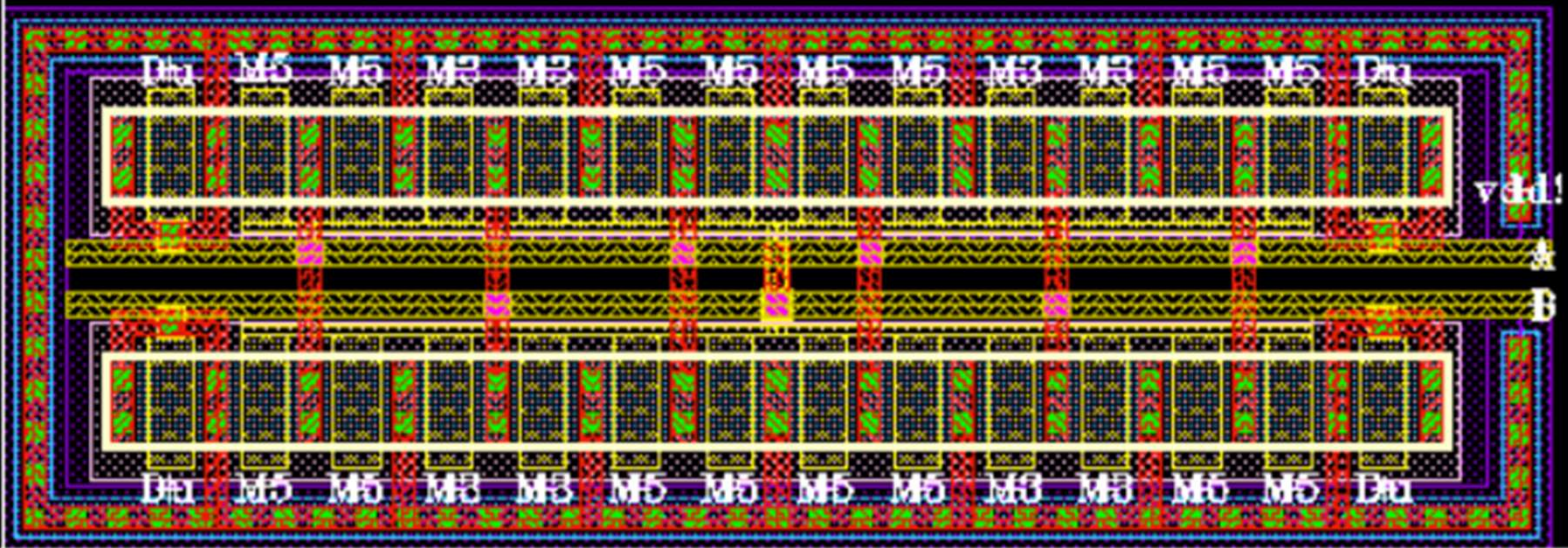


PARTEA 3

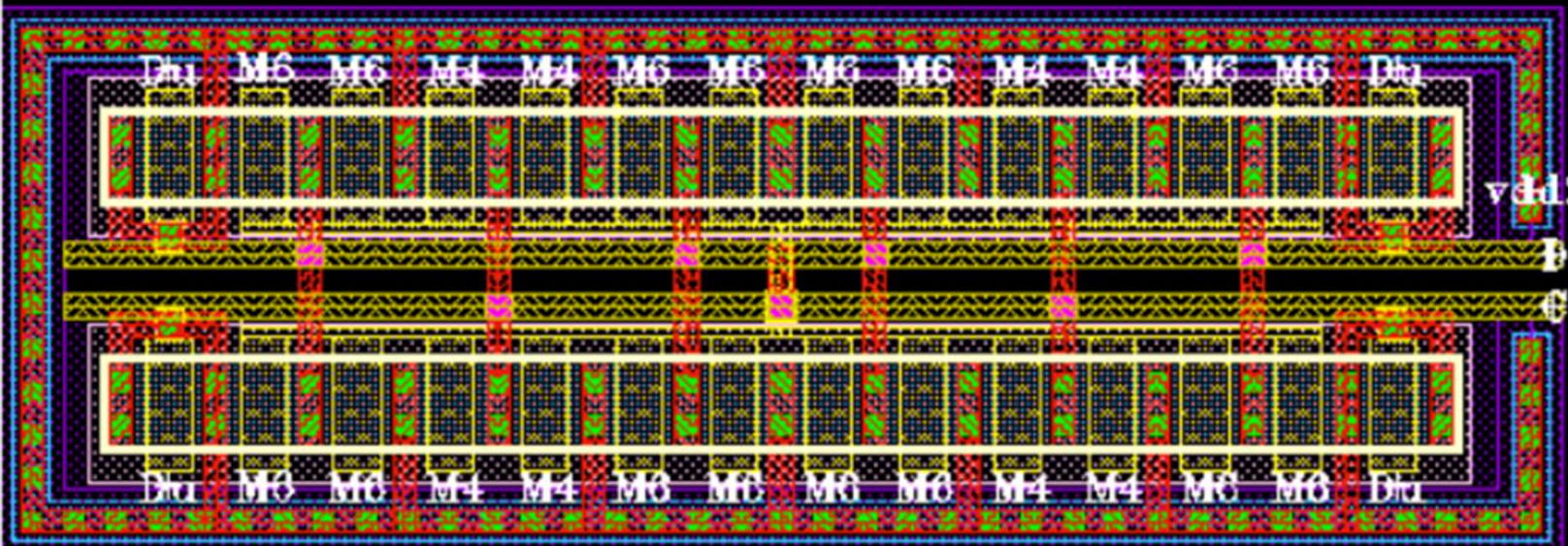
Layout



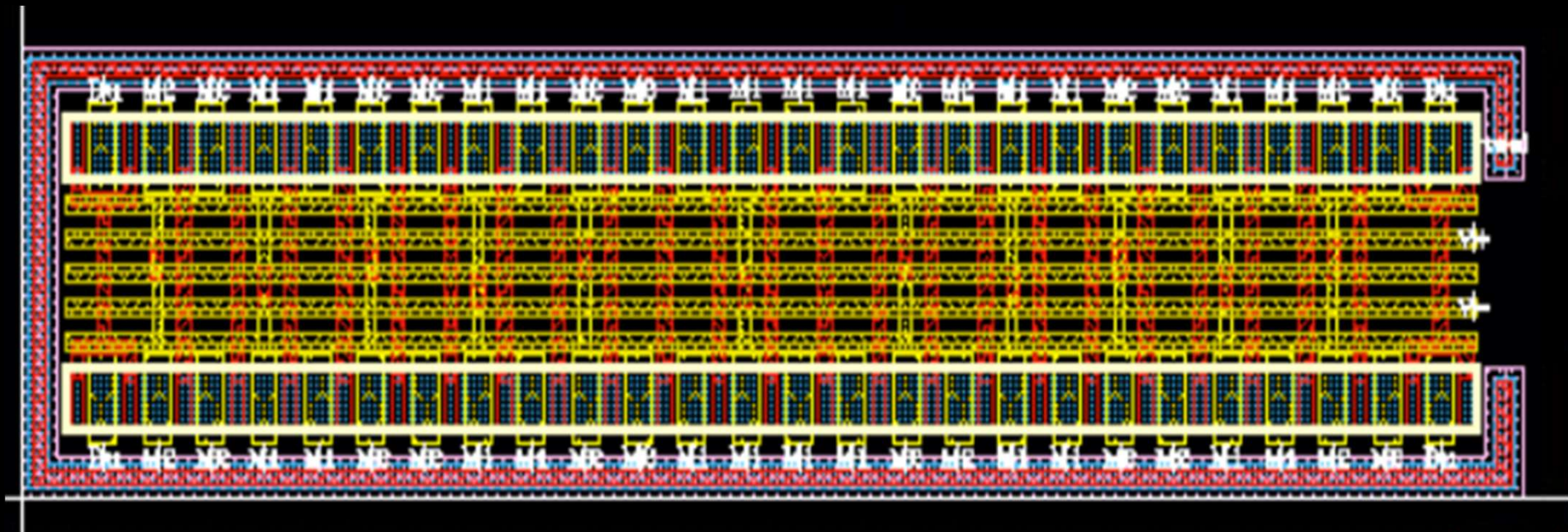
3.1 Layout oglinda de curent M5-M3



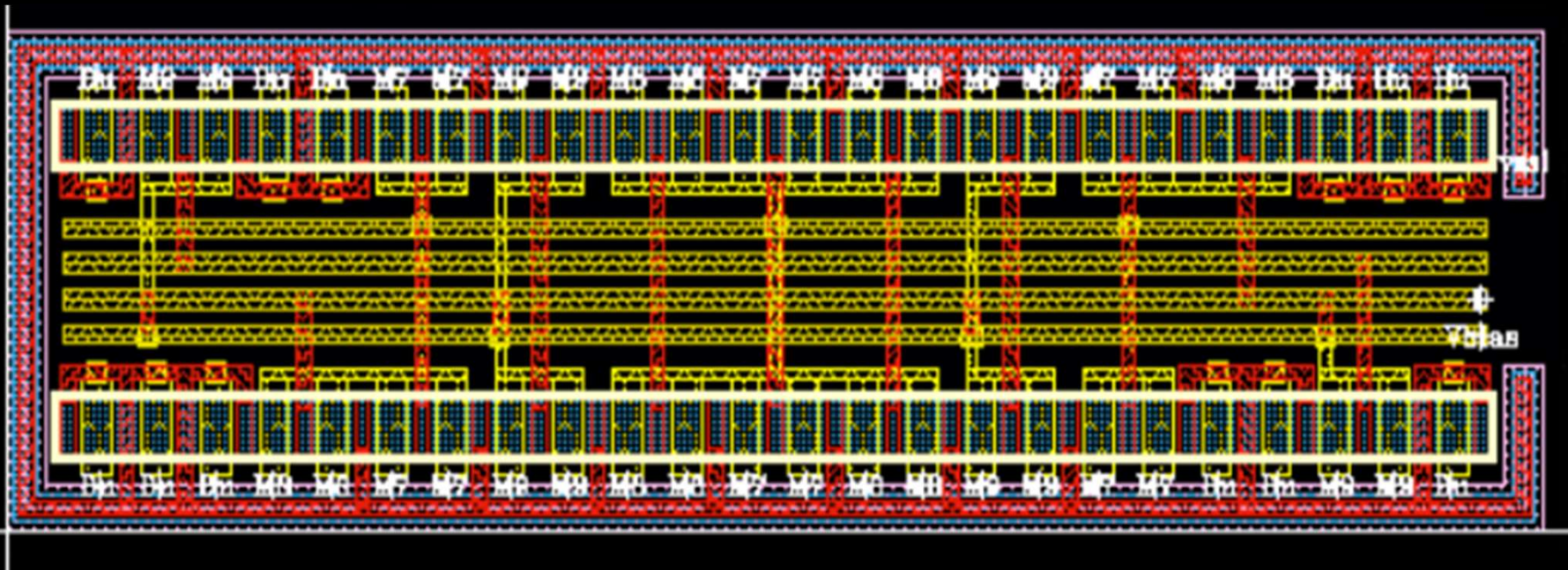
3.2 Layout oglinda de curent M4-M6



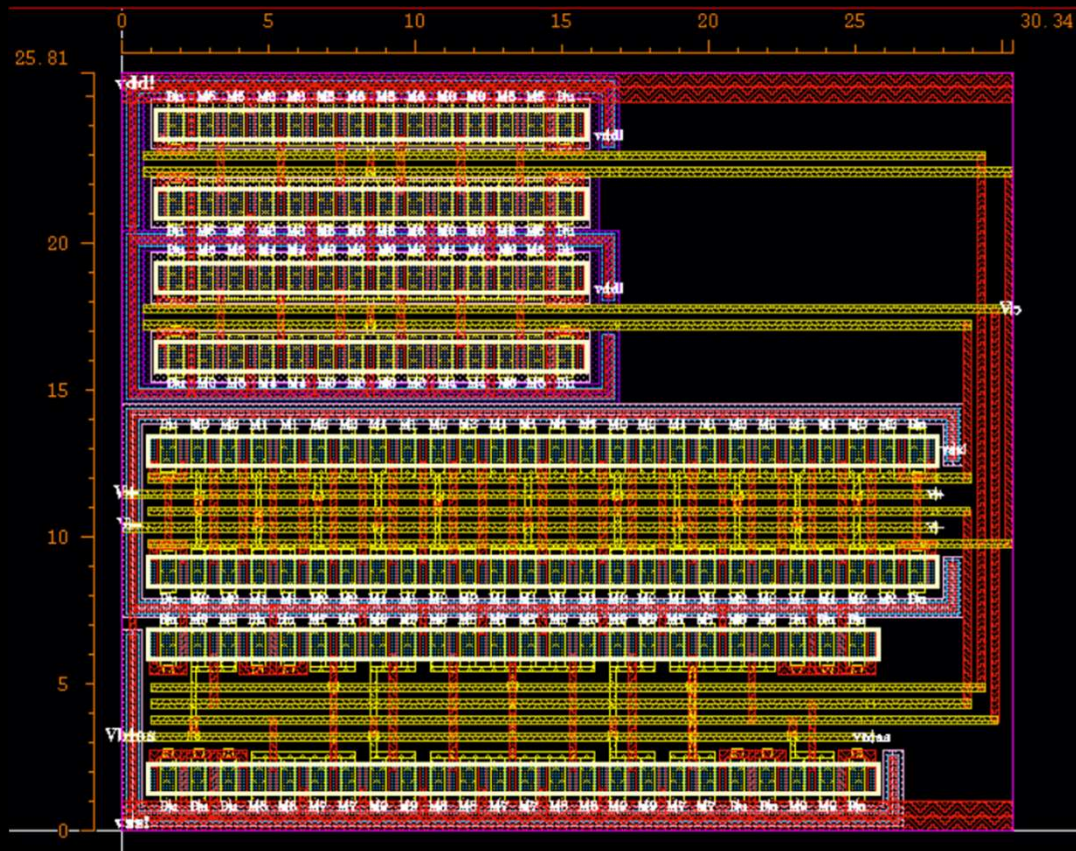
3.3 Layout oglinda de curent M1-M2



3.4 Layout gruparea M7-M8-M9

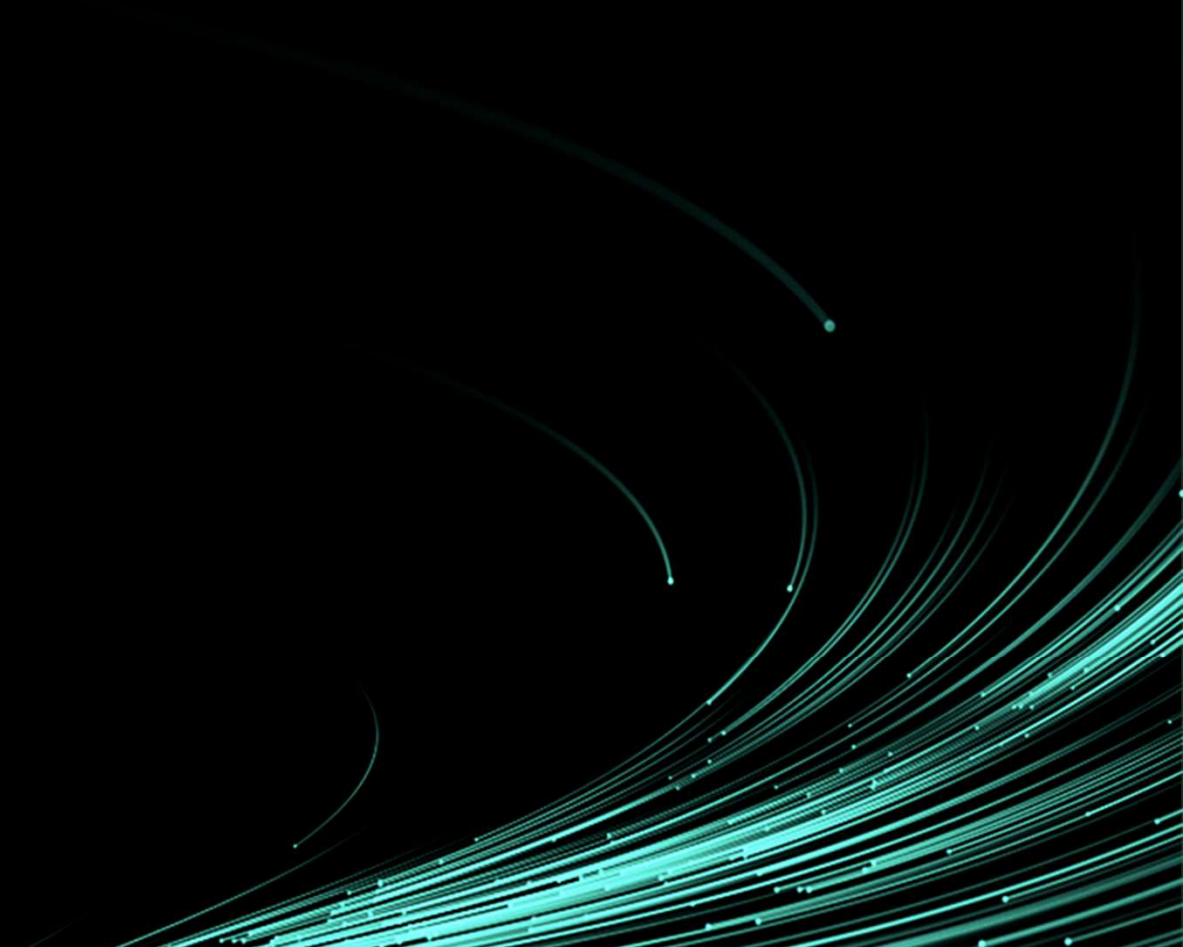


Layout final

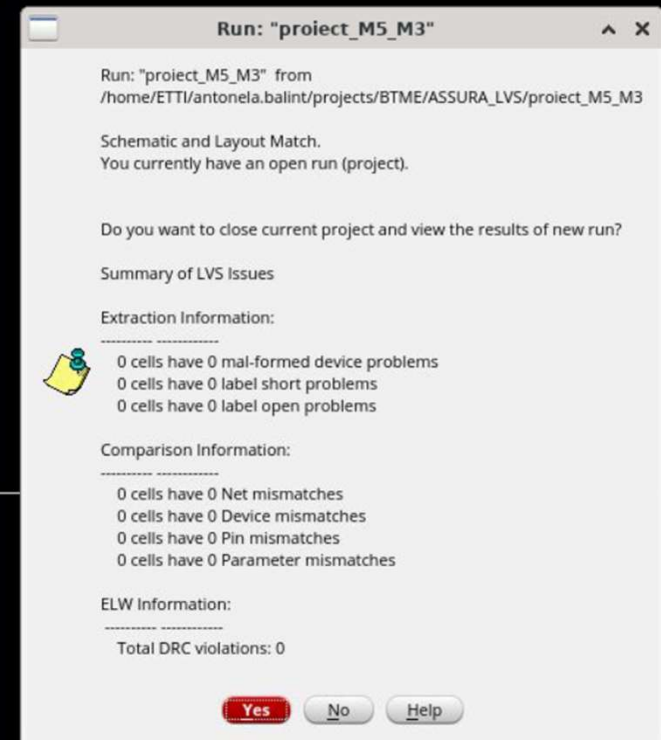


PARTEA 4

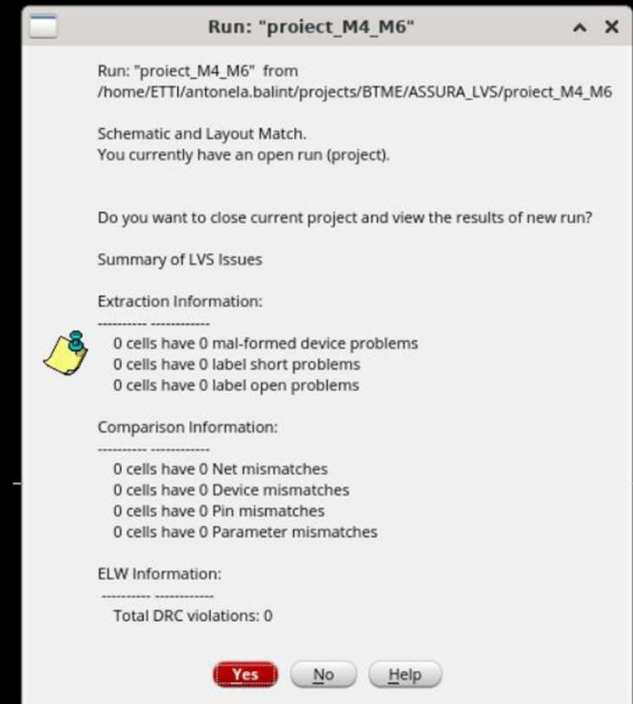
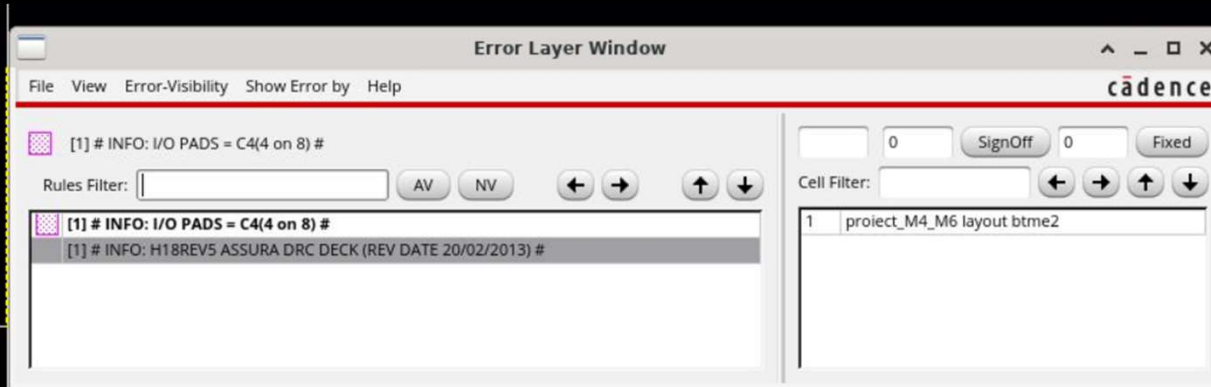
DRC & LVS



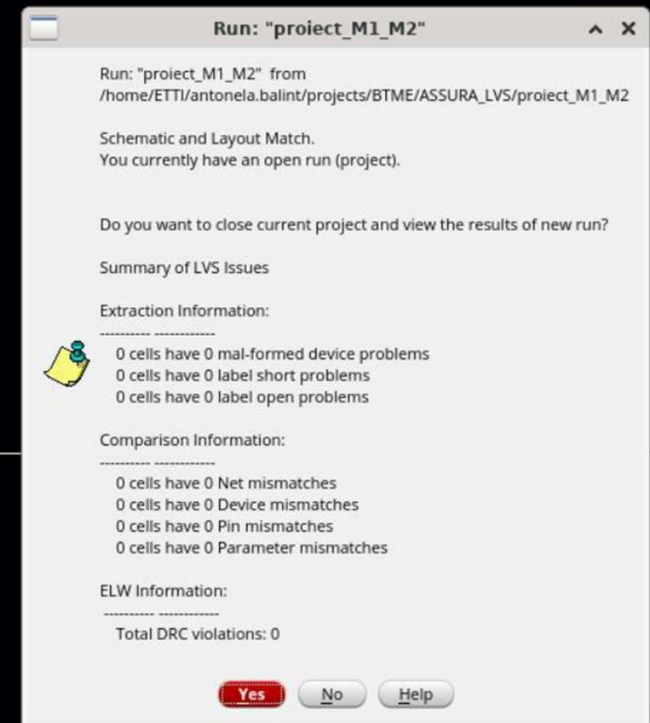
DRC & LVS M5-M3



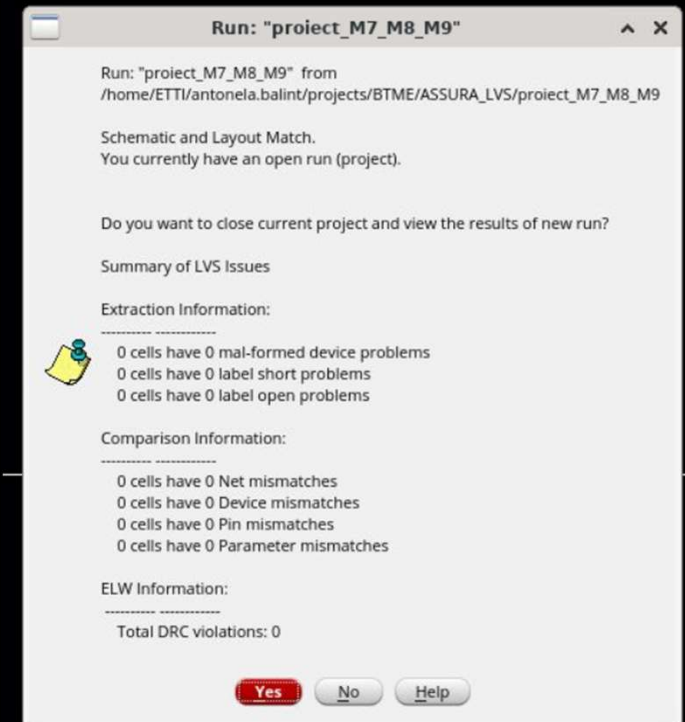
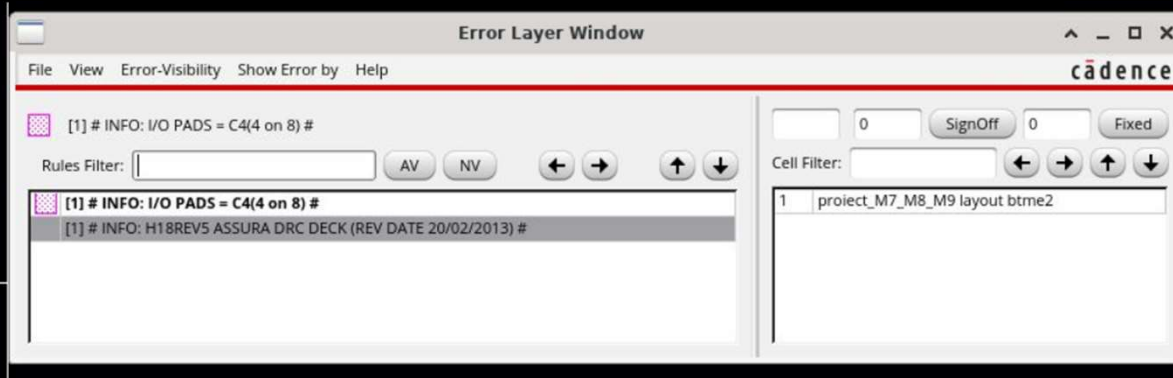
DRC & LVS M4-M6



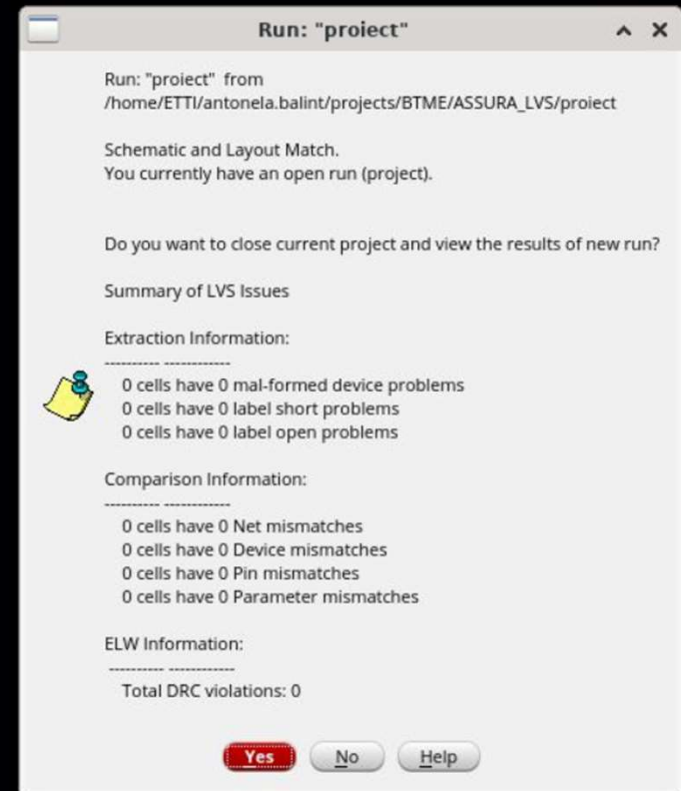
DRC & LVS M1-M2



DRC & LVS M7-M8-M9



DRC & LVS final



Multumesc!

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