

LAN8720A

File: LAN8720A.kicad_sch

Observations:

Voltage supervisor added to safely shut down ESP32 when the power supply feeds less than 2.7V to the source pin.

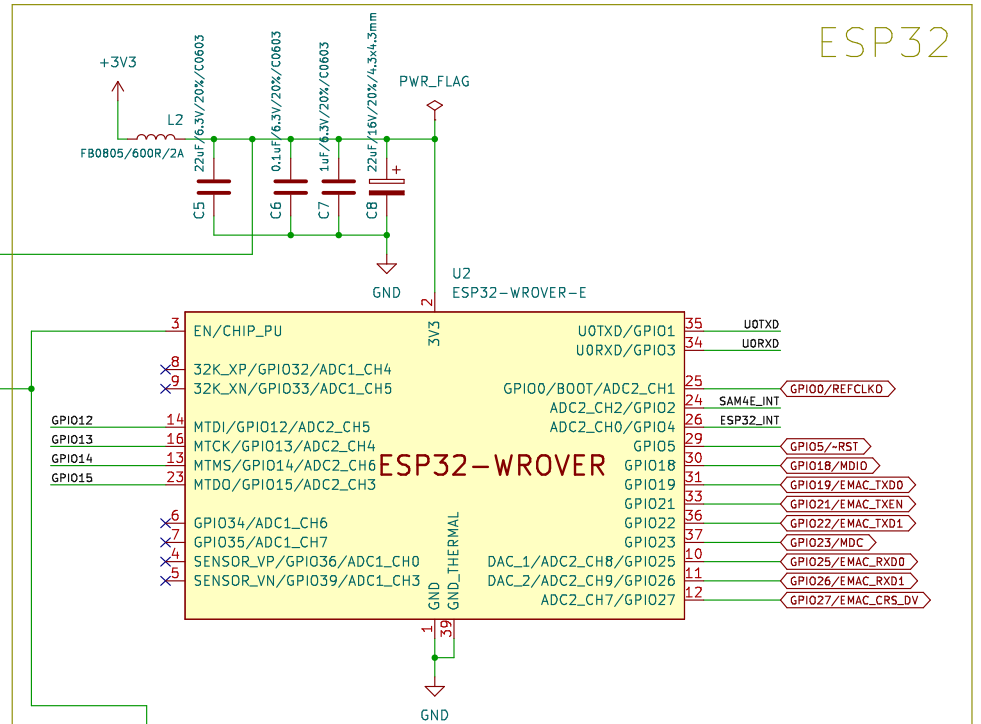
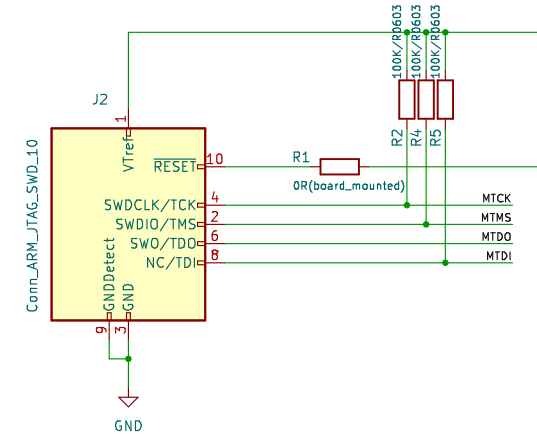
Auto-program (Boot & Enable) is made available via UART interface to flash ESP32.

DTR	RTS	→ EN	IO0
1	1	1	1
0	0	1	1
1	0	0	1
0	1	1	0

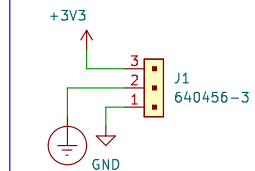
The 00hm resistor may be removed in order to isolate the JTAG port from this system reset signal. Other 00hm resistors are present for debug purposes.

SPI interface and JTAG debug interface share the same 4 pins for communication. The hardware must be configured accordingly for each module to be accessed.

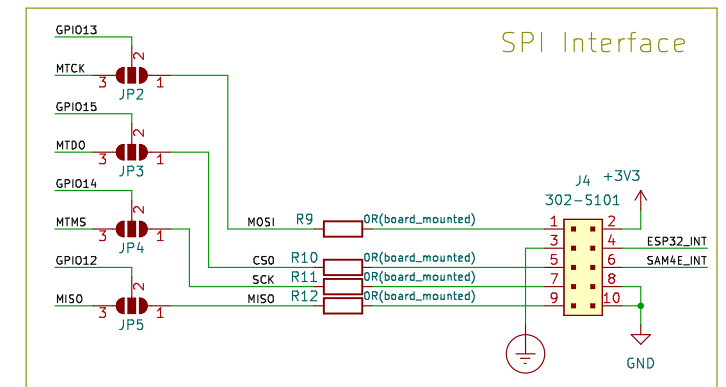
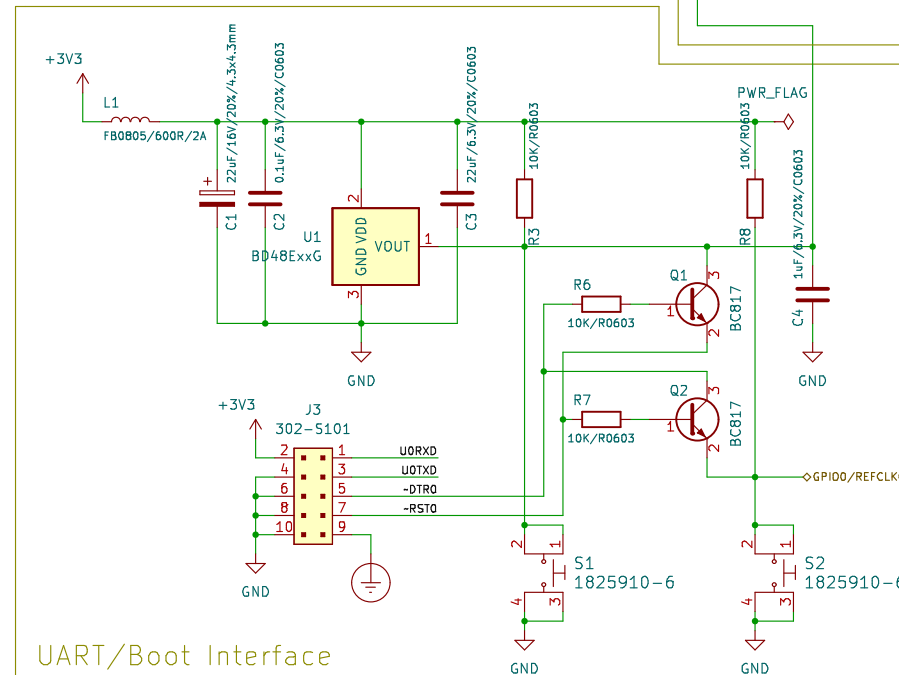
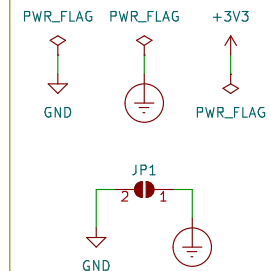
JTAG Interface



Test Points



PWR References



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File: ESP32-PLC.kicad_sch

Title: PLC

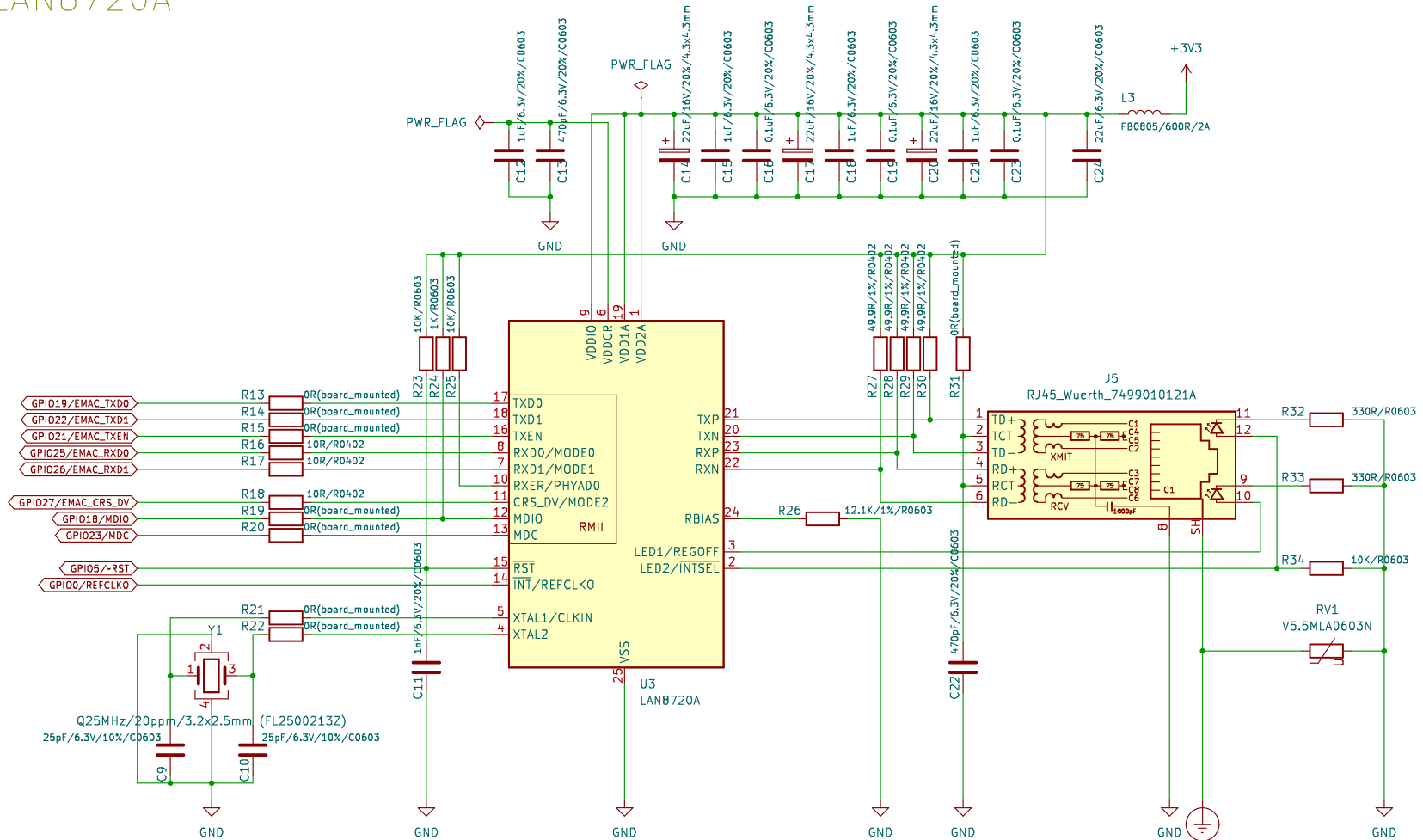
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KiCad E.D.A. kicad 7.0.7-7.0.7-ubuntu20.04.1

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Rev: A
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