



LCD-RTC

[illegible]

The diagram shows a differential RS485-2 circuit. It consists of two 7414 hex inverters configured as a differential line driver. The inputs of the inverters are connected to a 5V supply through 4.7kΩ resistors. The outputs of the inverters are connected to a DS9C03LVCM05 differential line driver. The driver's VCC and GND pins are connected to a 5V supply and ground, respectively. The driver's output pins are connected to the RS485-2 lines, which are terminated with 120Ω resistors. The circuit also includes pull-up and pull-down resistors (4.7kΩ and 10kΩ) to ensure proper signal levels.

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The image displays two circuit diagrams for a 4-to-1 multiplexer (Mux 16-4). Both diagrams use a 74164 16-bit shift register and a 74154 4-to-16 decoder.

Left Diagram: This diagram shows a basic implementation. The 74154 decoder's four outputs (Y0, Y1, Y2, Y3) are connected to the four data inputs (A, B, C, D) of the 74164 shift register. The shift register's output (Q15) is connected to the output of the multiplexer. The 74164 is configured with its clock (CLK) to pin 14, and its master/slave (MS) and enable (EN) pins to pin 15. The 74154 is configured with its enable pins (A, B, C) to pins 1, 2, and 3, and its output pins (Y0-Y15) to pins 10-25.

Right Diagram: This diagram shows an alternative implementation. The 74154 decoder's four outputs (Y0, Y1, Y2, Y3) are connected to the four data inputs (A, B, C, D) of the 74164 shift register. The shift register's output (Q15) is connected to the output of the multiplexer. The 74164 is configured with its clock (CLK) to pin 14, and its master/slave (MS) and enable (EN) pins to pin 15. The 74154 is configured with its enable pins (A, B, C) to pins 1, 2, and 3, and its output pins (Y0-Y15) to pins 10-25. Additionally, the 74154's output pins (Y0-Y15) are connected to a 10k resistor network, which is then connected to the output of the multiplexer.

The circuit diagram shows two RS485 driver sections, labeled RS485-A and RS485-B. Each section consists of a differential pair of MOSFETs (MOS1 and MOS2) driven by a common-mode voltage divider network. The input signals are RS485_A+ and RS485_A- for the first section, and RS485_B+ and RS485_B- for the second. The output lines are connected to a common-mode choke and a termination resistor (120Ω). The power supply is +5V and GND.

The diagram shows the RS485-6 module circuit. It features two MAX485 drivers. The first driver is connected to RS485_A and RS485_B. The second driver is connected to a 16C45 IC, which is also connected to RS485_A and RS485_B. The circuit includes a 12V supply, a 10K resistor, a 1K resistor, a 0.1uF capacitor, and a 10uF capacitor. The output is labeled RS485_A and RS485_B.

CONNECTOR RS485

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