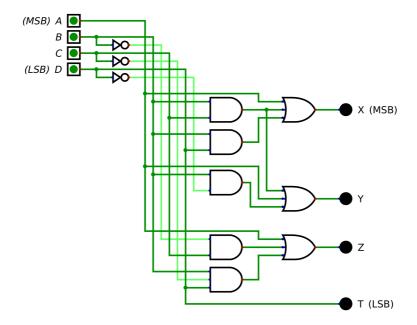
Assigned: 29/03/2023

Preliminary work due: 03/04/2023

Sessions: 03/04-07/04

The circuit in the picture (which is available on SUcourse as $bin_{-}to_{-}2421.dig$) is a binary to 2421 converter, whose design will be discussed in the class.



Use this circuit as a template to design and demonstrated a 4-bit "binary to gray code converter", composed of AND, OR, and INV as your circuit components. Upload the .dig file of your design to SUcourse as your preliminary work (before 12:30.)

During the laboratory session, you will be asked to make modifications and/or additions to the circuit, and submit your design by the end of the session.