Design and Optimization of a Class-E Amplifier for a Loosely Coupled Planar Wireless Power System

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Abstract-In wireless power systems for charging batteryoperated devices, the selection of component values guaranteeing certain desired performance characteristics can be a tedious trial-and-error process, either sweeping component values in circuit simulations or changing components by hand. This difficulty is compounded by the variable nature of the load resistance presented by a device under charge. This brief considers component selection for a specific wireless power system architecture, which is an open-loop class-E inverter using a series-parallel arrangement for load impedance transformation. Formulas for the optimal receiver, transmitter, and class-E components are derived given a set of constraints on the resistance, phase, quality factor, and drain voltage waveform. Using a 16 cm \times 18 cm primary and a $4 \text{ cm} \times 5 \text{ cm}$ secondary coil, the derived formulas are used to build a wireless power system. We show that the system has desirable performance characteristics, including a power delivery of over 3.7 W, peak efficiency of over 66%, and decreasing power delivery with increasing load resistance.

 $\it Index\ Terms$ —Class E, inductive coupling, wireless power transfer.

I. INTRODUCTION

wireless charging system for battery-operated consumer electronics equipped with receiving coils could cut the last wire of portable devices [1], [2]. Near-field inductive coupling has promise as a wireless power technology [3]–[6].

Fig. 1 shows a block diagram for a generalized wireless power system, and the circuit diagram is shown in Fig. 2. The inverter is a class-E amplifier [7] driven by a low-power clock at 240 kHz, followed by a series—parallel impedance transformation network [8]. Selecting the values of $C_{\rm rx}$, $L_{\rm out}$, $C_{\rm out}$, and C_t for optimum performance of the wireless power system presents a challenge. Reference [9] shows the design methodology for a similar architecture with closed-loop control, which allows control of power delivery under variable loading conditions. Reference [10] demonstrates how to choose design values for a class E without relying on Raab's waveform equations; however, it involves numerical root finding. Reference [11] presents a selection technique for an openloop system that relies on numerically sweeping component

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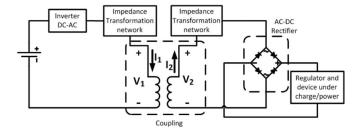


Fig. 1. One-to-one wireless power system block diagram.

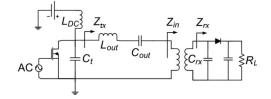


Fig. 2. Class-E driving circuit for a wireless power system.

values until the impedance and drain voltage satisfy certain constraints, and [12] demonstrates the same for a multiple transmitter/receiver system. While this method successfully finds appropriate component values, it is time consuming. This brief derives simple formulas for the optimum component values by applying the same constraints. This open-loop system has the advantage of power regulation under a variable load, without the added complexity and minimum battery supply voltage associated with a closed-loop system as in [9].

II. ANALYSIS

The optimum values for $C_{\rm rx}$, $L_{\rm out}$, $C_{\rm out}$, and C_t can be derived by applying several constraints on the system's response to the variable load resistance R_L . In this analysis, it is assumed that the components are lossless. In addition, assumptions are made about the class E to allow using Raab's equations [13], namely, that the transistor is a perfect switch and that the choke inductance is infinite. Before these derivations, it is necessary to have expressions for receiver impedance, i.e., $Z_{\rm rx}$, input impedance looking into the transmitter coil, i.e., $Z_{\rm in}$, and impedance looking into $L_{\rm out}$, i.e., $Z_{\rm tx}$. $Z_{\rm rx}$ can be expressed as follows:

$$Z_{\rm rx} = R_{\rm rx} + jX_{\rm rx}$$
$$= R_L \|C_{\rm rx}$$
(1)

$$= \frac{R_L - j\omega C_{\rm rx} R_L^2}{1 + \omega^2 R_L^2 C_{\rm rx}^2}.$$
 (2)

 $Z_{\rm in}$ can be found by examining the following coupling equations:

$$V_1 = j\omega L_1 I_1 + j\omega M I_2$$

$$V_2 = j\omega M I_1 + j\omega L_2 I_2$$
(3)

where L_1 , L_2 , and M are the transmitter coil, receiver coil, and mutual inductance, respectively. $Z_{\rm in}$ is V_1/I_1 , i.e.,

$$Z_{\rm in} = R_{\rm in} + jX_{\rm in} = \frac{\omega^2 M^2 R_{\rm rx}}{R_{\rm rx}^2 + (\omega L_2 + X_{\rm rx})^2} + j \left(\omega L_1 - \frac{\omega^2 M^2 (\omega L_2 + X_{\rm rx})}{R_{\rm rx}^2 + (\omega L_2 + X_{\rm rx})^2}\right).$$
(4)

 $Z_{\rm tx}$ is just $Z_{\rm in}$ with an additional series reactance from $C_{\rm out}$ and $L_{\rm out}$, i.e.,

$$Z_{\rm tx} = R_{\rm tx} + jX_{\rm tx}$$

$$= R_{\rm in} + j\left(\omega L_{\rm out} - \frac{1}{\omega C_{\rm out}} + X_{\rm in}\right). \tag{5}$$

A. Design Equation for C_{rx}

Selection of $C_{\rm rx}$ is determined on the basis of efficiency and quality factor Q of $Z_{\rm tx}$. If the real part of $Z_{\rm in}$ is too low compared with the coil parasitics, the system will be inefficient. If it is too large, it is difficult to get Q high enough for class-E operation (about 1.78 [14]). By forcing the peak real part of $Z_{\rm in}$ to be a specified value, i.e., R_0 , a compromise between efficiency and Q can be reached. To derive which $C_{\rm rx}$ forces the maximum real part of $Z_{\rm in}$ to be R_0 , the R_L corresponding to the peak value is found by setting $\partial R_{\rm in}/\partial R_L$ to zero. This yields a polynomial of degree six, where four of the roots are composed of a double conjugate pair and can thus be ignored. Thus

$$R_L = \pm \frac{j}{\omega C_{\rm ry}}.$$
(6)

There are two real roots, i.e.,

$$R_L = \pm \frac{\omega L_2}{1 - \omega^2 L_2 C_{\rm rx}} \tag{7}$$

which can be substituted back into (4); then, the real part is set to R_0 , i.e.,

$$R_{\rm in} = \pm \frac{M}{2L_2} \frac{\omega M}{\omega^2 C_{\rm res} L_2 - 1} = R_0.$$
 (8)

Solving for C_{rx} yields two roots, i.e.,

$$C_{\rm rx} = \frac{R_0 L_2 \pm \frac{1}{2} \omega M^2}{R_0 \omega^2 L_2^2}.$$
 (9)

The negative root gives a $C_{\rm rx}$ value that ensures that the $Z_{\rm in}$ phase will increase with increasing R_L , which has the desirable effect of lowering power delivery at a high load resistance. This is desirable because, in the case of a device being charged, a high load resistance (thousands of ohms) corresponds to a fully charged condition and, thus, a low power requirement.

B. Design Equation for L_{out}

The purpose of $L_{\rm out}$ is to ensure the circuit has a minimum Q high enough for proper functioning of the class E. Q is smallest when the real part of $Z_{\rm in}$ is highest, at R_0 . Since $L_{\rm out}$ contributes the largest part of the reactance of $Z_{\rm tx}$, i.e.,

$$Q \sim \frac{\omega L_{\text{out}}}{R_0} \tag{10}$$

 $L_{\rm out}$ is found to be

$$L_{\text{out}} = \omega^{-1} Q R_0. \tag{11}$$

C. Design Equation for C_{out}

 $C_{\rm out}$ brings the range of the phase of $Z_{\rm tx}$ to a range that allows zero-voltage switching (ZVS) operation of the class E and maximum efficiency. From [15], this phase range is 40° to 70°. By setting the minimum phase to a specified value, i.e., ϕ , efficient operation can be achieved. The location of the minimum phase is where

$$\frac{\partial \angle (Z_{\rm tx})}{\partial R_L} = 0 \tag{12}$$

which yields a quadratic equation in R_L with the roots

$$R_{L} = \pm 2R_{0} \left(\frac{L_{2}}{M}\right)^{2} \times \sqrt{\frac{\omega C_{\text{out}} (\omega L_{1}(k^{2} - 1) - R_{0}Q) - 1}{\omega C_{\text{out}} (\omega L_{1}(k^{2} - 1) - R_{0}(Q + 2)) - 1}}$$
(13)

where $k = \sqrt{M^2/L_1L_2}$. Substituting (13) into (5) and setting $\tan(\phi) = X_{\rm tx}/R_{\rm tx}$ and squaring to eliminate the radical in the numerator yields a quadratic equation in $C_{\rm out}$, with two real roots, i.e.,

$$C_{\text{out}} = \omega^{-1} \left\{ \omega L_1 (1 - k^2) + R_0 \left(Q + 1 \pm \sec(\phi) \right) \right\}$$

$$\times \left\{ R_0^2 \left(Q^2 + 2Q - \tan(\phi)^2 \right) + 2Q R_0 \omega L_1 (1 - k^2) + \omega^2 L_1^2 (1 - k^2)^2 + 2\omega L_1 R_0 (1 - k^2) \right\}^{-1}. (14)$$

The greater root, corresponding to the positive ϕ , yields

$$C_{\text{out}} = \frac{\omega^{-1}}{\omega L_1(1 - k^2) + R_0 (Q + 1 - \sec(\phi))}$$
(15)

after simplification.

D. Design Equation for C_t

Finally, C_t is selected to guarantee ZVS operation of the class E. From [13], this optimum value of C_t , given a load resistance R, is

$$C_t = \frac{2\omega^{-1}}{\left(1 + \frac{\pi^2}{4}\right)R}. (16)$$

Since the load resistance in the wireless power system is variable, C_t is selected based on the maximum R, which, for

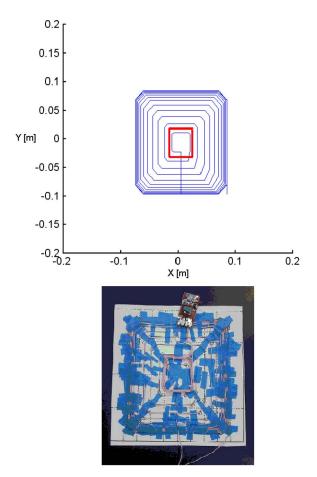


Fig. 3. Test setup. (Top) Coils, where red is the receiver and blue is the transmitter. (Bottom) Coils.

the circuit under consideration, is the magnitude of $Z_{\rm tx}$ as R_L increases to infinity. Taking this limit

$$R = \omega L_{\text{out}} + \frac{1}{\omega C_{\text{out}}} - \omega^2 M^2 \frac{\omega C_{\text{rx}}}{\omega^2 C_{\text{rx}} L_2 - 1}$$
 (17)

and substituting in the derived values for the other components [see (9), (11), and (15)], the optimum C_t is found, i.e.,

$$C_t = \frac{2\omega^{-1}}{\left(1 + \frac{\pi^2}{4}\right)(1 + \sec(\phi))R_0}.$$
 (18)

III. EXPERIMENTAL RESULTS

Having derived the optimum component values for a given L_1 , L_2 , M, R_0 , Q, and ϕ , this section demonstrates the performance of the system.

A test system was built, consisting of a 16 cm \times 18 cm, 13-turn, spiral transmitting coil, which is designed by the technique described in [16], and a rectangular 4 cm \times 5 cm, 6-turn, receiving coil. Both coils were constructed of 100-strand #40 American Wire Gauge Litz wires to minimize coil parasitics. The switching transistor was an IRLR3410, which was chosen because of its low output capacitance (typically below 100 pF), much lower than C_t . The IRLR3410 was driven with a gate voltage of 5 V and a supply voltage of 12 V, corresponding to a turn-on resistance of 0.125 Ω . Fig. 3 shows a picture

TABLE I DESIGN PARAMETERS

Parameter	Value	Parasitic Resistance
L_1 (measured)	34.58 μ H	0.39 Ω
L_2 (measured)	$4.05~\mu{\rm H}$	0.09 Ω
M (measured)	1.65 μH	-
R_0	7.5 Ω	-
\overline{Q}	2	-
ϕ	65°	-

TABLE II COMPONENT VALUES

Component	Calculated	Measured	Parasitic Resistance
C_{rx}	100.55 nF	100.00 nF	0.04 Ω
C_{out}	11.90 nF	11.57 nF	0.03 Ω
C_t	15.15 nF	14.55 nF	0.03 Ω
$\overline{L_{out}}$	9.95 μH	9.52 μH	0.16 Ω

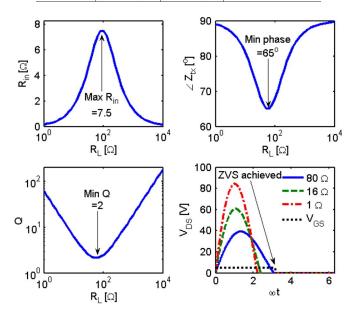


Fig. 4. From top left, clockwise: $R_{\rm in}$, $\angle Z_{\rm tx}$, drain voltage waveform at three R_L values, with a gate voltage for reference, and Q.

of the test system. Based on the proposed method and the equations in Section II, the optimum component values were calculated. Table I gives values of the inductances L_1 , L_2 , and M, which were measured at 240 kHz using an HP 4192A LF impedance analyzer, and the design parameters R_0 , Q, and ϕ . Table II gives the calculated component values and lists the measured (at 240 kHz) values of the actual components being used, as well as their parasitic resistances. R_0 was chosen as 7.5 Ω based on the total component and coil parasitics, which amounted to 0.58 Ω . In general, selection of R_0 is system dependent, but it should be at least an order of magnitude higher than the parasitics, because the peak transmitting coil efficiency will be $R_0/(R_p+R_0)$ (where R_p is the parasitic on the transmitter side). In this case, R_0 of 7.5 Ω is about 13 times R_p , or an efficiency value of about 93%. A larger R_0 value will result in higher efficiency, but how large an R_0 value is acceptable depends on the availability of inductors of sufficient size to achieve minimum Q. Minimum Q and ϕ values were chosen based on allowable values given in [14] and [15], with an additional buffer to tolerate some deviations of real components. Fig. 4 shows the calculated $R_{\rm in}$, $\angle Z_{\rm tx}$,

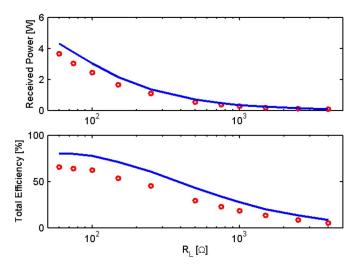


Fig. 5. Received power and total efficiency as a function of R_L . (Circles) Measured values. (Solid line) Simulated using the calculated component values.

drain voltage waveform, and Q, demonstrating that the desired constraints are met using the component selection formulas.

One of the key challenges for a wireless power system is to have desirable performance responding to a variable load. To evaluate the system performance with regard to power and efficiency, R_L was swept from 60 to 4000 Ω by means of an electronic load. The dc voltage and current were measured at the load and at the supply. The transmitted power was measured using a current probe (Agilent N2783A), a voltage probe (Agilent N2863A), and an oscilloscope (Agilent DSO 5034A), whereas the received power was measured using the dc electronic load (BK 8500).

Fig. 5 shows the received power and total efficiency versus load resistance, as well as the simulated values of power and efficiency, using the ideal calculated component values. The system has power delivery of over 3.7 W and peak efficiency of over 66%. The important feature in Fig. 5 is the trend of decreasing received power and total efficiency with increasing R_L , which is guaranteed by component selection. This trend is what allows the system to charge a battery and be open loop, contrasted with closed-loop wireless power systems such as [9]. A charging battery looks like a resistance (represented by R_L), which increases with increasing charge. Thus, as the battery approaches full charge, the system would deliver less and less power. The ideal performance (solid line) is close to the real system (circles). Power and efficiency are lower by 5% to 10% in the actual system, primarily due to the deviation of L_{out} and C_{out} in the real components. Deviations from their ideal values cause ϕ and Q to shift, which has a substantial effect on the class-E efficiency.

To further investigate the sensitivity to component selection, a Monte Carlo simulation was run, assuming the components are normally distributed, with means given by the derived component formulas and with standard deviations σ , such that 3σ is the component tolerance. These simulations were carried out at component tolerance levels of 5%, 10%, and 20%. Fig. 6 shows the 95% confidence intervals for the received power and total efficiency at the three tolerance levels. As can be seen, the power is skewed low, with tolerances (in terms of upper

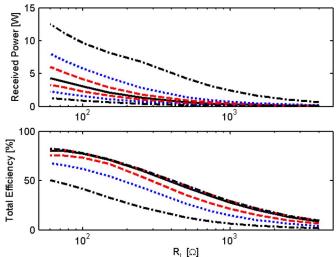


Fig. 6. Received power and total efficiency as a function of R_L , as well as their 95% confidence intervals with (red dashed line) 5% component tolerance, (blue dotted line) 10% component tolerance, and (black dash-dotted line) 20% component tolerance.

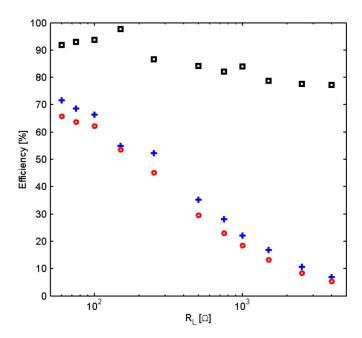


Fig. 7. Efficiency as a function of R_L . (Black squares) Class-E efficiency. (Blue pluses) Coupling efficiency. (Red circles) Total efficiency.

bound/lower bound, as a percentage difference from the mean) of about +35/-25% at 5% component tolerance, +115/-45% at 10% component tolerance, and +425/-70% at 20% component tolerance. The efficiency is skewed very high, with tolerances of about +3.4/-18% at 5% component tolerance, +3.6/-38% at 10% component tolerance, and +4.3/-70% at 20% component tolerance. This skew low in the power confidence intervals and skew high in the efficiency confidence intervals shows that the system is not optimized for maximum power delivery, but rather efficiency. This makes sense, as all of the constraints (R_0 , ϕ , Q, and ZVS) used for component selection are chosen to maximize the efficiency of the class E. At a tolerance of 5% or less, the confidence interval shows

that the performance is still decent. For greater tolerances, the potential variability of performance is probably unacceptable for most applications.

Fig. 7 shows the amplifier efficiency, coupling efficiency, and total efficiency. The amplifier efficiency is ac transmitting power over dc input power; the coupling efficiency is dc power at the load over ac transmitted power, and the total efficiency is the product of these two efficiency values. In Fig. 7, the coupling efficiency comprises most of the losses. The coupling efficiency decreases with increasing load resistance, because as R_L increases, $R_{\rm in}$ decreases, whereas the parasitics remain the same; thus, more power is dissipated through the parasitics, the dominant losses being in the transmitting coil and in L_{out} . Thus, while component selection is performed to optimize the amplifier efficiency, it does nothing for the coupling efficiency. The coupling efficiency could be improved by mitigating parasitics or increasing the mutual inductance. The amplifier efficiency peaks where $\angle Z_{\rm tx}$ is in the 40° – 70° range, which by design is in the neighborhood of the phase minimum.

IV. CONCLUSION

This brief has presented a set of design equations for optimizing the performance of a class-E amplifier used in an inductively coupled wireless power system. By applying constraints on the real part of the input impedance to the primary coil, the phase of the input impedance, the minimum Q, and the drain voltage waveform, components can be selected to guarantee desirable operating characteristics of the system, namely, the ZVS operation of the class E and the trend of decreasing power received with increasing load resistance. The proposed optimization method was tested in a system composed of a 16 cm \times 18 cm primary coil and a 4 cm \times 5 cm secondary coil with a variable load. The system shows power delivery of over 3.7 W and peak efficiency of over 66%, in addition to the desirable trend of decreasing power and efficiency with respect to increasing load resistance.

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