hw6

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1 Problem 4

Resolution table for subtype X01LH of std_{logic}

	'X'	'0'	'1'	'L'	'H'
'X'	'X'	'X'	'X'	'X'	'X'
'0'	'X'	'0'	'X'	'0'	'0'
'1'	'X'	'X'	'1'	'1'	'1'
'L'	'X'	'0'	'1'	'L'	'X'
'H'	'X'	'0'	'1'	'X'	'H'

2 Problem 5

Assume R initially Z and type is X01Z

Time (ns)	R(0)	R(1)	R(2)	Resolution
0	'Z'	'Z'	'Z'	'Z'
1	'0'	'Z'	'0'	'0'
2	'0'	'Z'	'0'	'0'
3	'1'	'Z'	'0'	'X'
4	'1'	'1'	'0'	'X'
5	'1'	'1'	'1'	'1'
6	'1'	'1'	'1'	'1'
7	'Z'	'1'	'1'	'1'
8	'Z'	'Z'	'1'	'1'
9	'Z'	'Z'	'Z'	'Z'