

April 10, 2025

## Syntax Directed Definition (SDD) :

Syntax directed definitions are a generalization of Context Free Grammars in which:

- 1) Grammar symbols have an associated set of attributes
- 2) Productions are associated with semantic rules for computing values of the attributes.

eg:

CFG:

$$E \rightarrow E + T$$

SDD:

$$E \rightarrow E + T$$

attribute  
associated  
with E/E/T

$$\{ E.\text{val} \rightarrow E.\text{val} + T.\text{val} \}$$

Semantic Rule  
( $E.\text{val} \rightarrow E.\text{val} + T.\text{val}$ )

এটা val এর মধ্যে same value নয়।  
কারণ কোর নয়, সুন্দর নয়। So no connection between  
the E.val's other than this SDD formula.

## Syntax Directed Translation:

Syntax Directed Translation is done by attaching rules or program fragments to productions in grammar.

eg:

SDT :  $E \rightarrow E + T \{ \text{print } "i" \}$

Q) What's the difference bet<sup>n</sup> SDD & SDT?

eg:

SDD:

CFG

$S \rightarrow E$

Semantic Rules

$\{ S.\text{val} = E.\text{val} \}$

$E \rightarrow E_1 + T \{ E.\text{val} = E_1.\text{val} + T.\text{val} \}$

$E \rightarrow T$       ↑  
                ↑  
                1 free  
                separate  
                 $\downarrow$   
                 $\downarrow$   
                 $\downarrow$   
                 $\downarrow$

$T \rightarrow T_1 * F \quad \{ T.\text{val} = T_1.\text{val} * F.\text{val} \}$

$T \rightarrow F \quad \{ T.\text{val} = F.\text{val} \}$

$F \rightarrow \text{digit} \quad \{ F.\text{val} = \text{digit}. \text{lexval} \}$

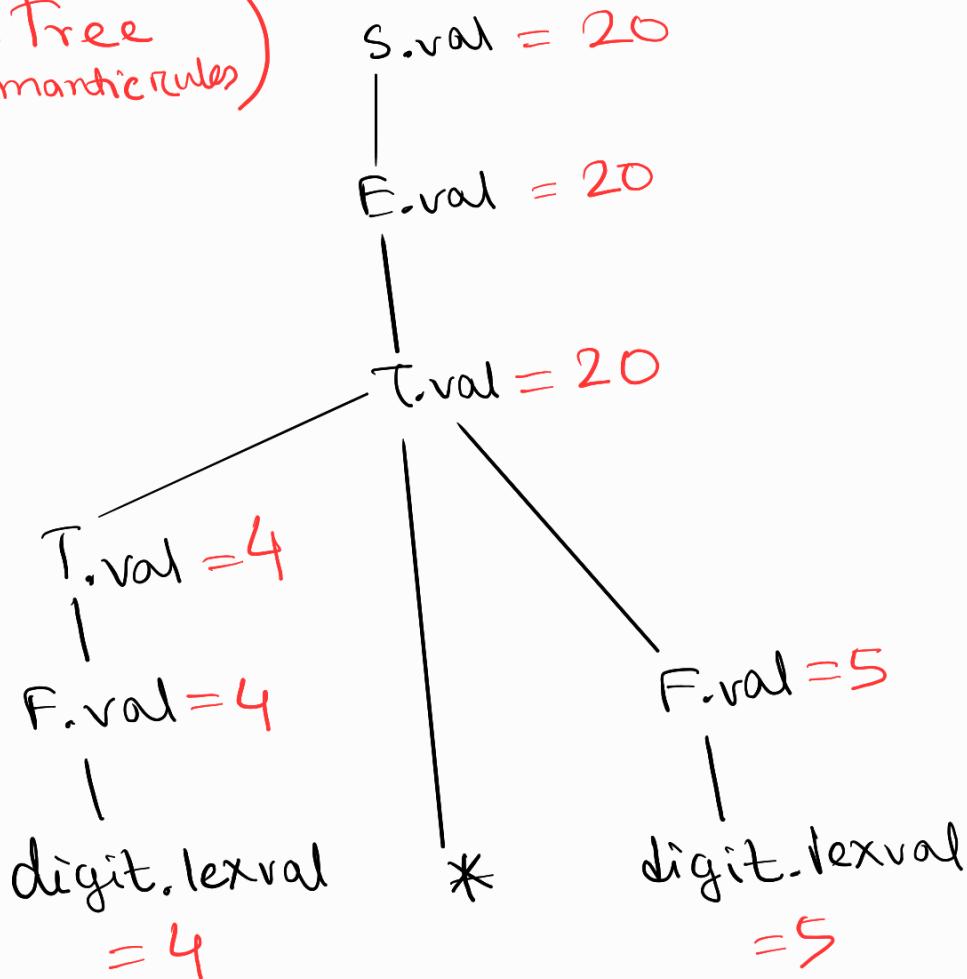
→ input string: 4 \* 5

∴ tokens: digit \* digit

SDT  
rule  
Semantic rule  
CFG

## ; Annotated Parse Tree:

(Parse Tree  
with semantic rules)



(Q) Write the different types of attributes

Two Types of Attributes:

1) Synthesized Attribute: A synthesized attribute at Node N is defined in terms of attribute values at the children of N and at N itself.

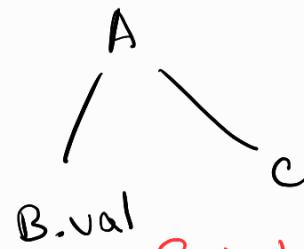
eg:  $A \rightarrow BC \quad \{A - \overbrace{\phantom{A}}^{\downarrow} \}$

so like  $\begin{array}{c} A \cdot \text{sum} \\ | \\ B \end{array}$   $\rightarrow$  val की synthesized attribute  
 $\begin{array}{c} A \cdot \text{val} \\ | \\ B \end{array}$   $\rightarrow$  if it uses any attribute from child nodes B/C or from another attribute of A like sum.

## 2) Inherited Attribute: An inherited attribute

at Node N is defined only in terms of attribute values at N's parent, N itself and N's siblings.

eg:  $A \rightarrow BC$



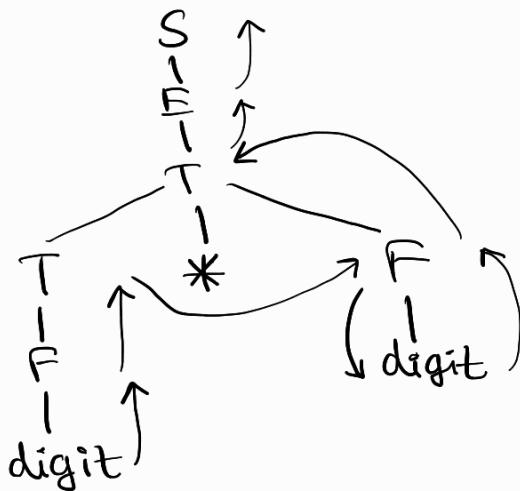
यदि inherited attribute इसे यदि  
B का parent A का B का attribute जो more than  
one attribute used है, तो B का अपना attribute  
used है, तो sibling node C का attribute  
used है।

So like  $B.val = B.val + C.val + A.val$  एकी example

## Dependency Graph:

A dependency graph helps us to determine how the attribute values can be computed. It tells us about which order we should evaluate the tree.

eg:

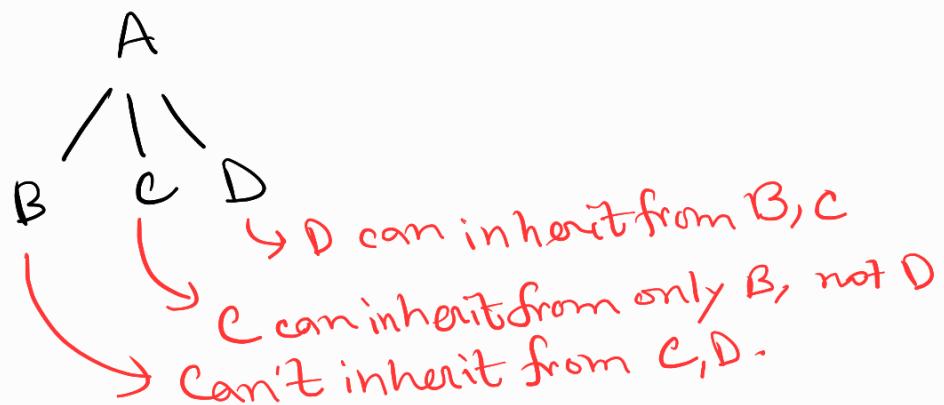


Q) Draw a dependency graph for a CFG

## Evaluation orders for SDD :

- S attributed definitions → A SDD is S-attributed if every attribute is synthesized.
- L attributed definitions → If a SDD uses both Synthesized and Inherited attribute with a restriction that inherited attributes can inherit from the left sibling only, then we can call that SDD a L-attributed SDD.

eg:



3:30 class  
April 12

## Type expression:

eg SDD

CFG

$T \rightarrow BC$

Semantic Rules

$$\left\{ \begin{array}{l} T.t = C.t \\ C.b = B.t \end{array} \right\}$$

$B \rightarrow \text{int}$        $\{B.t = \text{integer}\}$

$B \rightarrow \text{float}$        $\{B.t = \text{float}\}$

$C \rightarrow [\text{num}] C_1$        $\{C.t = \text{array}(\text{num}.val, C_1.t)\}$

$\text{C}_1.b = C.b$

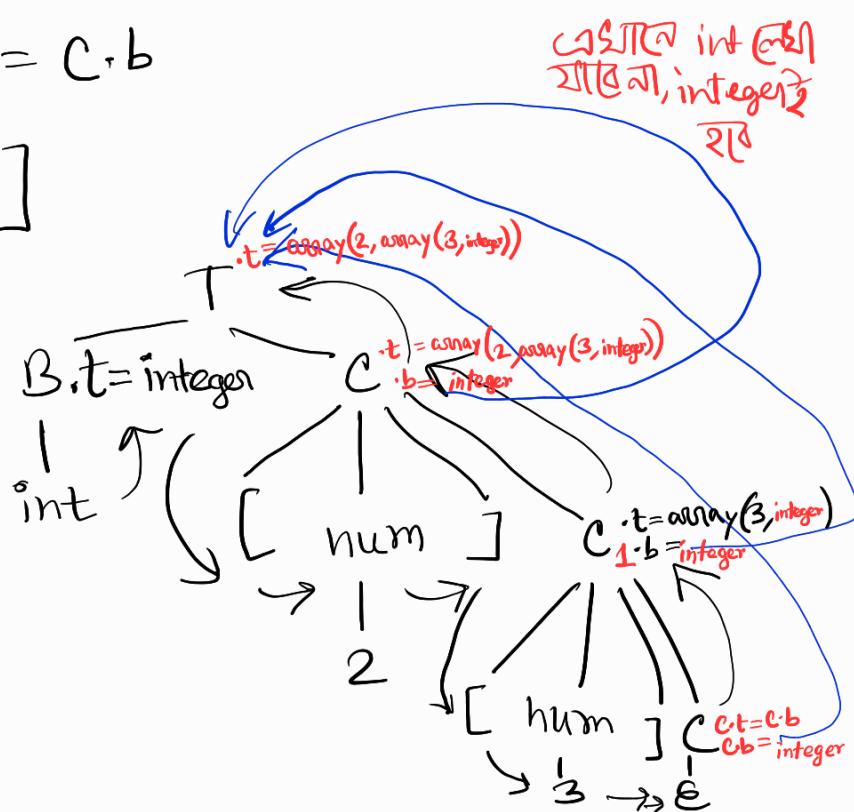
$C \rightarrow E$        $C.t = C.b$

Input string:  $\text{int}[2][3]$

Annotated ParseTree:

$E \rightarrow C$  reduction  
problem  
 $C.b$  exist  
 $C.t = C.b$  hence root node referred.

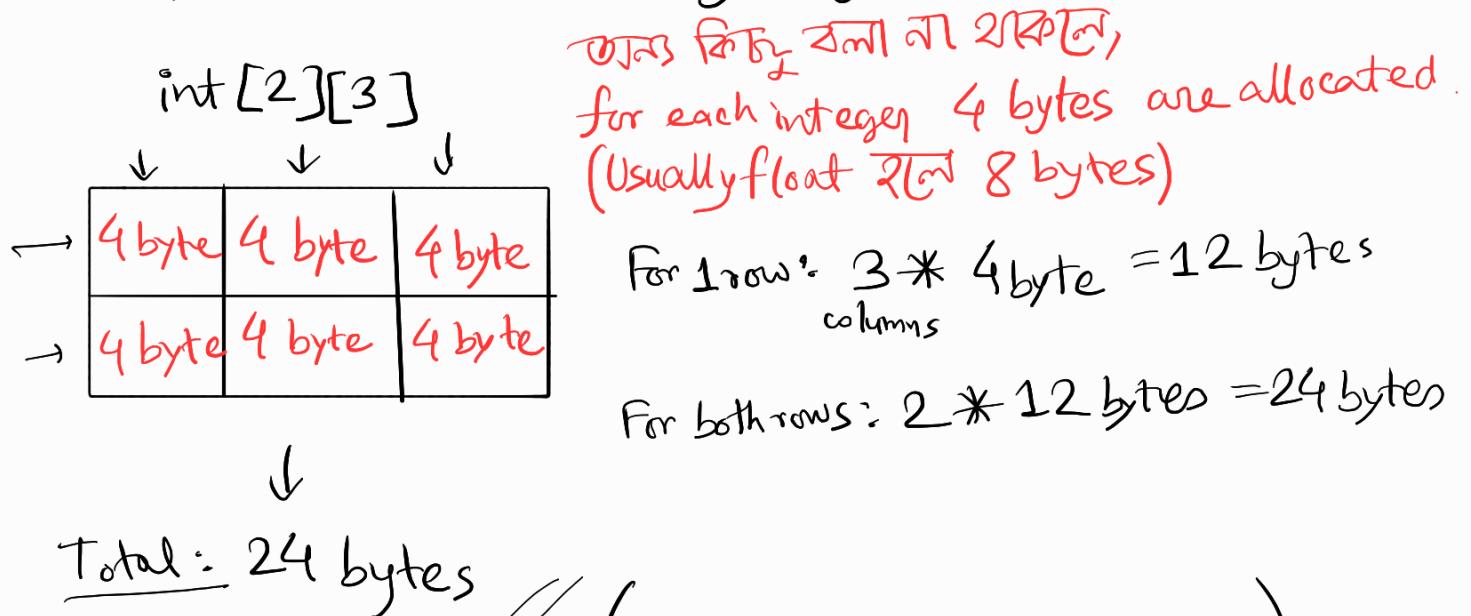
Only after  $T$  Sem Rule  $C.b = B.t$   
execute, update



Thus,

T.T (Q2) The "array(2, array(3, integer))" is the "type expression" for input string "int[2][3].

- With the help of this type expression, computer can allocate memory in your system.



- (calculation questions तोड़ाए)
- (Draw Dependency graph for given tree)
- (Draw parse tree from given SDD and code)

Types:

- Basic Type  $\rightarrow$  int, float, double, char
- Composite Type  $\rightarrow$  array linked list

**Eg:**

CFG

$T \rightarrow BC$

Semantic Rules

$$\left\{ \begin{array}{l} T.t = C.t \\ C.b = B.t \end{array} \right\}$$

**Basic Type**  
 $B \rightarrow \text{int}$   
 $B \rightarrow \text{float}$

$\left\{ \begin{array}{l} B.t = \text{integer} \\ B.t = \text{float} \end{array} \right\}$

**Composite Type**  
 $C \rightarrow [num] C_1$

$\left\{ \begin{array}{l} C.t = \text{array(num.val, } C_1.t) \end{array} \right\}$

$C_1.b = C.b$

$C \rightarrow E$

$C.t = C.b$

- $E \in$  কোনো Type
- $E \in$  না
- $E$  এর variable both Basic and Composite হাতে  
ৰাখো এখন আলাদা  
rule'এ রাখো  
ক্ষেত্ৰে

### Sequence of Declarations:

Refers to how declarations are handled within a grammar's production rules.

**Eg**

$P \rightarrow \{ \text{offset} = 0; \}$

$P$  produces offset = 0 and  
then D.

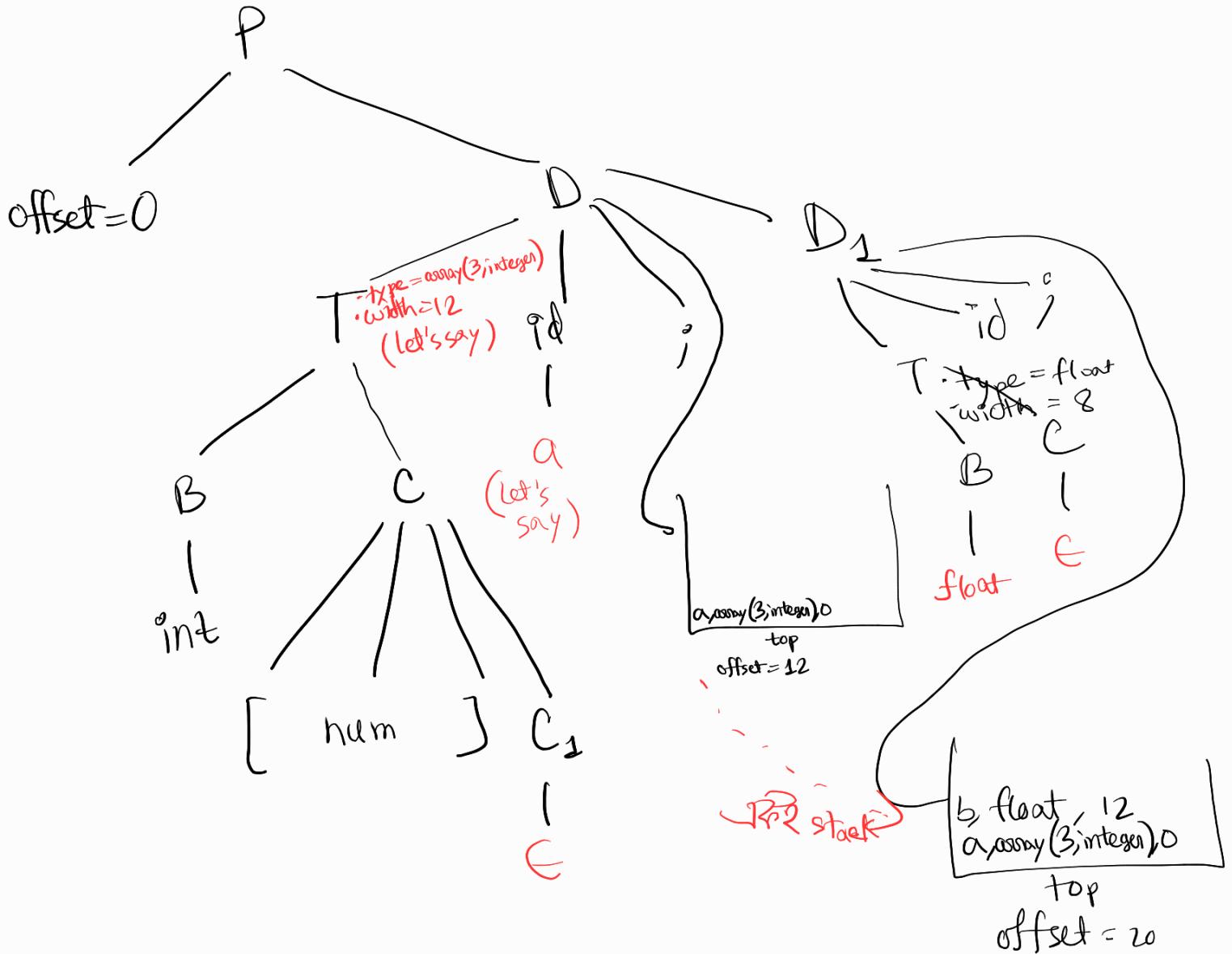
$D \rightarrow Tid ; \left\{ \begin{array}{l} \text{top.put(id.lexeme, } T.type, \text{offset}); \\ \text{offset} = \text{offset} + T.width; \end{array} \right\}$

$D$  produces Tid; { ... }  $\Rightarrow D_1$

sequence of 2 nodes

$D \rightarrow E$

$D$  produces epsilon



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SOP:

$P \rightarrow \{ \text{offset} = 0 \} D$

$D \rightarrow T \text{id} ; \{ \text{top.push(id.lexeme, T.type, offset);}$   
 $\text{offset} = \text{offset} + T.\text{width}; \}$

$D_1$

$D \rightarrow E$

$T \rightarrow BC \{ T.\text{type} =$   
 $T.\text{width} =$

$\}$

$B \rightarrow \text{int} \{ B.\text{type} =$

$C \rightarrow [\text{num}] C_1 \{ C.\text{type} =$

$C \rightarrow E$

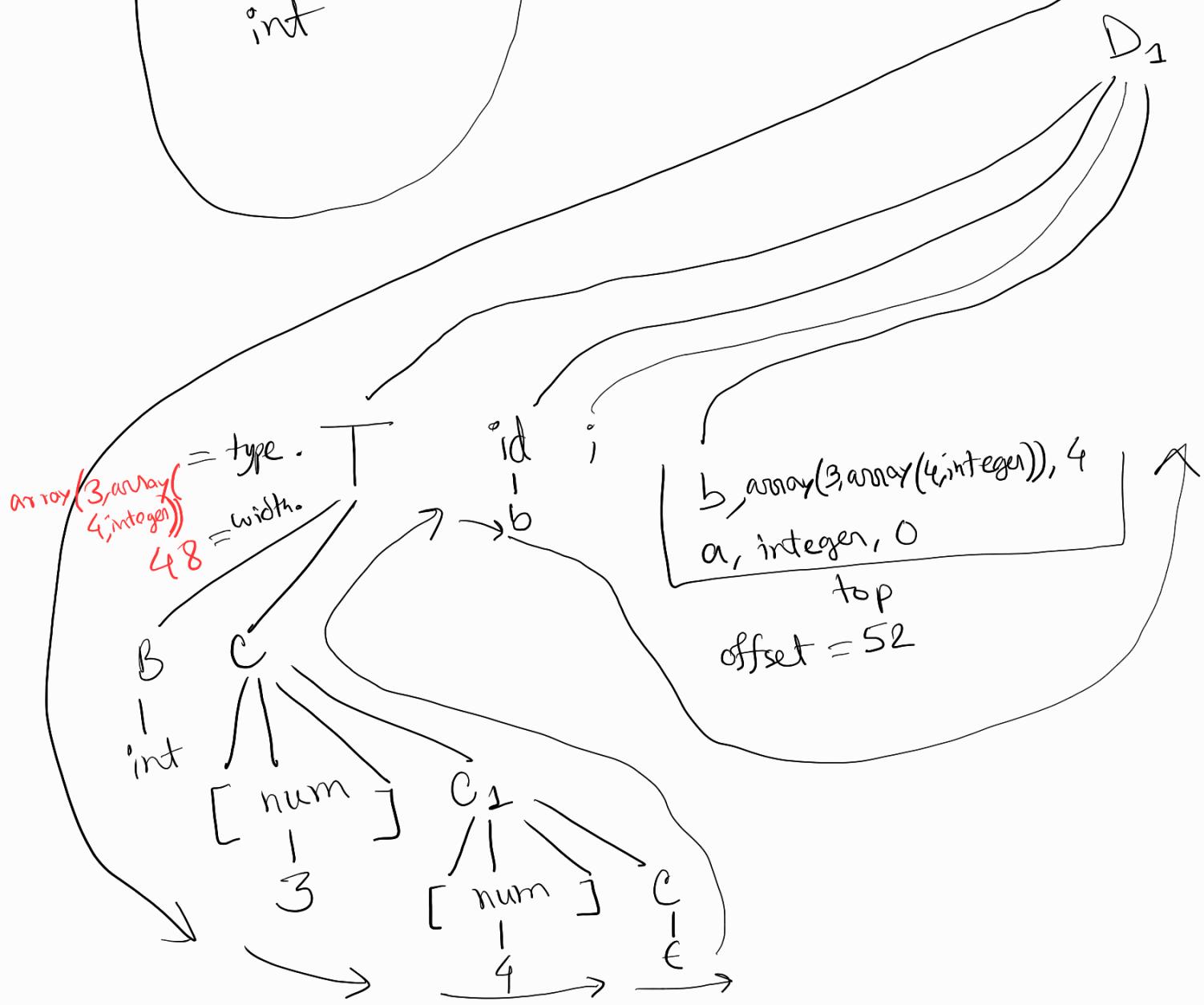
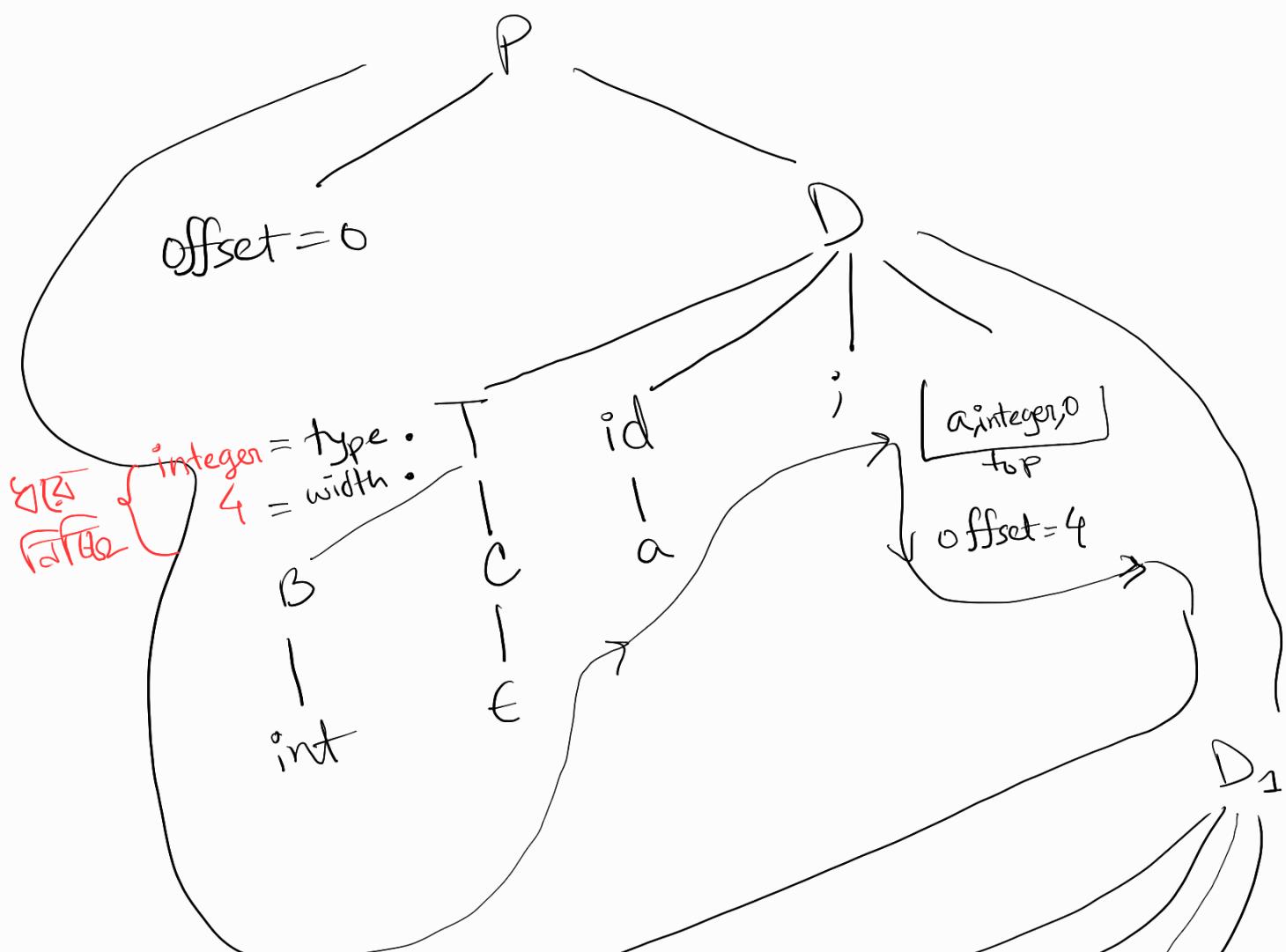
Quiz 3: May 1<sup>st</sup>/3<sup>rd</sup>  
Syllabus:

Assignment deadline:  
May 29

inp str:

int a;  
int [3][4] b;

Tree:



SDD:

$$D \rightarrow T \text{id}; D_1 | \epsilon$$

$$T \rightarrow BC | \text{record } 'l' D_1 'r'$$

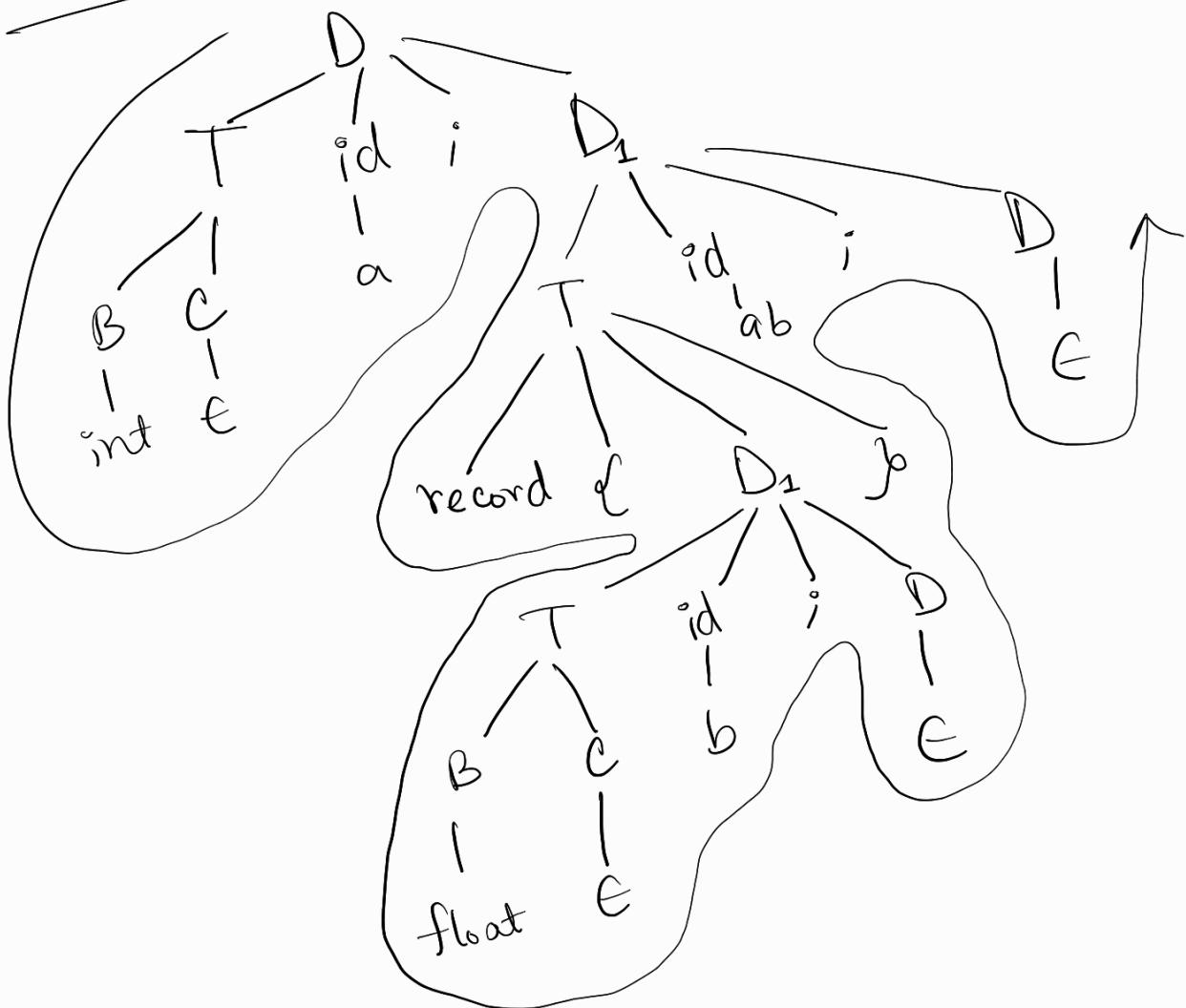
$$B \rightarrow \text{int} | \text{float}$$

$$C \rightarrow \epsilon | [\text{num}] C_1$$

input string:

```
int a;  
record {  
    float b;  
}ab;
```

Tree:



## Assignment SDD grammar:

$P \rightarrow \{ \text{offset} = 0; \} \rightarrow D$

$D \rightarrow T \text{ id}; \{ \text{top.put}$

$\} \rightarrow D_1$

$D \rightarrow E$

$T \rightarrow B$

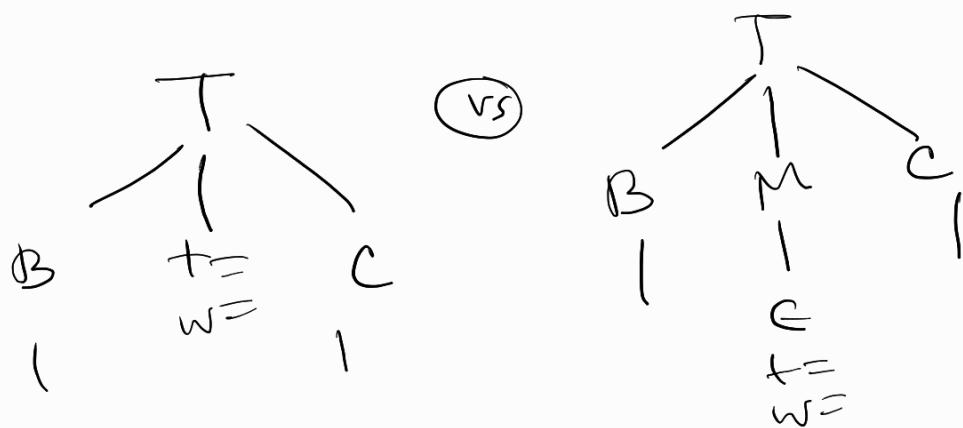
## Using Dummy NonTerminals for semantic rules

SDD:

$T \rightarrow B \{ t = B.\text{type} ; w = B.\text{width} \}$

$T \rightarrow B M C$        $C \xrightarrow{\text{Dummy NT}}$

$M \rightarrow E \{ t = B.\text{type} , w = B.\text{width} \}$



No difference!

Same day, বিকাশের class :

SDD:

P → {offset = 0} D

D → T id ; {top.push(id.lexeme, T.type, offset);  
offset = offset + T.width} D<sub>1</sub>

D → E

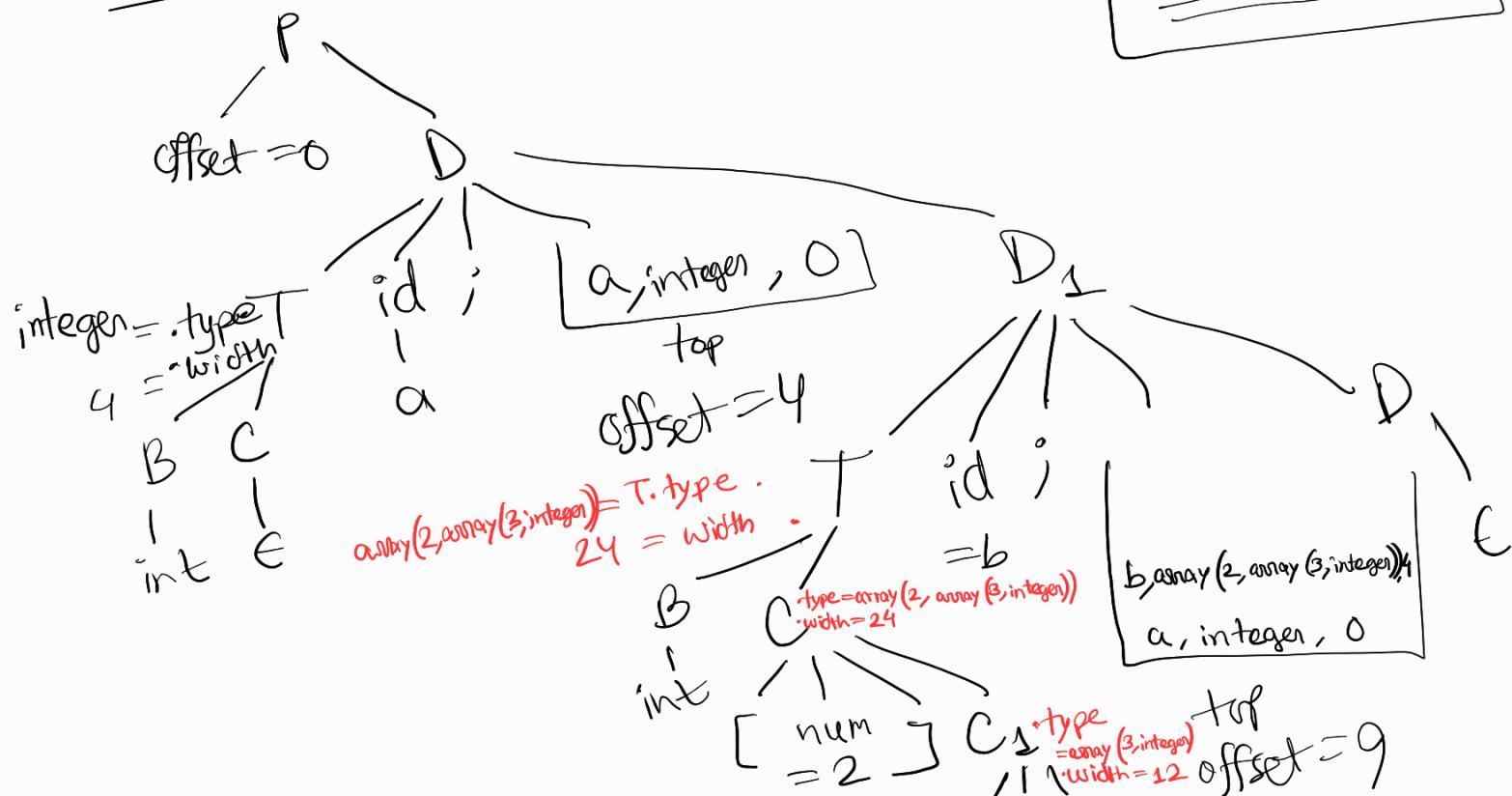
T → BC {T.type = C.type T.width = C.width}

B → int {B.type = integer B.width = 4}

C → [num] C<sub>1</sub> {C.type = array(num.value, C<sub>1</sub>.type);  
C.width = C<sub>1</sub>.width \* num.val}

C → E {C.type = B.type;  
C.width = B.width}

Tree:



inputstr:  
int a;  
int [2][3] b;  
=

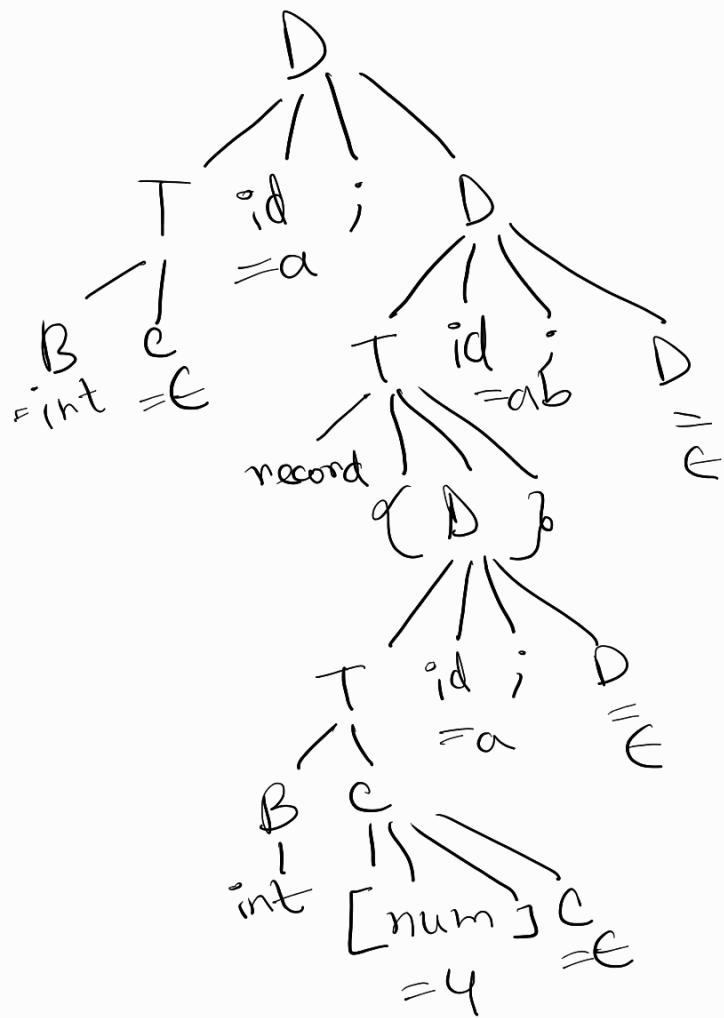
$\left[ \begin{array}{l} \text{num} \\ = 3 \end{array} \right]$  C.type = integer  
 C.width = 4  
 $\epsilon$

SDD:

$D \rightarrow T \text{id}; D \mid \epsilon$   
 $T \rightarrow BC \mid \text{record } \{ D \} D \}$   
 $B \rightarrow \text{int} \mid \text{float}$   
 $C \rightarrow \epsilon \mid [\text{num}] C$

input string

int a;  
 record {  
 int[4] a;  
 } ab;



April 19 [Online Class @ 7 PM]

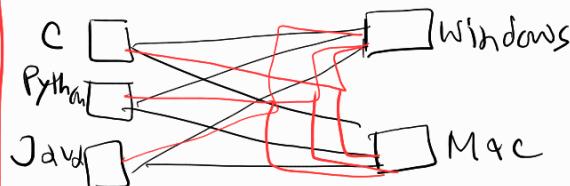
## Intermediate Code Generation

→ Generation of T.A.C (Three Address Code) that's machine independent.

Q) Why do we generate Machine Independent Code? What are the advantages?

m X n Compiler

Q) की प्रिया? see book



Benefits of dividing Compilation steps into Frontend & Backend:

যেকোনো নতুন Frontend কি যেকোনো নতুন Backend এর সাথে কোনো সম্পর্ক নেই।

In every line,  
highest 6 bit address থারে  
highest 2 bit operator থারে

### Three Address Code Generation:

Input code:

$$a = b + c + d$$

T.A.C. :

$$\begin{aligned} t1 &= b + c \\ t2 &= t1 + d \\ a &= t2 \end{aligned}$$



Temporary variables  
Used কোনো ঘোষণা নেই

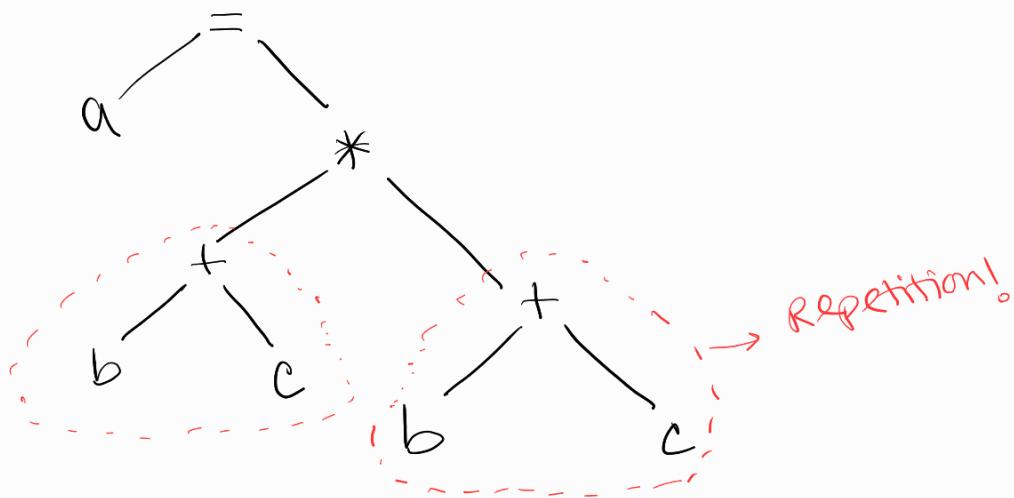
Optimized T.A.C.:

$$\begin{aligned} t1 &= b + c \\ a &= t1 + d \end{aligned}$$

Question 4 Optimized কীভুল  
পুরুষ কৃত্তি লাইসেন্স,  
বা কোনো your choice

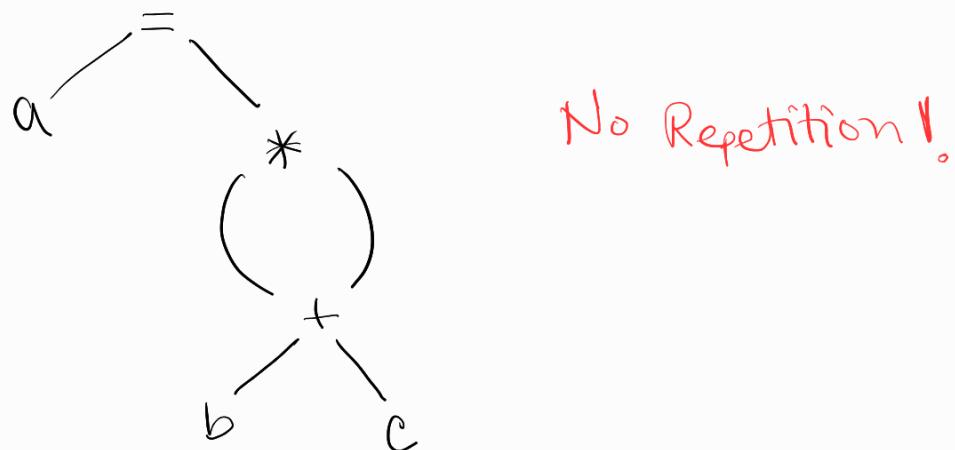
## DAG → Directed Acyclic Graph

Input str:  $a = (b+c)* (b+c)$  Brackets are ~~not part~~  
Syntax Tree:  
in BNF, not part of  
inp. str.



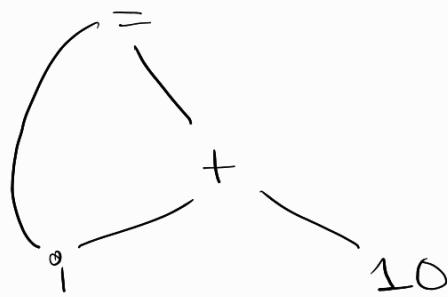
DAG u no subtrees can be repeated!

## DAG:



eg    inp str:  $i = i + 10$

DAG:



Array of Records:

0	id		
1	num		
2	+	0	1
3	=	0	2
4			

Index 0 is set value  
at 1 " " " addition  
Index 2

Index 0 is set value  
to what's stored in  
index 2.

H/W#  $a + a * (b - c) + (b - c) * d$

Draw the DAG for this expression  
before next class!

Also draw Array of Records

## Three Address Code generation Instructions/Rules:

### 1. Assignment Instructions:

$$x = y \quad op \quad z$$

### 2. Assignment Instructions for Unary Operations :

$$\begin{aligned} &\rightarrow a = -c \\ &\cdot a = \text{int}(c) \end{aligned}$$

Operands in total  
number in 2

$$x = op \quad y$$

### 3. Copy Instructions:

$$x = y$$

### 4. Unconditional Jump:

goto L

L: ---  
---  
---

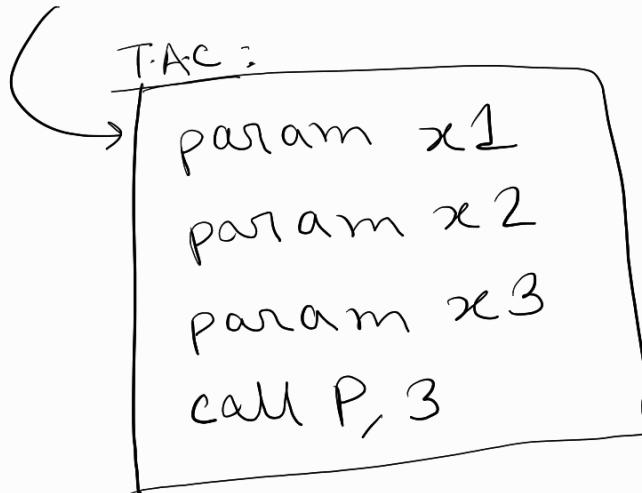
### 5. Conditional Jump:

if False x goto L

### 6. For Loop (প্রিও সেটিং)

## 7. Function :

eg:  $P(x_1, x_2, x_3)$



April 24

TAC Representations

Quadruples

Triples

Indirect Triples

$x = \&y \rightarrow x \text{ equals address of } y.$

$x = *y \rightarrow x \text{ equals value in the variable stored by pointer } y$

$*x = y \rightarrow \text{The variable that's being pointed by } x \text{ will have the value stored in } y.$

eg

$$a = b * -c + b * -c$$

Non-optimized Three Address Code:

Unary minus ↗  
↙ generates for loop

$$t1 = -c \quad \text{in } t1 = \text{minus } c$$

$$t2 = b * t1$$

$$t3 = \text{minus } c$$

$$t4 = b * t3$$

$$t5 = t2 + t4$$

$$a = t5$$

↙ ~~w82~~

"uminus c" 322

For this,

# Quadruples Representation:

(Array of Records)

जहाँ arg1 पर दृश्यता रखें, न करें।

Quadruples  
ref. Numbers

	OP	arg1	arg2	result
0	minus	c		t1
1	*	b	t1	t2
2	minus	c		t3
3	*	b	t3	t4
4	+	t2	t4	t5
5	=	t5		a

# Triple's Representation:

ক্ষেত্রে Triple's Representation Numbers (indices সংজ্ঞা)  
 যেখানে T-A-C এর instruction number denote করে, যাই  $T$  result column  
 এবং  $A$  এর মান।

	OP	arg 1	arg 2
0	minus	c	
1	*	b	(0)
2	minus	c	
3	*	b	(2)
4	+	(1)	(3)
5	=	a	(4)

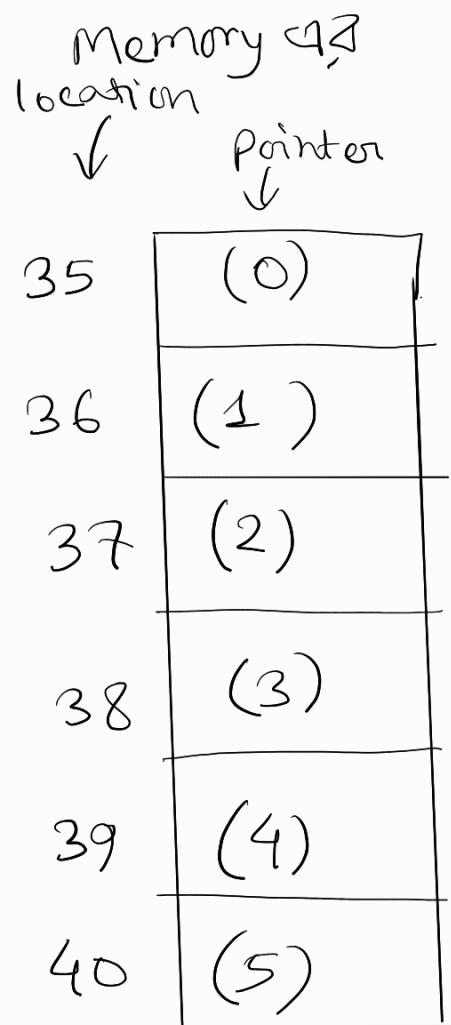
Assign operation (9) LHS (result) পরিসংরক্ষণ করে এবং argument কর

Quiz 4 syllabus :  
 The 3 Representations

## Indirect Triples :

Triple's Rep:

op	arg 1	arg 2
minus	c	
*	b	(0)
minus	c	
*	b	(2)
+	(1)	(3)
=	a	(4)



## Static Single Assignment:

With the help of Static Single Assignment, we can understand whether a variable's value has already changed or not.

eg:  $t_1 = \text{minus } c$   
 $t_2 = b * t_1$   
 $t_3 = \text{minus } c$   
 $t_4 = b * t_3$   
 $t_5 = t_2 + t_4$   
 $a = t_5$

Number system  
Digit अल्पाल्प द्वय  
द्वयकृति,

SDD for the TAC:

Symantic Rule (No brackets)

$S \rightarrow \text{id} = E; S.\text{code} = E.\text{code} \parallel \text{gen}(\text{top.get(id.lexeme)})^c = E.\text{addr}$

concatenation ↗ ↘ generates TAC line, like "x=0x11" & "x=t1"

$E \rightarrow E_1 + E_2 \quad E.\text{addr} = \text{new Temp}()$  परीक्षा के temporary variable (t1, t2..) create करें।

$E.\text{code} = E_1.\text{code} \parallel E_2.\text{code} \parallel \text{gen}(E.\text{addr}^c = E_1.\text{addr}^c + E_2.\text{addr}^c)$

$E \rightarrow -E_1 \quad E.\text{addr} = \text{new Temp}()$

$E.\text{code} = E_1.\text{code} \parallel \text{gen}(E.\text{addr}^c = \text{minus}^c E_1.\text{addr})$

$E \rightarrow (E_1) \quad E.\text{addr} = E_1.\text{addr}$

$E.\text{code} = E_1.\text{code}$

$E \rightarrow \text{id} \quad E.\text{addr} = \text{top.get(id.lexeme)}$

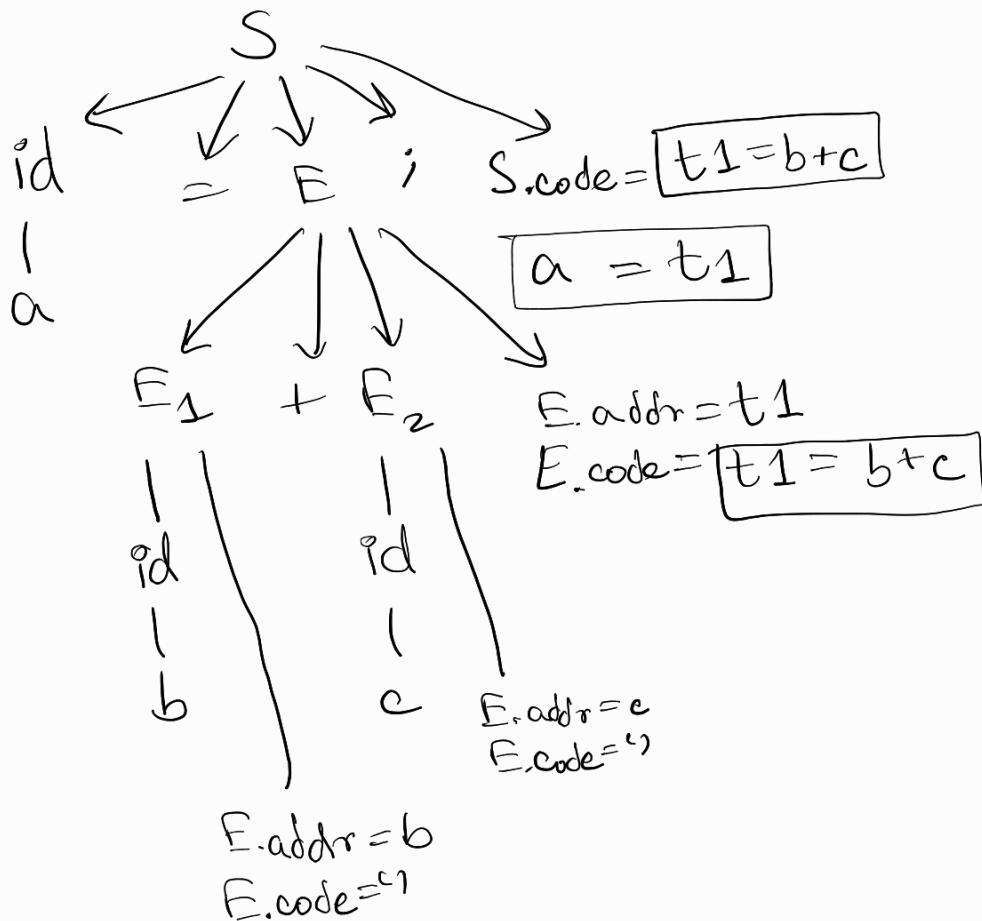
$E.\text{code} = ''$  ↗ empty string

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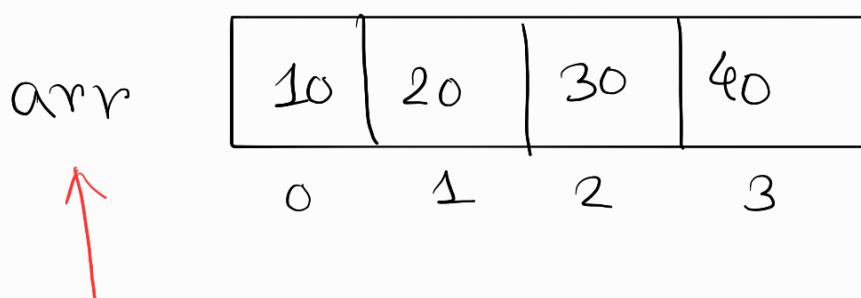
कोई input string  
of code नहीं है  
SDD parse करें।  
Using its syntax tree  
and convert to  
a T-A-C

Input string:  $a = b + c;$

## Parse Tree:



## Relative address Calculation for Array :



base address = 0      → (let's say)  
datatype = integer      ↑

'For each integer, 4 bytes memory will be allocated.'

2 Representations:

1) Row Major

2) Column Major

↑ Representation of integer  
in 2D and higher D arrays.

## 1D Arrays:

(Row Major Rep)

0	10
4	20
8	30
12	40
16	

$a[2]$  finds compiler directly 8 byte skip करे to fetch value of index 2.

calculation is like

$$0 + 4 * 2 = 0 + 8 \\ = 8$$

So the formula is,

Rel-  
Address = base +  $\left( \begin{array}{l} \text{Size} \\ \text{of} \\ \text{each} \\ \text{integer} \end{array} \times \begin{array}{l} \text{Index} \\ \text{No-} \end{array} \right)$

< For One Dimensional Array >

## 2D Arrays:

arr <

0	1	2	3	4	5
1	6	7	8	9	10
2	11	12	13	14	15
3	16	17	18	19	20
4	21	22			

base address = 0

data type = integer

for each integer  $\rightarrow$  4 bytes ...

Row Major Rep.  
Used More

0	1
4	2
8	3
12	
16	4
20	5
24	6
28	7
32	8
36	9

प्रक्रिया

Row को पहले  
प्रक्रियायाम्य

Column major : (Used less) ↓

Column Oriented  
प्रोसेसिंग में लाभ है।  
क्यों?

0	
4	1
8	6
12	11
16	16
20	21
24	2
28	7
32	12
36	17
40	22
44	3

$\text{arr}[1][3]$  finds 9 fetched  
bytes, skipping 32 bytes (in  
the row major representation),  
Hence calculation:

- Row size =  $4 \times 5 = 20$  bytes
- Since  $\text{arr}[1] \dots$ , we've to skip  
row 0 and 1 (and row 1,  
and then 3 indices more):

$$0 + (20 \times 1) + (4 \times 3) = 32$$

Row size       $\text{Idx}$       cols       $\text{Idx}$   
 on the row      1                  2                  ↓  
 Relative address

So, the formula for

Relative address calc. for

2D array:

Relative Address

$$\text{= Base} + (\text{Size of a row} \times \text{Your row number}) + (\text{Column number} \times \frac{\text{Value}}{\text{size}})$$

SDD:

$S \rightarrow id = E ; \{ gen(top.get(id.lexeme) = ?E.addr); \}$   
|  $L = E ; \{ gen(L.array.base['L-addr'] = ?E.addr); \}$

$E \rightarrow E_1 + E_2 \{ E.addr = new Temp();$   
 $gen(E.addr = ?E_1.addr + ?E_2.addr); \}$

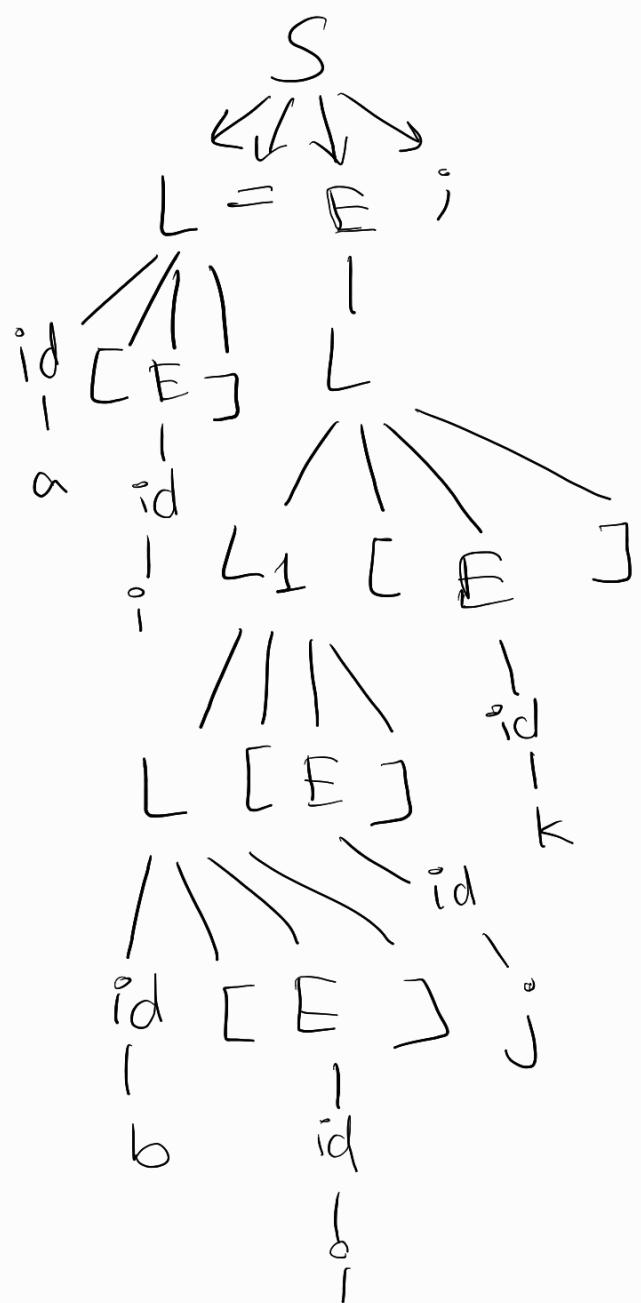
|  $id \{ E.addr = top.get(id.lexeme); \}$   
|  $L \{ E.addr = new Temp();$   
 $gen(E.addr = ?L.array.base['L-addr']); \}$

$L \rightarrow id[E] \{ L.array = top.get(id.lexeme);$   
 $L.type = L.array.type.elem;$   
 $L.addr = new Temp();$   
 $gen(L.addr = ?E.addr * ?L.type.width); \}$

|  $L_1[E] \{ L.array = L_1.array;$   
 $L.type = L_1.type.elem;$   
 $t = new temp();$   
 $L.addr = new Temp();$   
 $gen(t = ?E.addr * ?L.type.width);$   
 $gen(L.addr = ?L_1.addr + ?t); \}$

Input String:

$a[i] = b[i][j][k];$



May 3

Saturday

Q) SDD<sub>2</sub>

$S \rightarrow id = E; \{ gen (top.get(id.lexeme) \leftarrow E.addr); \}$

|  $L = E; \{ gen (L.addr.base \leftarrow L.addr); \} \leftarrow E.addr;$

$E \rightarrow E_1 + E_2 \quad \{ E.addr = new Temp();$   
 $\quad gen (E.addr \leftarrow E_1.addr + E_2.addr); \}$

|  $id \quad \{ E.addr = top.get(id.lexeme); \}$

|  $L \quad \{ E.addr = new Temp();$   
 $\quad gen (E.addr \leftarrow L.array.base \leftarrow L.addr); \}$

$L \rightarrow id[E] \quad \{ L.array = top.get(id.lexeme);$   
 $\quad L.type = L.array.type.elem;$   
 $\quad L.addr = new Temp();$   
 $\quad gen (L.addr \leftarrow E.addr * L.type.width); \}$

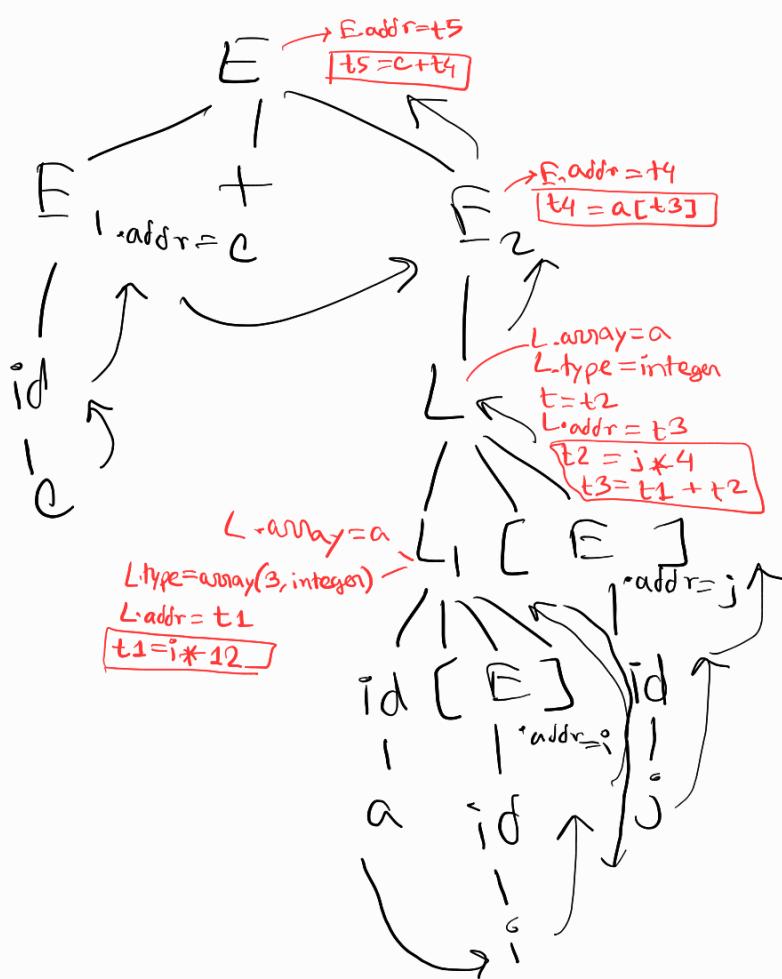
|  $L_1[E] \quad \{ L.array = L_1.array;$   
 $\quad L.type = L_1.type.elem;$   
 $\quad t = new Temp();$   
 $\quad L.addr = new Temp();$   
 $\quad gen (t \leftarrow E.addr * L.type.width);$   
 $\quad gen (L.addr \leftarrow L_1.addr + t); \}$

Given,

Let  $a$  denote a  $2 \times 3$  array of integers, and let  $c, i$  and  $j$  all denote integers. Then, the type of  $a$  is  $\text{array}(2, \text{array}(3, \text{integer}))$ . Its width is 24, assuming that the width of an integer is 4. The type of  $a[i]$  is  $\text{array}(3, \text{integer})$  of width  $w_1 = 12$ . The type of  $a[i][j]$  is integer.

Input String:  $C + a[i][j]$

Soln:



Base address

variable at 240  
base address given

Generated code:-

$t_1 = i * 12$   
 $t_2 = j * 4$   
 $t_3 = t_1 + t_2$   
 $t_4 = a[t_3]$   
 $t_5 = C + t_4$

May 8

## Control Flow

- We will use control flow statements such as if-else statements, while statements. Translation of control flow statements are tied with Boolean expressions.  
For writing Boolean expression we use Boolean operators.

Boolean operators are applied to—

1. Boolean variables
2. Relational expressions with relational operators

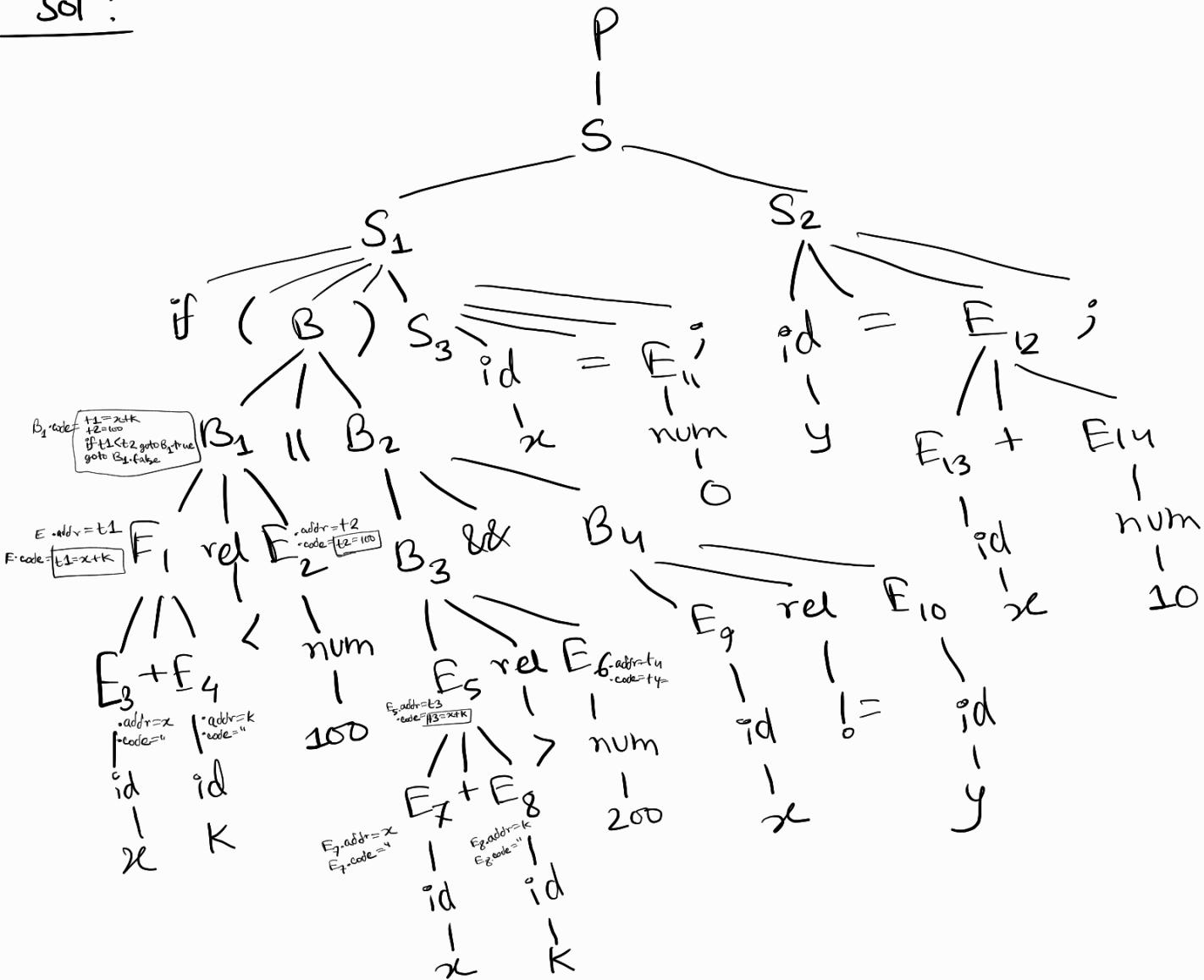
< > != == >= <=

In given SDD:

- $S \rightarrow \text{if } (B) S_1$
- $S \rightarrow \text{if } (B) S_1 \text{ else } S_2$
- $S \rightarrow \text{while } (B) S_1$

- जहाँ  $B$  is Boolean expressions
- $S$  is statement
- प्रतिक्रिया blocks जाएं इन using labels.
- integer रूप num जाएं

Sol<sup>n</sup>:



Code values

$$t1 = x + k \quad (E_1)$$

$$t2 = 100 \quad (E_2)$$

$$t1 = x + k$$

$$t2 = 100$$

$$\text{if } t1 < t2 \text{ goto } B_1 \cdot \text{true}$$

$$\text{goto } B_2 \cdot \text{false}$$

$$t3 = x + k \quad (E_5)$$

synthesized attributes  
एक से compute करवा,  
inherited लाने

