NIKHIL K. KAVATE

Embedded Developer ~ Engineer

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SUMMARY

Embedded Systems Engineer with expertise in IoT and system-level development. Skilled in STM32, ESP32, ESP8266, CAN communication, and sensor integration. Passionate about kernel optimization, HPC, AI-powered applications, and renewable energy solutions. Committed to innovative problem solving and continuous learning.

SKILLS

Languages:	C, C++, Python, Assembly, Bash (Shell Script), SQL.
Operating System:	FreeRTOS, Linux, Windows.
Technologies:	CAN, I2C, SPI, UART, CoAP, MQTT, Zigbee, LPWAN, LoRa, REST, Wireless LAN, RTOS.

PROJECTS

BSc Mini Project — **Counter Circuit**
https://github.com/niikhilkavate/BSC-Electronics-Mini-Project.git
Developed a 7-segment display counter using CD4026 and 555 Timer, with an RC debounce circuit for accurate pulse counting.

BSc Project — **Green House Control System**
https://github.com/niikhilkavate/BSC-Electronics-Project.git
PIC Microcontroller Based Green House Control Automatic irrigation system.

MSc Projec — **Suryasankalp**
https://github.com/niikhilkavate/MSC-Electronics-Project.git
Design Solar Power Based Off-Grid Storage System for Home Automation.

CDAC Project — **DriveSafe**
https://github.com/niikhilkavate/PGDiploma-Electronics-Project..git
Developed a smart vehicle safety system with alcohol detection, ignition control, and real-time speed tracking via IoT.

EDUCATION

Year	Qualification	Institution
2025	**CDAC PG-DESD**	Sunbeam Institute of Information Technology Hinjewadi, Pune.
2024	**MSc Electronics Science**	Modern College of Arts, Science and Commerce.
2022	**BSc Electronics Science**	Modern College of Arts, Science and Commerce.
2017	**HSC-Science (Electronics)**	Abasaeb Garware College,Pune ,Maharashtra.
2015	**SSC-General**	Laxman Rao Apte Prashala, Pune.

EXPERIENCE

08/2022 – 11/2023 Wipro PARI PVT LTD — **Company**

- Reviewing code changes to ensure adherence to coding standards best practices and code quality.
- Writing modifying and maintaining control software based on project requirements. Investigating and addressing software defects reported by testers or end users ensuring they are tracked and resolved.
- Collaborating with hardware engineers system architects and testers to ensure smooth integration and functionality of control software within the overall system.

CERTIFICATE

Jul 2023 — **National Apprenticeship Training Scheme** — **NATS**
Ministry of Education (MoE), Government of India

Aug 2024 — **PG Diploma In Embedded Systems and Design** — **PG-DESD**
Centre for Development of Advanced Computing (C-DAC)

LANGUAGES

English, **Marathi**, **Hindi**