Sr.No	OPCODE	INSTRUCTION	NO OF ARG	ARG1	ARG2	size	isMODRM	isrd
MOV								
1	89 /r	**MOV** reg32,reg32	2	REG	REG	2	YES	NO
2	B8+ rd id	**MOV** reg32,Imm32	2	REG	IMM	5	NO	NO
3	8B /r	**MOV** reg32,[mem]	2	REG	MEM	6	YES	NO
4	C7 /0 id	**MOV** [mem],Imm32	2	MEM	IMM	10	NO	NO
5	89 /r	**MOV** [mem],reg32	2	MEM	REG	6	YES	NO
ADD								
1	01 /r	**ADD** reg32,reg32	2	REG	REG	2	YES	NO
2	81 /0 id	**ADD** reg32,Imm32	2	REG	IMM	3	NO	NO
3	03 /r	**ADD** reg32,[mem]	2	REG	MEM	6	YES	NO
4	81 /0 id	**ADD** [mem],Imm32	2	MEM	IMM	7	NO	NO
5	01 /r	**ADD** [mem],reg32	2	MEM	REG	6	YES	NO
6	83 /0 ib	**ADD** reg32, imm8	2	REG	IMM	3	NO	NO
7	83 /0 id	**ADD** [mem], imm8	2	MEM	IMM	7	NO	NO
SUB								
1	29 /r	**SUB** reg32,reg32	2	REG	REG	2	YES	NO
2	81 /5 id	**SUB** reg32,Imm32	2	REG	IMM	3	NO	NO
3	2B /r	**SUB** reg32,[mem]	2	REG	MEM	6	YES	NO
4	81 /5 id	**SUB** [mem],Imm32	2	MEM	IMM	7	NO	NO
5	29 /r	**SUB** [mem],reg32	2	MEM	REG	6	YES	NO
6	81 /5 ib	**SUB** reg32, imm8	2	REG	IMM	3	NO	NO
7	81 /5 ib	**SUB** [mem],imm8	2	MEM	IMM	7	NO	NO
MUL								
1	F7 /4	**MUL** reg	1	REG	MEM	2	YES	NO
2	F7 /4	**MUL** [mem]	1	REG	IMM	6	YES	NO
DIV								
1	F7 /6	**div** reg	1	REG	-	2	YES	NO
2	F7 /6	**div** [mem]	1	[MEM]	-	6	YES	NO
INC								
1	40+ rd	**inc** reg32	1	REG	-	1	YES	YES
2	FF /0	**inc**[mem]	1	MEM	-	6	YES	NO
DEC								
1	48+rd	**dec** reg32	1	REG	-	1	YES	YES
2	FF /1	**dec**[mem]	1	MEM	-	6	YES	NO
XOR								
1	31 /r	**xor** reg32,reg32	2	REG	REG	2	YES	NO
2	81 /6 id	**xor** reg32,imm32	2	REG	IMM	6	NO	NO
3	33 /r	**xor** reg32,[mem]	2	REG	MEM	6	YES	NO
4	81 /6 id	**xor** [mem],imm32	2	MEM	IMM	10	NO	NO
5	31 /r	**xor** [mem],reg32	2	MEM	REG	6	YES	NO
6	81 /6 ib	**xor** reg32,imm8	2	REG	IMM	3	NO	NO
7	83 /6 ib	**xor** [mem],imm8	2	MEM	IMM	7	NO	NO
CMP								
1	39 /r	**cmp** reg32,reg32	2	REG	REG	2	YES	NO
2	81 /7 id	**cmp** reg32,imm	2	REG	IMM	3	NO	NO
3	3B /r	**cmp** reg32,[mem]	2	REG	MEM	6	YES	NO
4	81 /7 id	**cmp** [mem],imm	2	MEM	IMM	7	NO	NO
5	39 /r	**cmp** [mem],reg32	2	MEM	REG	6	YES	NO
6	83 /7 ib	**cmp** reg32,imm8	2	REG	IMM	3	NO	NO
7	83 /7 ib	**cmp** [mem],imm8	2	MEM	IMM	7	NO	NO
JMP								
1	FF /4	jmp reg32	1	REG	-	2	YES	NO
2	FF /4	jmp [mem]	1	MEM	-	6	YES	NO
3	0F 84 cd	jz [mem]	1	MEM	-	2	YES	NO
4	0F 85 cd	jnz [mem]	1	MEM	-	2	YES	NO