NITTE MEENAKSHI INSTITUTE OF TECHNOLOGY

NITTE EDUCATION TRUST

An Autonomous Institution, Affiliated to Visvesvaraya Technological University, Belagavi
Approved by AICTE, New Delhi and Accredited by NAAC - 'A+' Grade

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KNOWLEDGE - CHARACTER - UNITY

TRANSCRIPT AS PER RECORDS

Student Name	: NIKHIL KUMAR G
University Seat Number	: 1NT16EC092
Degree	: BACHELOR OF ENGINEERING
Programme	: ELECTRONICS AND COMMUNICATION ENGINEERING

CREDIT BASED SYSTEM AND GPA SCHEME OF EVALUATION

Duration of Course	:4 Years(3 Years for Lateral Entry)
Medium of Instruction	:ENGLISH
Year of Entry	: 2016-2017
Year of Passing	: 2019-2020

Sub Code	Subject Name	Credit	Grade	Exam Year	Sub Code	Subject Name	Credit	Grade	Exam Year
SEMESTER 1					**SEMESTER 2**				
14MAT11	Engineering Mathematics – I	4	C	Dec/Jan17	14MAT21	Engineering Mathematics-II	4	C	May/June17
14CHE12	Engineering Chemistry	4	E	Dec/Jan17	14PHY22	Engineering Physics	4	C	May/June17
14CCP13	Computer Concepts & C Programming	4	B	Dec/Jan17	14EMC23	Engineering Mechanics	4	B	May/June17
14CED14	Computer Aided Engineering Drawing	4	E	Dec/Jan17	14EME24	Elements of Mechanical Engineering	4	C	May/June17
14ELN15	Basic Electronics Engineering	4	C	Dec/Jan17	14ELE25	Basic Electrical Engineering	4	D	May/June17
14CPL16	Computer Programming Lab	1.5	A	Dec/Jan17	14WSL26	Workshop Practice	1.5	B	May/June17
14CHL17	Engineering Chemistry Lab	1.5	C	Dec/Jan17	14PHL27	Engineering Physics Lab	1.5	B	May/June17
14CIV18	Environmental Studies	-	PP	Dec/Jan17	14CIP28	Constitution of India and Professional Ethics	-	PP	May/June17
14ENG19	Communicative English	2	B	Dec/Jan17					
SEMESTER 3					**SEMESTER 4**				
14MAT31	Engineering Mathematics-III	4	C	Dec/Jan18	14MAT41	Engineering Mathematics-IV	4	E	April/May18
14EC32	Analog Electronics Circuits	4	E	Dec/Jan18	14EC42	Linear Integrated Circuit	4	C	April/May18
14EC33	Digital Electronics	4	C	Dec/Jan18	14EC43	Control System	4	C	June18
14EC34	Network Analysis	4	E	Dec/Jan18	14EC44	Field and Waves	4	D	April/May18
14EC35	Signals and Systems	4	C	Jul/Aug18	14EC45	Digital System Design using Verilog	4	C	April/May18
14EC36	Microprocessor and Microcontroller	3	C	Dec/Jan18	14EC46	Microcontroller	3	B	April/May18
14ECL37	Analog Electronics Lab	1.5	B	Dec/Jan18	14ECL47	Microcontroller Lab	1.5	B	April/May18
14ECL38	Digital Electronics Lab	1.5	B	Dec/Jan18	14ECL48	Digital System Design using Verilog Lab	1.5	A	April/May18
SEMESTER 5					**SEMESTER 6**				
14EC51	Digital Signal Processing	4	C	Dec18/Jan19	14EC61	Information Theory and Coding	4	B	June18
14EC52	Analog Communication	4	C	Dec18/Jan19	14EC62	Digital Communication	4	B	April/May19
14EC53	Microwave Engineering	4	C	Dec18/Jan19	14EC63	Antenna and Wave Propagation	4	C	April/May19
14EC54	Fundamentals of VLSI	4	C	Dec18/Jan19	14ECH64	Operation Research	4	B	April/May19
14EC55	Data Structures using C++	4	B	Dec18/Jan19	14ECE656	IP Networking	4	C	April/May19
14ECE555	Advanced Microcontroller	3	C	Dec18/Jan19	14CSO666	Introduction to Java Programming	3	B	April/May19
14ECL57	Digital Signal Processing Lab	1.5	A	Dec18/Jan19	14ECL67	VLSI Lab	1.5	B	April/May19
14ECL58	LIC+Analog Communication Lab	1.5	A	Dec18/Jan19	14ECL68	ADC Lab	1.5	A	April/May19
SEMESTER 7					**SEMESTER 8**				
14EC71	Optical Fiber Communication	4	B	Dec19	14EC81	Wireless Communication	4	A	July/Aug20
14EC72	Computer Communication and Network	4	B	Dec19	14ECE824	Multimedia Communication	4	A	July/Aug20
14EC73	Power Electronics	4	B	Sep20	14ECP83	Major Project	13	A	July/Aug20
14ECE7511	Applied DSP	4	A	Dec19					
14ECH74	Entrepreneurship Development & IPR	3	B	Dec19					
14ECL77	Power Electronics Lab	1.5	B	Dec19					
14ECL78	Computer Communication Network Lab	1.5	A	Dec19					
14ECP79	Internship and Mini Project	2	A	Dec19					
14ISO753	Design & Development of Web Application	3	C	Dec19					

Total Credits Earned: 200

CGPA: 7.27

CONTROLLER OF EXAMINATIONS   06/01/22

PRINCIPAL

Grade	S	A	B	C	D	E	F
Grade Points	10	09	08	07	05	04	00
Marks Range	>=90	75-89	60-74	50-59	45-49	40-44	0-39

PP: Passed the Mandatory Non Credit Course
NP: Not Passed the Mandatory Non Credit Course