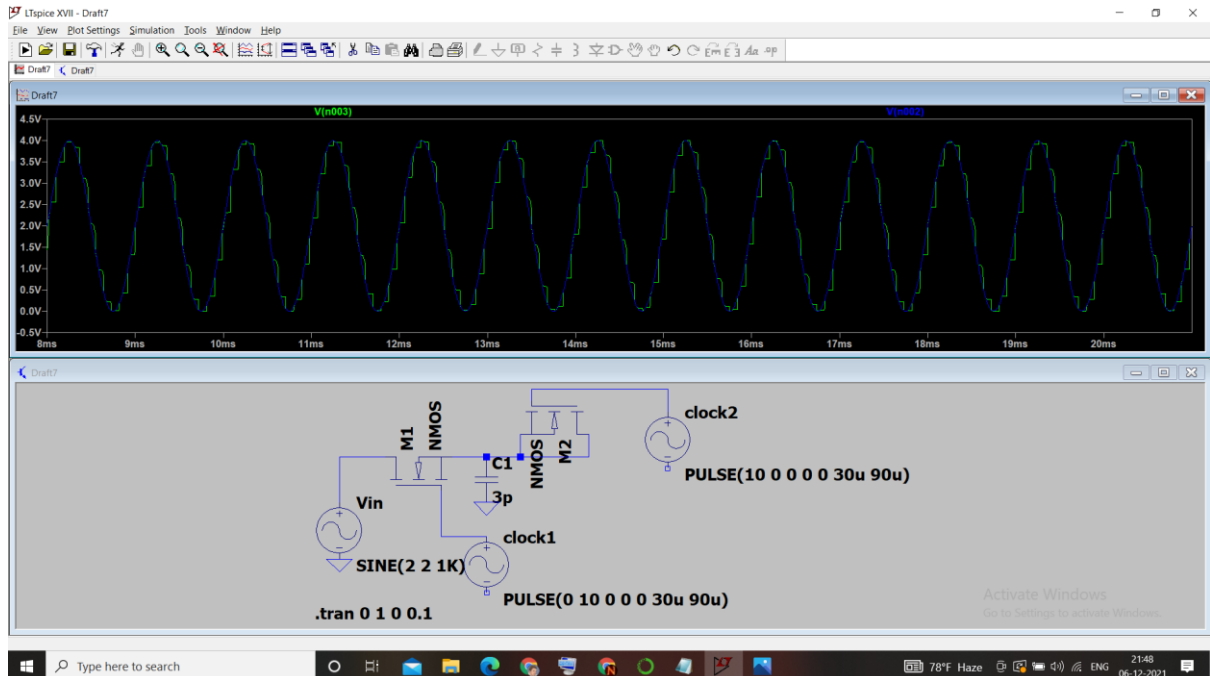


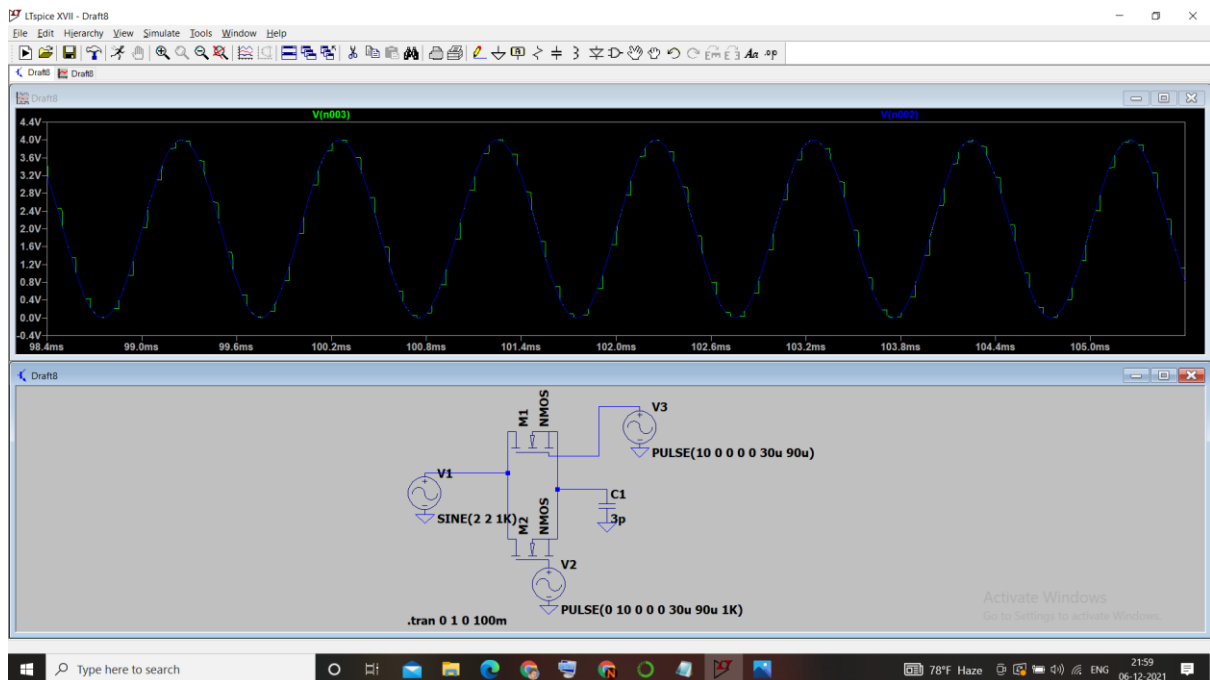
Mixed Signal Design Assignment – II

Neelampalle Nikhil Kumar

18bec031 - ECE

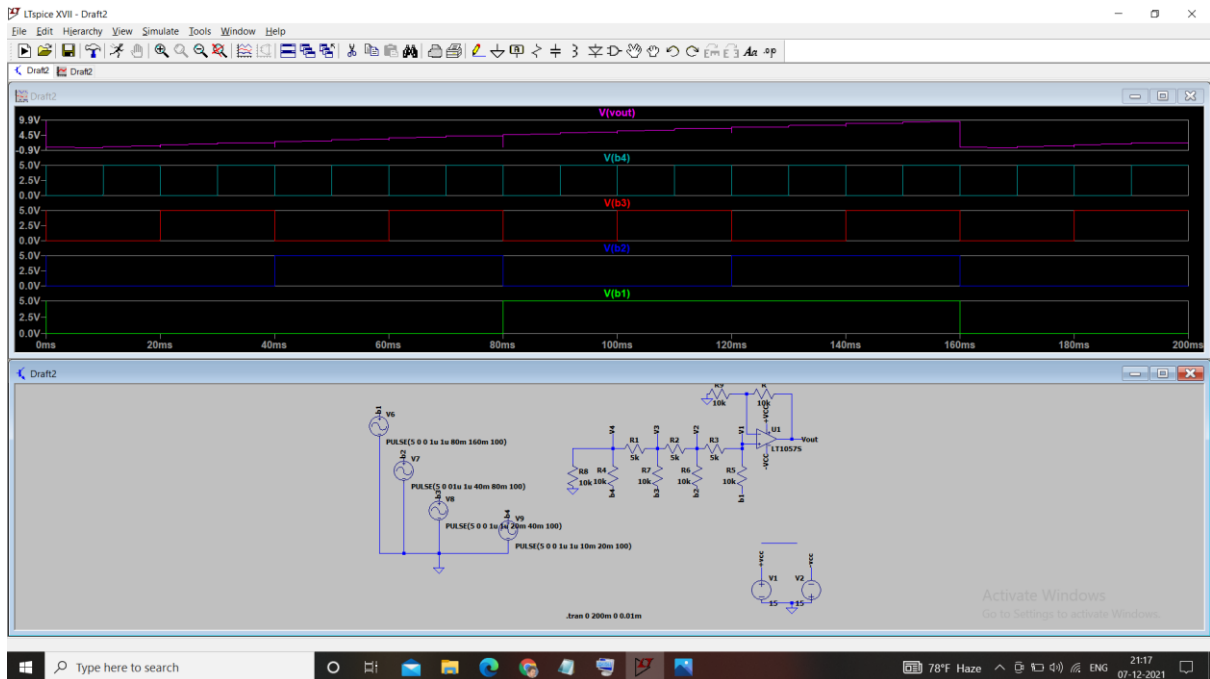
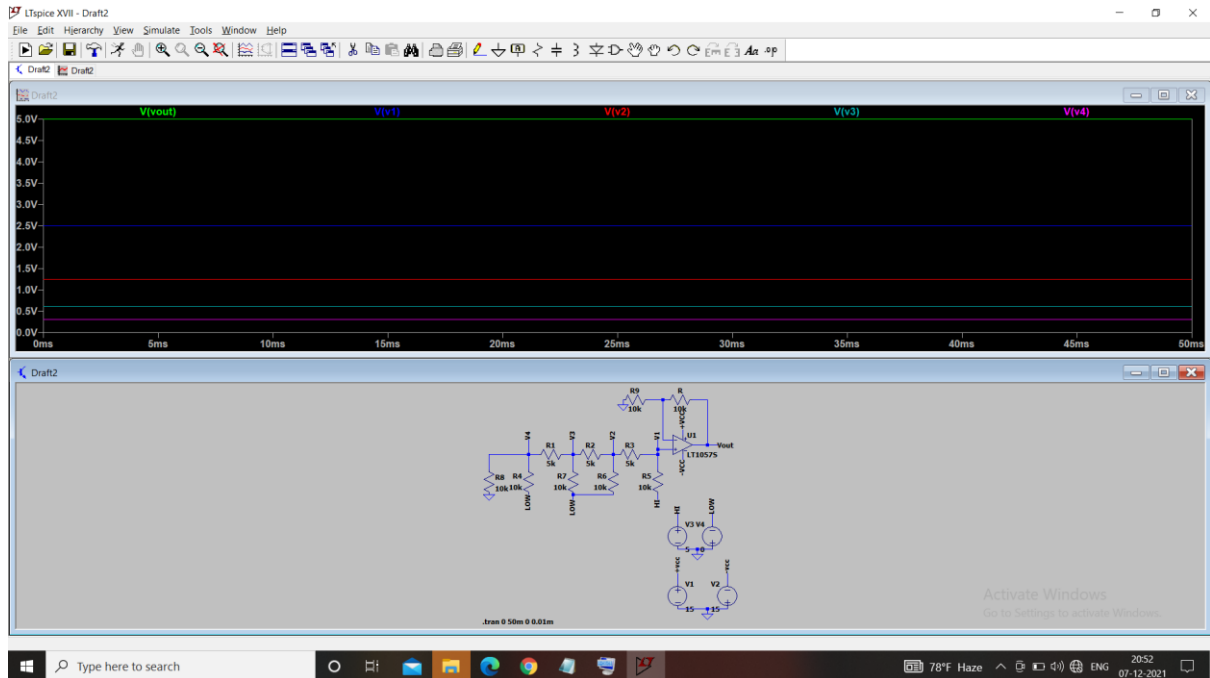


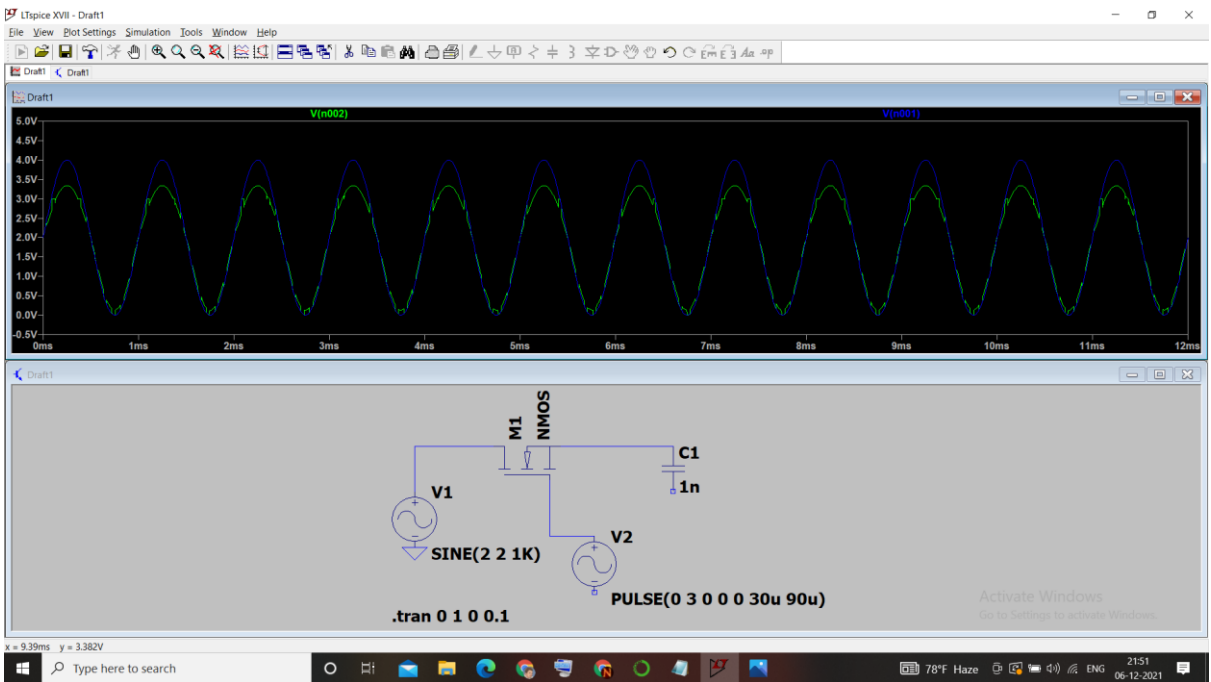
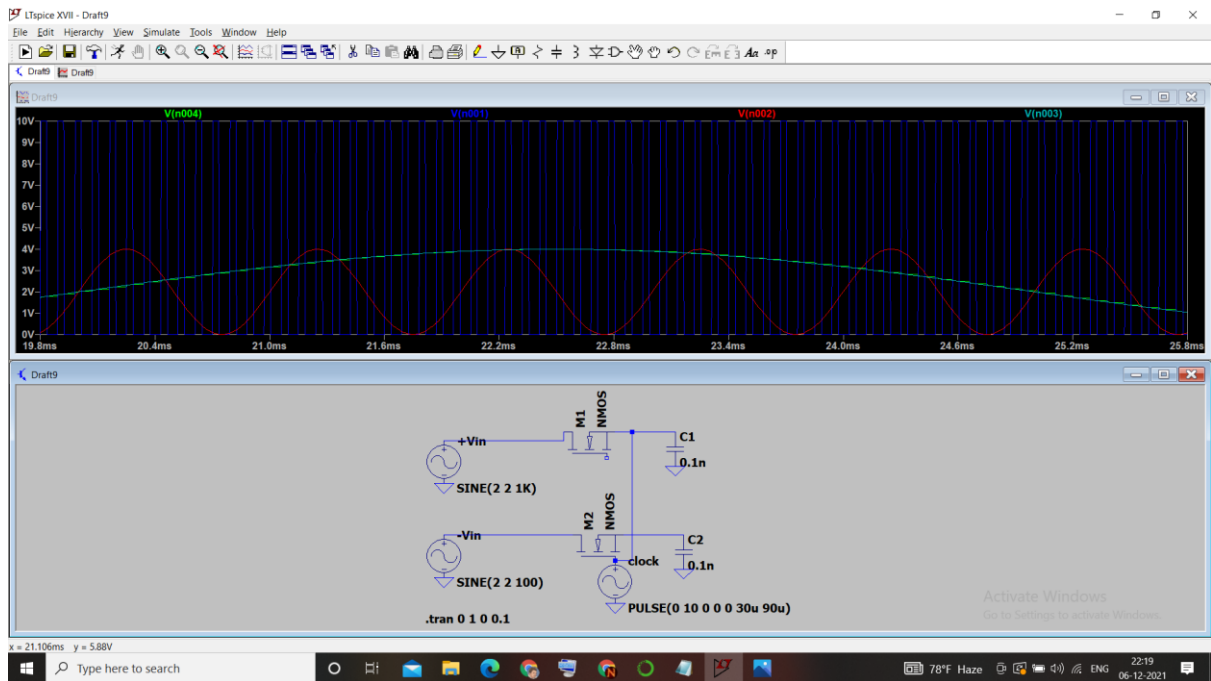
Dummy Transistor



Transmission Gates S/H

DAC





S/H Circuit

