

Nikhil Navaratna

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EDUCATION

Nanyang Technological University (NTU)

Aug. 2021 – Aug. 2025

Ph.D., Physics

Singapore

- **GPA: 4.88/5.0**

Birla Institute of Technology and Science, Pilani

July 2020

M.Sc. (Hons.) Physics, B.E (Hons.) Electronics and Communications

Hyderabad, India

- **GPA: 8.59/10**

WORK EXPERIENCE

Nanyang Technological University (NTU)

Aug. 2021 – Present

Graduate Researcher. Supervisor: Prof. Ranjan Singh

- Designed and implemented high-frequency SiGe HBT amplifiers for the 300 GHz band.
 - Contributed to the design and layout optimization of a power amplifier, achieving a bandwidth of **51 GHz** and a gain of 10.2 dB. **Design sent for tapeout.**
 - Led the design of a low noise amplifier (LNA) achieving a gain of **11 dB**, and an input reflection coefficient better than -18 dB. The design is finalized and **ready for tapeout.**
- Engineered silicon-based on-chip interconnects and devices for the 300 GHz band.
 - Developed broadband terahertz interconnects, achieving record data transmission rates of **400 Gbps** over 80 GHz bandwidths with minimal signal loss.
 - Conceptualized and implemented compact **monolithic terahertz Mach Zehnder interferometers** with < 2 dB insertion loss and > 20 dB isolation.
- Development of **6G testbed** for characterizing high-frequency devices
 - Implemented a testbed capable of both on-chip and over-the-air data communication experiments with complex modulation formats.
 - Performed real-time device characterization of waveguides and reconfigurable intelligent surfaces.

Texas Instruments

Jul. 2019 – Dec. 2019

Intern

Bangalore, India

- Developed a method to calculate jitter at various stages in digital circuits and oscillators using Cadence Virtuoso, improving circuit optimization and performance.
- Applied jitter analysis to enhance the reliability and timing precision of digital circuit designs, contributing to more stable and efficient circuit operation

SKILLS

- **Simulation and modelling:** Keysight ADS, CST Studio, COMSOL Multiphysics, and Cadence Virtuoso
- **Measurement and Characterization:** Experienced in characterizing high-frequency devices using Vector Network Analyzers (VNAs), Real Time Oscilloscopes (RTOs), spectrum analyzers, and other high-frequency measurement tools.
- **Programming Languages:** C++, Python, and MATLAB for modeling, simulation automation and data processing. Proficient in automating measurement setups and data acquisition using LabVIEW

SELECTED PUBLICATIONS

- **Navaratna, N.**, Tan, Y. J., Kumar, A., Gupta, M., & Singh, R. (2023). On-chip topological THz biosensors. *Applied Physics Letters*, 123(3), 033705.
- Gupta, M., **Navaratna, N.**, Szriftgiser, P., Ducournau, G., & Singh, R. (2023). 327 Gbps THz silicon photonic interconnect with sub- λ bends. *Applied Physics Letters*, 123(17), 171102.
- Kumar, A., Tan, Y. J., **Navaratna, N.**, Gupta, M., Pitchappa, P., & Singh, R. (2024). Slow light topological photonics with counter-propagating waves and its active control on a chip. *Nature Communications*, 15(1), 926.
- Review paper: **Navaratna, N.**, & Singh, R. (2024). Convergent electronic-photonic systems for future 6G-XG wireless communications. *Nature Electronics*, *Under review*
- **Navaratna, N.**, Muhammed, H., & Singh, R. (2025). Beyond $f_{\max}/2$ cascode amplifiers for 300 GHz communications, *Manuscript under preparation*
- **Navaratna, N.**, & Singh, R. (2025). Monolithic terahertz topological photonic Mach Zehnder Interferometer, *Submitted to Nature Communications*

LEADERSHIP EXPERIENCE

Graduate Student Council

Aug. 2023 – Aug.2024

President

- Led initiatives to improve student engagement, academic collaboration and overall professional development.
- Represented the student body in multiple decision-making processes with faculty and administration.

INTERESTS

Reading, programming, astronomy and running