1. Which interrupt has the highest priority?

a) INTR b) TRAP c) RST6.5

2. In 8085 name the 16 bitregisters?

a) Stack pointer b) Program counter c) a & b

3. Which of the following is hardware interrupts?

a) RST5.5, RST6.5, RST7.5 b) INTR, TRAP c) a & b

4. What is the RST for the TRAP?

a) RST5.5 b) RST4.5 c) RST4

5. What are level Triggering interrupts?a) INTR&TRAP b)RST6.5&RST5.5 c)RST7.5&RST6.5

6. Which interrupt is not level sensitive in 8085?a) RST6.5 is a raising edge-trigging interrupt.b) RST7.5 is a raising edge-trigging interrupt.c) a & b.

7. What are software interrupts?

a) RST 0 - 7 b) RST 5.5 - 7.5 c) INTR, TRAP

8. Which stack is used in 8085?

a) FIFO b) LIFO c) FILO

9. Why 8085 processor is called an 8 bit processor?

a) Because 8085 processor has 8 bit ALU.b) Because 8085 processor has 8 bit data bus.c) a & b.

10. What is SIM?

a) Select Interrupt Mask b) Sorting Interrupt Mask c) Set Interrupt Mask.

11. RIM is used to check whether, \_\_\_\_\_\_

a) The write operation is done or notb) The interrupt is Masked or not

c) a & b

12. What is meant by Maskable interrupts?

a) An interrupt which can never be turned off.b) An interrupt that can be turned off by the programmer. c) none

13. In 8086, Example for Non maskable interrupts are

a) Trapb) RST6.5 c) INTR

14. What does microprocessor speed depends on?

a) Clock b) Data bus width c) Address bus width

15. Can ROM be used as stack?

a) Yes b) No c) sometimes yes, sometimes no

16. Which processor structure is pipelined? a) all x80 processors  b) all x85 processors c) all x86 processors

17. Address line for RST3 is? a) 0020H b) 0028H c) 0018H

18. In 8086 the overflow flag is set when

a) The sum is more than 16 bits b) Signed numbers go out of their range after an arithmetic operation c) Carry and sign flags are setM.

19. The advantage of memory mapped I/O over I/O mapped I/O is,

a) Faster b) Many instructions supporting memory mapped I/O c) Require a bigger address decoder d) All the above

20. BHE of 8086 microprocessor signal is used to interface the

a) Even bank memoryb) Odd bank memoryc) I/Od) DMA

21. In 8086 microprocessor the following has the highest priority among all typeinterrupts

.a) NMIb) DIV 0c) TYPE 255d) OVER FLOW

22. In 8086 microprocessor one of the following statements is not true.

a) Coprocessor is interfaced in MAX mode b) Coprocessor is interfaced in MIN mode c) I/O can be interfaced in MAX / MIN moded) Supports pipelining

23. 8088 microprocessor differs with 8086 microprocessor in

a) Data width on the output b) Address capability c) Support of coprocessord) Support of MAX / MIN mode

24. Address line for TRAP is?

a) 0023H b) 0024H c) 0033H

Key: 1.1 C 1.2 C 1.3 C 1.4 B 1.5 B 1.6 B1.7 A 1.8 B 1.9 A 1.10 C 1.11 B 1.12 B1.13 A 1.14 C 1.15 B 1.16 C 1.17 C 1.18 B1.19 D 1.20 B 1.21 A 1.22 B 1.23 A 1.24 B

. POP operation  
a) decrements SP  
b) increments SP  
c) decrements SS  
d) increments SS  
View Answer

Answer:b  
Explanation: Each POP operation increments the SP ( Stack Pointer) register.

|  |  |
| --- | --- |
| The \_\_\_\_\_\_\_\_ ensures that only one IC is active at a time to avoid a bus conflict caused by two ICs writing different data to the same bus. | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | control bus | | [**B.**](javascript:%20void%200;) | control instructions | | [**C.**](javascript:%20void%200;) | |  |  | | --- | --- | | address decoder | @ | | | [**D.**](javascript:%20void%200;) | CPU | | |
| When referring to instruction words, a mnemonic is: | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | a short abbreviation for the operand address | | [**B.**](javascript:%20void%200;) | |  |  | | --- | --- | | a short abbreviation for the operation to be performed | @ | | | [**C.**](javascript:%20void%200;) | a short abbreviation for the data word stored at the operand address | | [**D.**](javascript:%20void%200;) | shorthand for machine language | | |
| The technique of assigning a memory address to each I/O device in the computer system is called: | |
| |  |  |  |  | | --- | --- | --- | --- | | [**A.**](javascript:%20void%200;) | |  |  | | --- | --- | | memory-mapped I/O | @ | | | [**B.**](javascript:%20void%200;) | ported I/O | | [**C.**](javascript:%20void%200;) | dedicated I/O | | [**D.**](javascript:%20void%200;) | wired I/O | | |
| Single-bit indicators that may be set or cleared to show the results of logical or arithmetic operations are the: | |
| |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | [**A.**](javascript:%20void%200;) | |  |  | | --- | --- | | flags | @ | | [**B.**](javascript:%20void%200;) | registers | | [**C.**](javascript:%20void%200;) | monitors | [**D.**](javascript:%20void%200;) | decisions | | |
| When referring to instruction words, a mnemonic is: | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | a short abbreviation for the operand address | | [**B.**](javascript:%20void%200;) | |  |  | | --- | --- | | a short abbreviation for the operation to be performed | @ | | | [**C.**](javascript:%20void%200;) | a short abbreviation for the data word stored at the operand address | | [**D.**](javascript:%20void%200;) | shorthand for machine language | | |
|  | When was the first 8-bit microprocessor introduced? |
| |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | [**A.**](javascript:%20void%200;) | 1969 | [**B.**](javascript:%20void%200;) | |  |  | | --- | --- | | 1974 | @ | | | [**C.**](javascript:%20void%200;) | 1979 | [**D.**](javascript:%20void%200;) | 1985 | |
| What type of circuit is used at the interface point of an output port? | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | decoder | | [**B.**](javascript:%20void%200;) | |  |  | | --- | --- | | latch | @ | | | [**C.**](javascript:%20void%200;) | tristate buffer | | [**D.**](javascript:%20void%200;) | none of the above | | |
|  | I/O mapped systems identify their input/output devices by giving them a(n) \_\_\_\_\_\_\_\_. |
| |  |  |  |  | | --- | --- | --- | --- | | [**A.**](javascript:%20void%200;) | |  |  | | --- | --- | | 8-bit port number | @ | | | [**B.**](javascript:%20void%200;) | 16-bit port number | | [**C.**](javascript:%20void%200;) | 8-bit buffer number | | [**D.**](javascript:%20void%200;) | 8-bit instruction | |
| What type of circuit is used at the interface point of an input port? | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | decoder | | [**B.**](javascript:%20void%200;) | latch | | [**C.**](javascript:%20void%200;) | |  |  | | --- | --- | | tristate buffer | @ | | | [**D.**](javascript:%20void%200;) | none of the above | | |
| The register in the 8085A that is used to keep track of the memory address of the next op-code to be run in the program is the: | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | stack pointer | | [**B.**](javascript:%20void%200;) | |  |  | | --- | --- | | program counter | @ | | | [**C.**](javascript:%20void%200;) | instruction pointer | | [**D.**](javascript:%20void%200;) | accumulator | | |
|  | The control bus and memories share a bidirectional bus in a typical microprocessor system. |
| |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | [**A.**](javascript:%20void%200;) | True | [**B.**](javascript:%20void%200;) | |  |  | | --- | --- | | False | @ | | |
| The 8085A is a(n): | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | 16-bit parallel CPU | | [**B.**](javascript:%20void%200;) | 8-bit serial CPU | | [**C.**](javascript:%20void%200;) | |  |  | | --- | --- | | 8-bit parallel CPU | @ | | | [**D.**](javascript:%20void%200;) | none of the above | | |
| A register in the microprocessor that keeps track of the answer or results of any arithmetic or logic operation is the: | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | stack pointer | | [**B.**](javascript:%20void%200;) | program counter | | [**C.**](javascript:%20void%200;) | instruction pointer | | [**D.**](javascript:%20void%200;) | |  |  | | --- | --- | | accumulator | @ | | | |
| What is the difference between a mnemonic code and machine code? | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | There is no difference. | | [**B.**](javascript:%20void%200;) | |  |  | | --- | --- | | Machine codes are in binary, mnemonic codes are in shorthand English. | @ | | | [**C.**](javascript:%20void%200;) | Machine codes are in shorthand English, mnemonic codes are in binary. | | |
| Which bus is a bidirectional bus? | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | address bus | | [**B.**](javascript:%20void%200;) | |  |  | | --- | --- | | data bus | @ | | | [**C.**](javascript:%20void%200;) | address bus and data bus | | [**D.**](javascript:%20void%200;) | none of the above | | |
| Which of the following buses is primarily used to carry signals that direct other ICs to find out what type of operation is being performed? | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | data bus | | [**B.**](javascript:%20void%200;) | |  |  | | --- | --- | | control bus | @ | | | [**C.**](javascript:%20void%200;) | address bus | | [**D.**](javascript:%20void%200;) | address decoder bus | | |
|  | What kind of computer program is used to convert mnemonic code to machine code? |
| |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | [**A.**](javascript:%20void%200;) | debug | [**B.**](javascript:%20void%200;) | |  |  | | --- | --- | | assembler | @ | | | [**C.**](javascript:%20void%200;) | C++ | [**D.**](javascript:%20void%200;) | Fortran | |
| Which method bypasses the CPU for certain types of data transfer? | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | Software interrupts | | [**B.**](javascript:%20void%200;) | Interrupt-driven I/O | | [**C.**](javascript:%20void%200;) | Polled I/O | | [**D.**](javascript:%20void%200;) | |  |  | | --- | --- | | Direct memory access (DMA) | @ | | | |

|  |
| --- |
| Which of the following is not an enhancement to the Pentium that was unavailable in the 8086/8088? |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | "Pipelined" architecture | | [**B.**](javascript:%20void%200;) | Expansion of cache memory | | [**C.**](javascript:%20void%200;) | Inclusion of an internal math coprocessor | | [**D.**](javascript:%20void%200;) | |  |  | | --- | --- | | Data/address line multiplexing | @ | | |
| DMA is particularly suited for data transfer between the \_\_\_\_\_\_\_\_. |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | disk drive and CPU | | [**B.**](javascript:%20void%200;) | |  |  | | --- | --- | | disk drive and RAM | @ | | | [**C.**](javascript:%20void%200;) | disk drive and ROM | | [**D.**](javascript:%20void%200;) | disk drive and I/O | |
| The first microprocessor had a(n)\_\_\_\_\_\_\_\_. |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | 1-bit data bus | | [**B.**](javascript:%20void%200;) | 2-bit data bus | | [**C.**](javascript:%20void%200;) | |  |  | | --- | --- | | 4-bit data bus | @ | | | [**D.**](javascript:%20void%200;) | 8-bit data bus | |
| Which microprocessor has multiplexed data and address lines? |
| |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | [**A.**](javascript:%20void%200;) | |  |  | | --- | --- | | 8086/8088 | @ | | [**B.**](javascript:%20void%200;) | 80286 | | [**C.**](javascript:%20void%200;) | 80386 | [**D.**](javascript:%20void%200;) | Pentium | |
| Which is not an operand? |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | Variable | | [**B.**](javascript:%20void%200;) | Register | | [**C.**](javascript:%20void%200;) | Memory location | | [**D.**](javascript:%20void%200;) | |  |  | | --- | --- | | Assembler | @ | | |

|  |  |
| --- | --- |
| Which is not part of the execution unit (EU)? | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | Arithmetic logic unit (ALU) | | [**B.**](javascript:%20void%200;) | |  |  | | --- | --- | | Clock | @ | | | [**C.**](javascript:%20void%200;) | General registers | | [**D.**](javascript:%20void%200;) | Flags | | |
| A 20-bit address bus can locate \_\_\_\_\_\_\_\_. | |
| |  |  |  |  | | --- | --- | --- | --- | | [**A.**](javascript:%20void%200;) | |  |  | | --- | --- | | 1,048,576 locations | @ | | | [**B.**](javascript:%20void%200;) | 2,097,152 locations | | [**C.**](javascript:%20void%200;) | 4,194,304 locations | | [**D.**](javascript:%20void%200;) | 8,388,608 locations | | |
| What is occurring when two or more sources of data attempt to use the same bus? | |
| |  |  |  |  | | --- | --- | --- | --- | | [**A.**](javascript:%20void%200;) | |  |  | | --- | --- | | Bus contention | @ | | | [**B.**](javascript:%20void%200;) | Direct memory access | | [**C.**](javascript:%20void%200;) | Bus interruption | | [**D.**](javascript:%20void%200;) | PPI | | |
| Which of the following is not a jump instruction? | |
| |  |  |  |  | | --- | --- | --- | --- | | [**A.**](javascript:%20void%200;) | |  |  | | --- | --- | | JB (jump back) | @ | | | [**B.**](javascript:%20void%200;) | JA (jump above) | | [**C.**](javascript:%20void%200;) | JO (jump if overflow) | | [**D.**](javascript:%20void%200;) | JMP (unconditional jump) | | |
|  | Which of the following was not a design improvement for the 8086/8088 over the 8085? |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | Execution unit (EU) | | [**B.**](javascript:%20void%200;) | 16-bit data bus | | [**C.**](javascript:%20void%200;) | |  |  | | --- | --- | | Arithmetic logic unit (ALU) | @ | | | [**D.**](javascript:%20void%200;) | Bus interface unit (BIU) | |
| Polled I/O works best when \_\_\_\_\_\_\_\_. |
| |  |  |  |  | | --- | --- | --- | --- | | [**A.**](javascript:%20void%200;) | |  |  | | --- | --- | | there are no priority considerations | @ | | | [**B.**](javascript:%20void%200;) | priority considerations are frequent | | [**C.**](javascript:%20void%200;) | the polling rate exceeds 1000 s | | [**D.**](javascript:%20void%200;) | the polling rate is below 1000 s | |
| Which of the following is not an arithmetic instruction? | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | INC (increment) | | [**B.**](javascript:%20void%200;) | CMP (compare) | | [**C.**](javascript:%20void%200;) | DEC (decrement) | | [**D.**](javascript:%20void%200;) | |  |  | | --- | --- | | ROL (rotate left) | @ | | | |
| During a read operation the CPU fetches \_\_\_\_\_\_\_\_. | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | a program instruction | | [**B.**](javascript:%20void%200;) | another address | | [**C.**](javascript:%20void%200;) | data itself | | [**D.**](javascript:%20void%200;) | |  |  | | --- | --- | | all of the above | @ | | | |
| he first Intel microprocessor to contain on-board cache memory was the \_\_\_\_\_\_\_\_. | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | 80386 | | [**B.**](javascript:%20void%200;) | |  |  | | --- | --- | | 80486 | @ | | | [**C.**](javascript:%20void%200;) | Pentium | | [**D.**](javascript:%20void%200;) | Pentium Pro | | |
| Which of the following is not an 8086/8088 general-purpose register? | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | Code segment (CS) | | [**B.**](javascript:%20void%200;) | Data segment (DS) | | [**C.**](javascript:%20void%200;) | Stack segment (SS) | | [**D.**](javascript:%20void%200;) | |  |  | | --- | --- | | Address segment (AS) | @ | | | |
| Which of the following is not a computer bus? | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | Data bus | | [**B.**](javascript:%20void%200;) | Control bus | | [**C.**](javascript:%20void%200;) | |  |  | | --- | --- | | Timer bus | @ | | | [**D.**](javascript:%20void%200;) | Address bus | | |
| With interrupt-driven I/O, if two or more devices request service at the same time, \_\_\_\_\_\_\_\_. | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | the device closest to the CPU gets priority | | [**B.**](javascript:%20void%200;) | the device that is fastest gets priority | | [**C.**](javascript:%20void%200;) | |  |  | | --- | --- | | the device assigned the highest priority is serviced first | @ | | | [**D.**](javascript:%20void%200;) | the system is likely to crash | | |
| The Pentium can address \_\_\_\_\_\_\_\_. | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | 1 MB | | [**B.**](javascript:%20void%200;) | 1 GB | | [**C.**](javascript:%20void%200;) | 2 GB | | [**D.**](javascript:%20void%200;) | |  |  | | --- | --- | | 4 GB | @ | | | |
| A port can be \_\_\_\_\_\_\_\_. | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | strictly for input | | [**B.**](javascript:%20void%200;) | strictly for output | | [**C.**](javascript:%20void%200;) | bidirectional | | [**D.**](javascript:%20void%200;) | |  |  | | --- | --- | | all of the above | @ | | | |
| Which of the following is not a computer functional block? | |
| |  |  |  |  | | --- | --- | --- | --- | | [**A.**](javascript:%20void%200;) | |  |  | | --- | --- | | Analog-to-digital converter | @ | | | [**B.**](javascript:%20void%200;) | Central-processing unit | | [**C.**](javascript:%20void%200;) | Memory | | [**D.**](javascript:%20void%200;) | Input/output ports | | |
| The Pentium microprocessor has a data bus of \_\_\_\_\_\_\_\_. | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | 16 bits | | [**B.**](javascript:%20void%200;) | 32 bits | | [**C.**](javascript:%20void%200;) | |  |  | | --- | --- | | 64 bits | @ | | | [**D.**](javascript:%20void%200;) | 128 bits | | |
| The process of jointly establishing communication is called \_\_\_\_\_\_\_\_. | |
| |  |  | | --- | --- | | [**A.**](javascript:%20void%200;) | DMA | | [**B.**](javascript:%20void%200;) | bidirectional addressing | | [**C.**](javascript:%20void%200;) | multiplexing | | [**D.**](javascript:%20void%200;) | |  |  | | --- | --- | | handshaking | @ | | | |