

CS & IT ENGINEERING





Operating Systems

CPU Scheduling

Lecture No. 3



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TOPICS TO
BE
COVERED

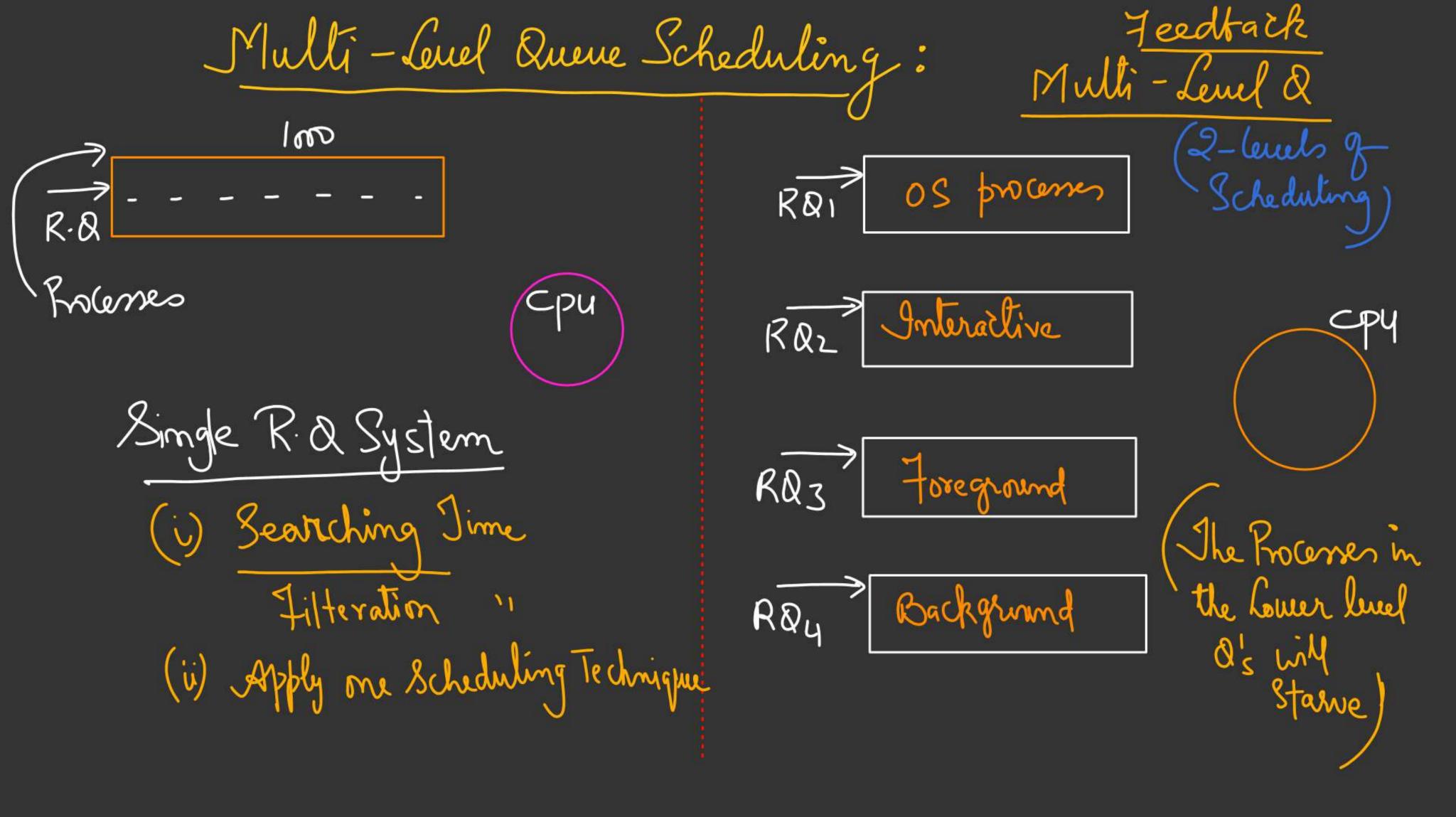
Round Robin

Performance of Round Robin

Multi-Level Queue

Scheduling

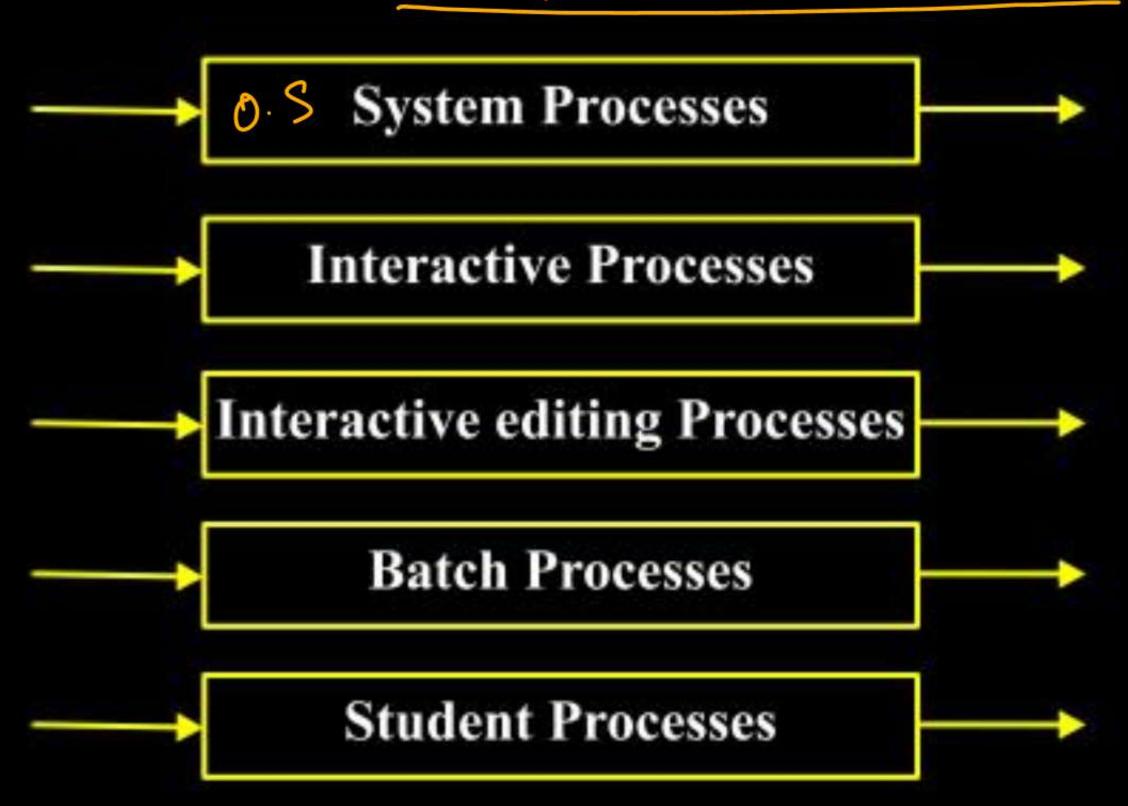
Performance 9 Round-Robin: The value of Ta should neither be too large, & mor too small; very 8mill Jime Quantum > wry large: cpu efficiency ~0 -> It degenerates to large Small work like FCFS -> less content -> More Content (i) Ta=10 Switch overhead Switching overhead KINTO A.T B.T -> System becomes -> cpu efficiency less (Poor) draps 7-0-3 Interactive -> System becomes 3-0-2 more Interactive 4-1-1 (ii) TQ = 0.1; &=2 cpu efficiently= 0.4











Highest Priority

Multilevel feedback Queue Scheduling



- Another way to put a preference on short-lived processes
 - Penalize processes that have been running longer
- Preemptive



Cart d'in the System generally uses FCFS to ensure Completion

Processes in Rq1 are scheduled only when no processes exist in Rq0

Processes in RQ2 are scheduled only when no processes exist in Rq1

Longer processes gradually drift downward

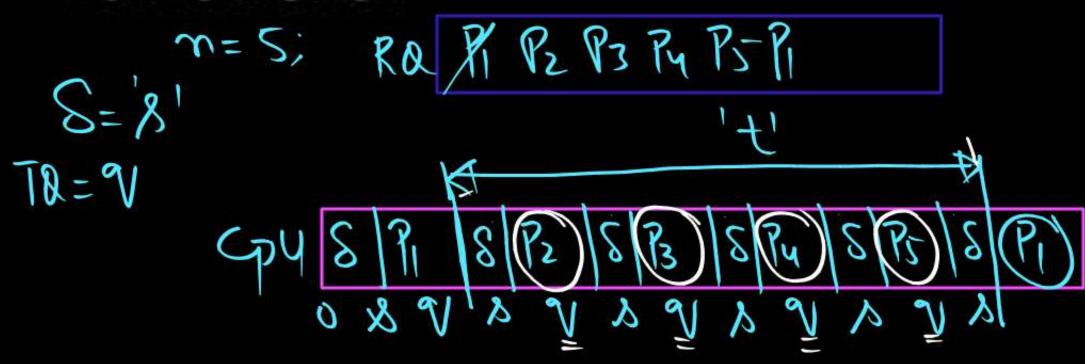
(ase Study Interviews) Implementation of cpy Scheduling in (a) LINUX: O(n); O(1): F-S 3) UNIX: R.R. based (c) WINDOWS: Privaty + d) MAC:





Consider a System with 'n' Processes arriving at time 0⁺ with substantially large Burst Times. The CPU scheduling overhead is 's' seconds, Time Quantum is 'q' seconds. Using Round Robin scheduling, what must be the value of Time Quantum 'q' such that each Process is guaranteed to get its turn at the CPU exactly after 't' seconds in its subsequent

run-on CPU.



$$(1-m)=(m-1)\cdot 9$$



Which of the following statements (s) is/are correct in the context of CPU Scheduling?





The goal is to only maximize CPU utilization and minimize throughput



Turnaround time includes waiting time



Implementing preemptive scheduling needs hardware support Timer



Round-robin policy can be used even when the CPU time required by each of the processes is not known Apriority. (Before hand)

Q.

Consider the following set of Processes, assumed to have arrived at time 0. Consider the CPU scheduling algorithms Shortest Job First (SJF) and Round Robin (RR). For RR assume that the processes are scheduled in the order P_1 , P_2 , P_3 , P_4 .

Processes	P ₁	P ₂	P ₃	P ₄
Burst time (in ms)	8	7	2	4

If the time quantum for RR is 4 ms, then the absolute value of the difference between the average turnaround times (in ms) of SJF and RR (round off to 2 decimal places) is



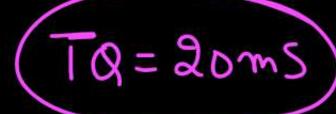


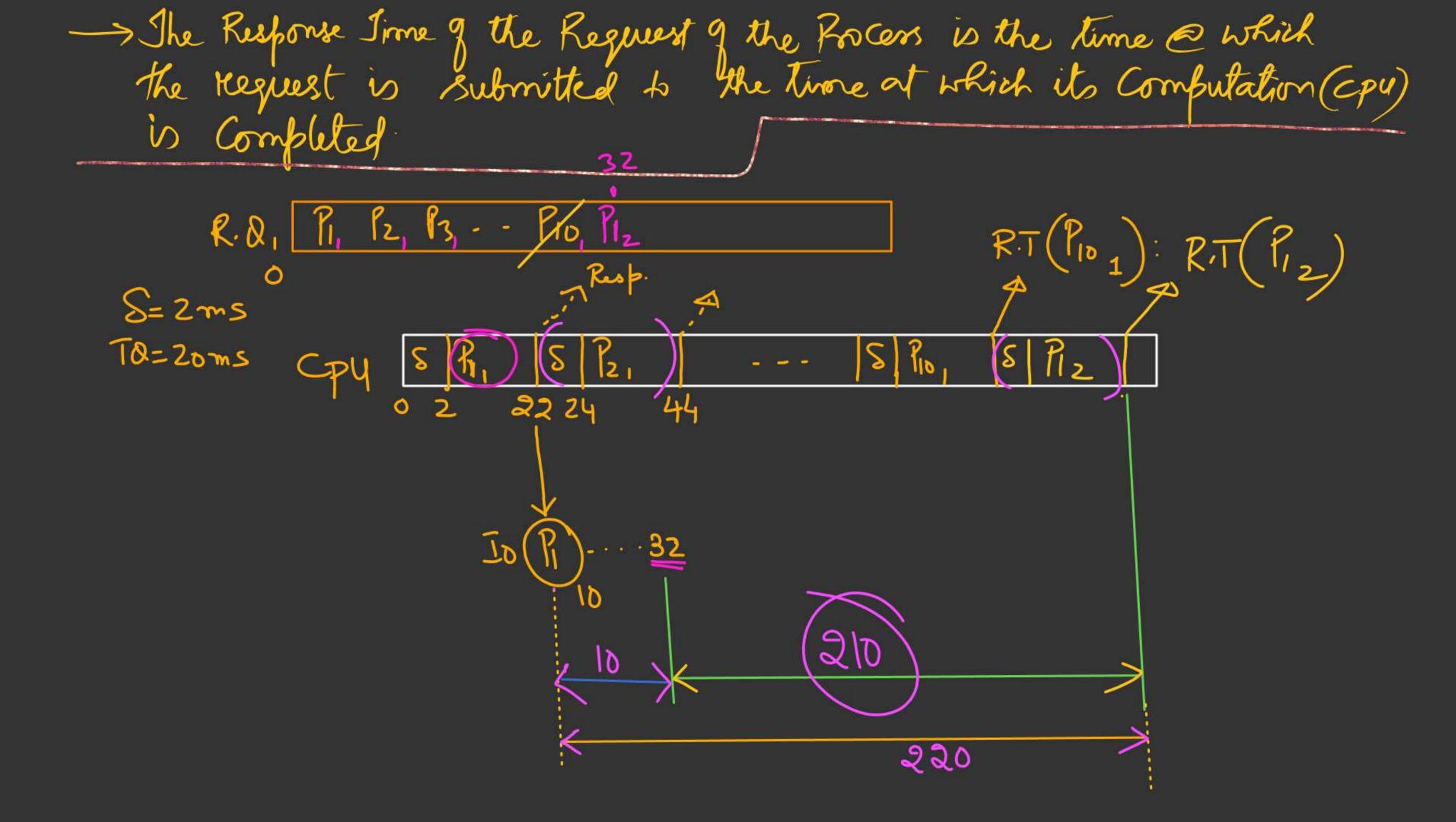
Consider a System using Round Robin Scheduling with 10 Reprocesses all arriving at the time 0. Each Process is associated with 20 identical Request. Each Process request consumes 20 ms of CPU time after which it spends 10 ms of time on I/O, thereafter, initiates subsequent Request. Assuming Scheduling Overhead of 2 ms and Time Quantum of 20 ms, Calculate

i. Response time of the 1st request of the 1st Process: 22m5

ii. Response time of the 1st request of the last Process : 10 (2+20)=220

iii. Response time of the subsequent request of any Process.





Q.



Consider four Processes P, Q, R, and S scheduled on a CPU as per Round Robin Algorithm with a Time Quantum of 4 units. The Processes arrive in the order P, Q, R, S, all at time t = 0. There is exactly one context switch from S to Q, exactly one context switch from R to Q, and exactly two context switches from Q to R. There is no context switch from S to P. Switching to a ready process after the termination of another process is also considered a context switch. Which one of the following

is NOT possible as cpu B.T of Processes? Ray a RSAR

A
$$P = 4$$
, $Q = 10$, $R = 6$, $S = 2$

B
$$P = 2$$
, $Q = 9$, $R = 5$, $S = 1$

$$P = 4$$
, $Q = 12$, $R = 5$, $S = 4$

D
$$P = 3, Q = 7, R = 7, S = 3$$

T&= 4

Q.

Skrt Book

Consider a System using RR Scheduling with TQ of 'Q' seconds & CPU Scheduling overhead is 'S' seconds. Each Process on an average run for 'T' seconds before blocking on I/O. Give a formula for CPU efficiency for each of the following conditions.

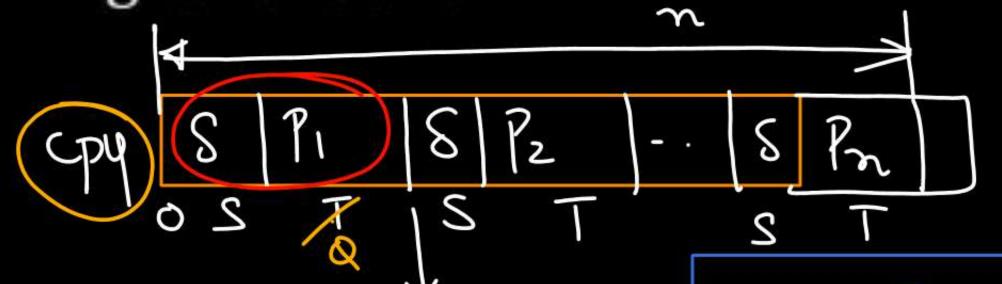
1.
$$Q = \infty$$

2.
$$Q > T$$

3.
$$S < Q < T$$

4.
$$Q = S$$

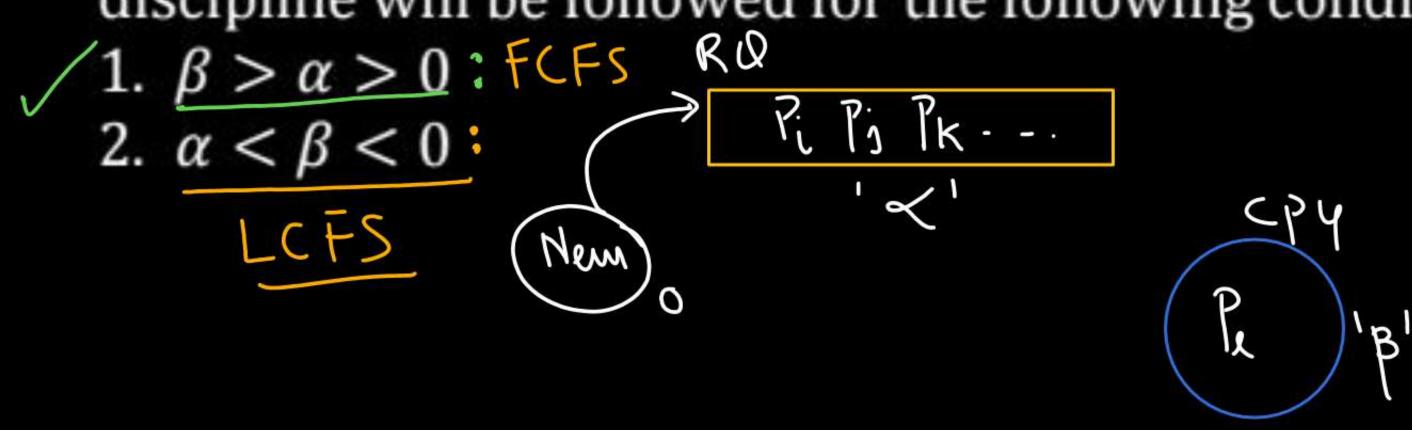
5.
$$Q \approx 0$$







Consider a System using Preemptive Priority based scheduling with dynamically changing priorities. On its arrival a Process is assigned a priority of zero and Running Process Priority increases at the rate of ' β ' and Priority of the Processes in the ready Q increases at the rate of ' α '. By dynamically changing the values of α and β one can achieve different Scheduling disciplines among the Processes. What discipline will be followed for the following conditions.





Consider Processes $P_1 \& P_2$ arriving in the ready queue at time 0 with following properties.



- i) P1 needs a total of 12 units of CPU time and 20 units of I/O time. After every 3 units of CPU time P1 spends 5 units on I/O. $\frac{7}{1} = \frac{3}{3}, \frac{5}{3}, \frac{3}{5}, \frac{5}{3}, \frac{5}{5}$
- ii) P2 needs a total of 15 units of CPU time and no I/O. P2 arrives just after P1. P2 < 15

Compute the Completion times of $P_1\&P_2$ using the following scheduling techniques:

1.SRTF

2.Round Robin with Time Quanta = 4 units







Three processes A, B and C each execute a loop of 100 iterations. In each iteration of the loop, a process performs a single computation that requires to CPU milliseconds and then initiates a single I/O operation that lasts for $t_{\rm io}$ milliseconds. It is assumed that the computer where the processes execute has sufficient number of I/O devices and the OS of the computer assigns different I/O devices to each process. Also the scheduling overhead of the OS is negligible. The processes have the following characteristics:

Process Id	t_c	tio
A	100 ms	500 ms
В	350 ms	500 ms
C	200 ms	500 ms

The processes A, B, and C are started at times 0,5 and 10 milliseconds respectively in a pure time-sharing system (round robin scheduling) that uses a time slice of 50 milliseconds. The time in milliseconds at which process C would complete its first I/O operation is

1. J.P.C & Process Synchronization

Process Coordination

> Inter-Process Communication

MINIX



