

Master thesis timeplan

Start Date: 01/03/2016

March					April				May				June					July				August												
9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35								
Identification of control approaches																																		
Understanding the system					Litterature review				Develop control structures																									
					Simulations																													
					Adjustments of structures						Matlab & Simulink Impl.						Benchmark tests																	
																					Implementation on real device													
																	Coding			Adjustments of parameters						Benchmark tests								
Hardware testing in the ongoing project																																		
																			</															