

**Government Polytechnic ,Nashik**  
**Department of Electronics and Telecommunication Engineering**

**Academic Year- 2024-2025**

**Term: ODD**

**Class: II EL-A with effect from 01/07/2024**

DAY/TIME	IIELA																
	10-11			11-12			12-1			1-1.30	1.30-2.30	2.30-3.30	3.30-3.45	3.45-4.45		4.45-5.45	
Monday	AET TH VST CL1			DEX TH SRD CL1			LIC TH SMS CL1			-X-	ACI TH JGJ CL1	EES TH PVR CL1	-X-	EL9 ELM PR SSM L7		EL9 ELM PR SSM L7	
Tuesday	EL7 DEX PR SRD L6	EL8 ELM PR SSM L7	EL9 LIC PR SMS L1B	EL7 DEX PR SRD L6	EL8 ELM PR SSM L7	EL9 LIC PR SMS L1B	ACI TH JGJ CL1			-X-	AET TH VST CL1	ELM TH SSM CL1	-X-	EL7 AET PR VST L1A	EL8 LIC PR SMS L1B	EL7 AET PR VST L1A	EL8 LIC PR SMS L1B
Wednes ay	ELM TH SSM CL1			DEX TH SRD CL1			EES TH V3 CL1			-X-	LIC TH SMS CL1	AET TH VST CL1	-X-	EL9 AET PR VST L1A		EL9 AET PR VST L1A	
Thursday	ELM TH SSM CL1			AET TH VST CL1			ACI TH JGJ CL1			-X-	DEX TH SRD CL1	LIC TH SMS CL1	-X-	EL7 ELM PR SSM L7	EL8 DEX PR SRD L6	EL7 ELM PR SSM L7	EL8 DEX PR SRD L6
Friday	ACI TH JGJ CL1			EL7 LIC PR SMS L1B	EL8 AET PR VST L1A	EL9 DEX PR SRD L6	EL7 LIC PR SMS L1B	EL8 AET PR VST L1A	EL9 DEX PR SRD L6	-X-	LIC TH SMS CL1	DEX TH SRD CL1	-X-	EES TH V3 CL1		---	
Course Name				Course Code/Abbreviation			Theory(Hrs.)			Practicals (Hrs.)			Total (Hrs.)		Name of Staff		
Applied Electronics				232148			4			6			10		Shri. V S Thakare		
Digital Electronics				232149			4			6			10		Shri.S R Devkate		
Linear Integrated Circuits				232150			4			6			10		Smt. S M Solanki		
Artificial and Computational Intelligence				232407			4						4		Dr. J G Joshi/V1		
Electronic Measurement and Instrumentation				232151			3			6			9		Shri. S S Mahajan		
Environmental Education and Sustainability				231302			3						3		V3		
				Total Load(Hrs.)/Week			22			24			46				

Shri.S.R.Devkate  
Time  
Table  
Incharge

Shri.D B Borude

Dr.Smt.P V Rathod

Principal

G P Nashik

Head of Department  
Electronics and Telecomm.Engg