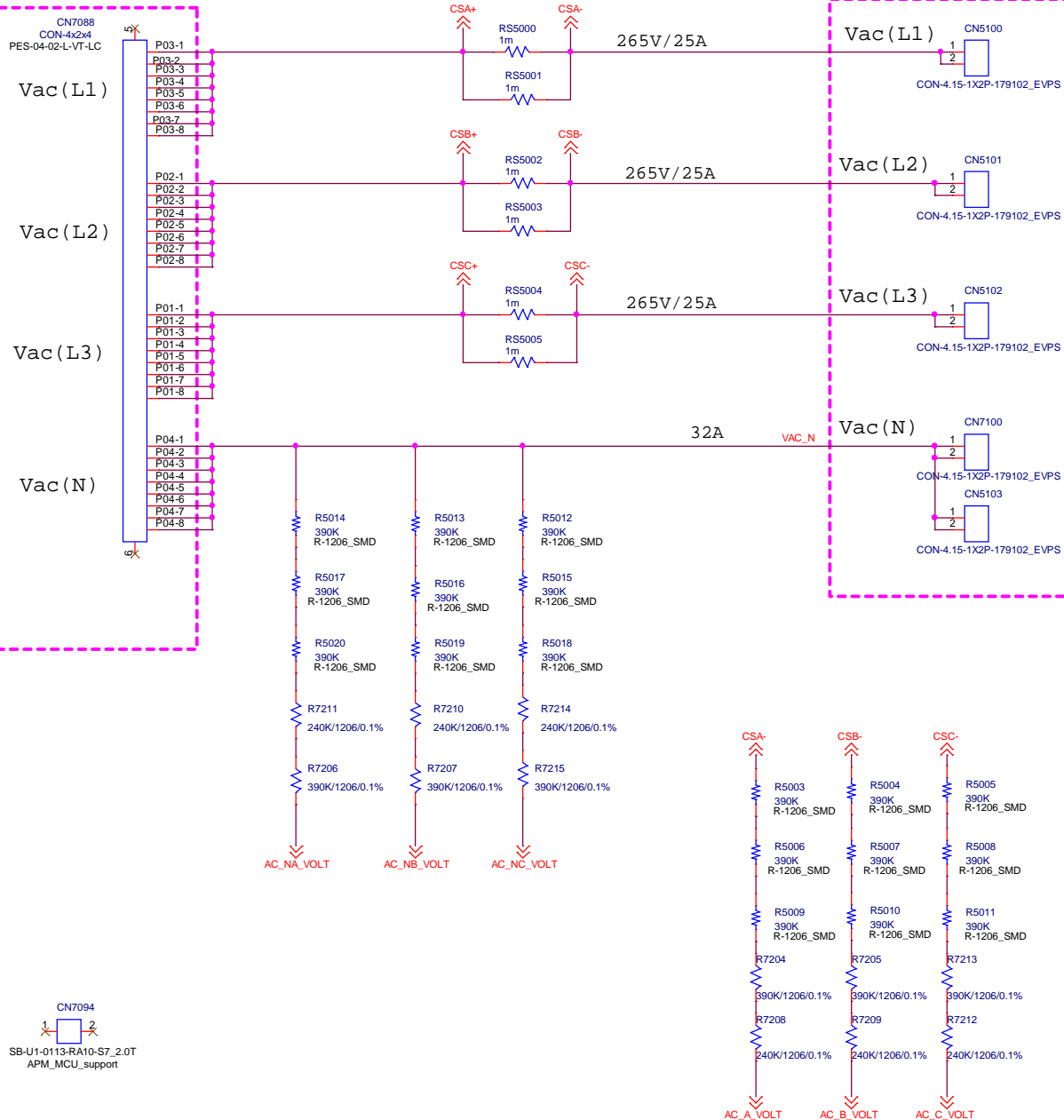
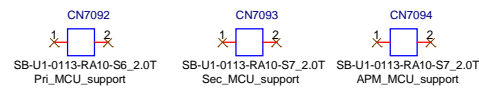


TO AC EMI BOARD (S11)

TO PFC CHOKE BOARD (S4)

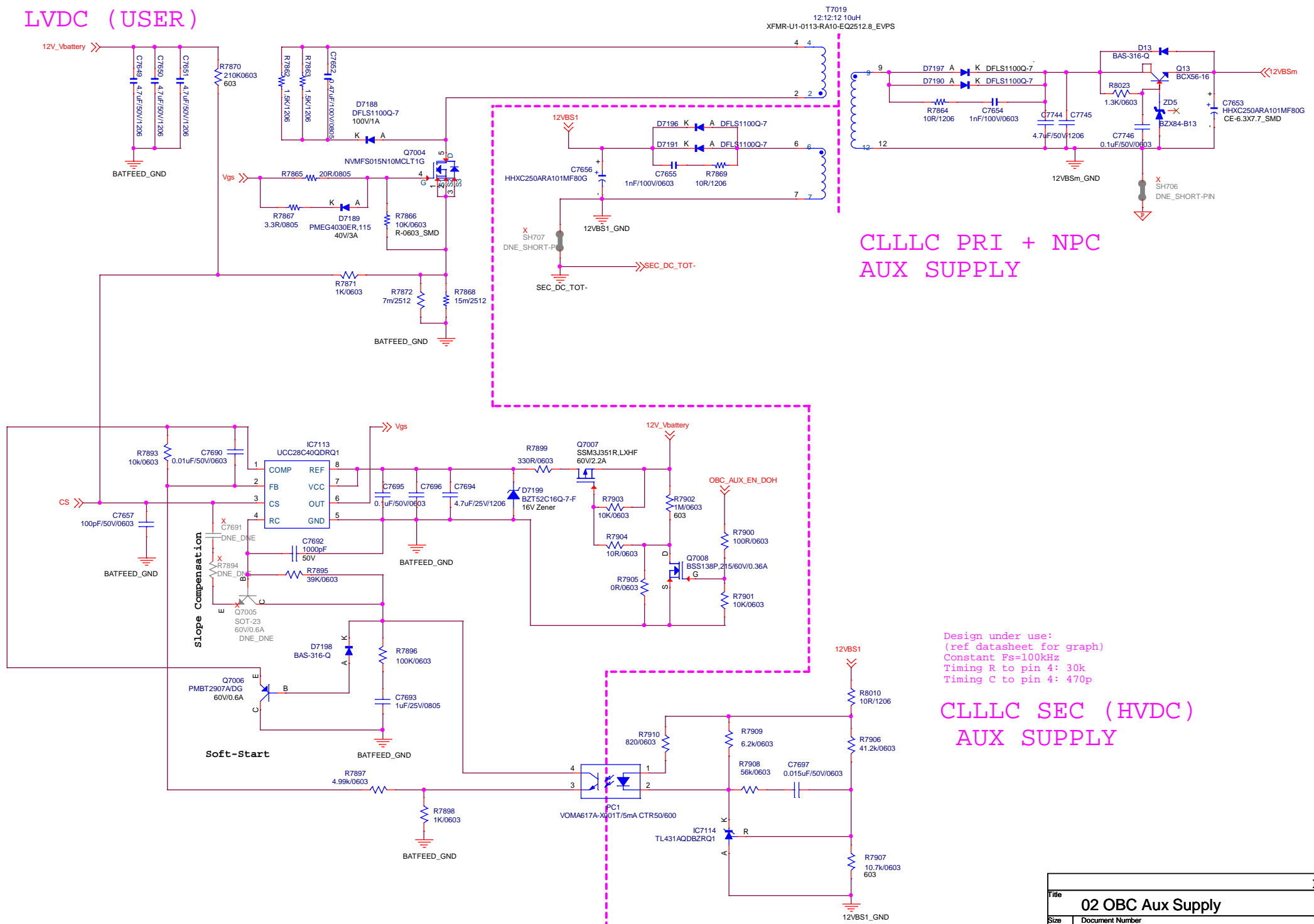
AC:  
Line to N - 265V rms  
Line to Line - 480V rms

AC:  
Line to N - 265V rms  
Line to Line - 480V rms



MB		
Title 01 AC Input		
Size A3	Document Number <Doc>	Rev A2
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## LVDC (USER)

CLLLC PRI + NPC  
AUX SUPPLY

Design under use:  
(ref datasheet for graph)  
Constant  $F_s=100\text{kHz}$   
Timing R to pin 4: 30k  
Timing C to pin 4: 470p

CLLLC SEC (HVDC)  
AUX SUPPLY

				MB	
Title					
02 OBC Aux Supply					
Size A3	Document Number <Doc>				Rev A2
Date:	Tuesday, December 13, 2022		Sheet	3	of 53

AC:  
Line to N - 265V rms  
Line to Line - 480V rms

DC link:  
800V/15A between PFC\_OUT to PFC\_NEG  
400V between PFC\_OUT to GND-P  
400V between PFC\_NEG to GND-P

TO PFC CHOKE BOARD (S4)

TO BULK CAP BOARD (S3)

TO L1 NPC BOARD

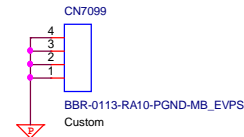
TO L2 NPC BOARD

TO L3 NPC BOARD

Thermistor

Thermistor

Thermistor

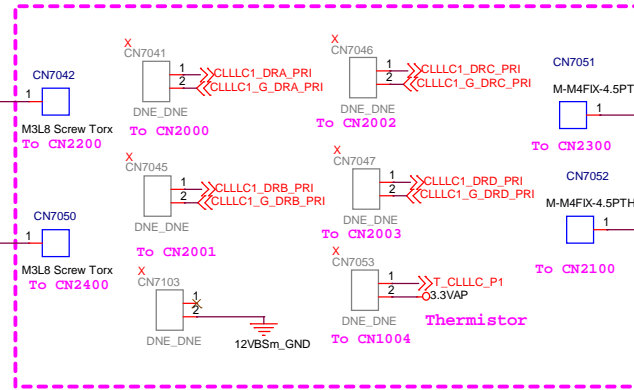


MB		
Title		
03 AC Current Sense + Connectors		
Size	Document Number	Rev
A3	<Doc>	A2
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# TO BULK CAP BOARD (S3)



## TO PRI FB BOARD

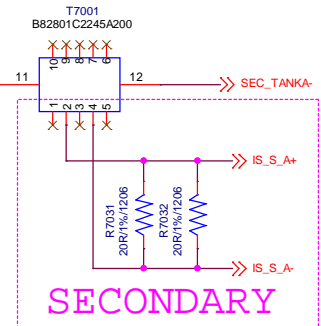
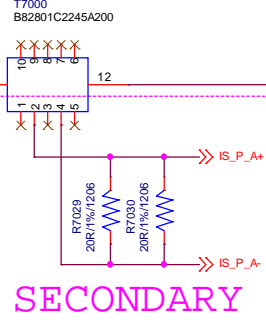


AC (flywire to XFMR)  
800V peak/20A max

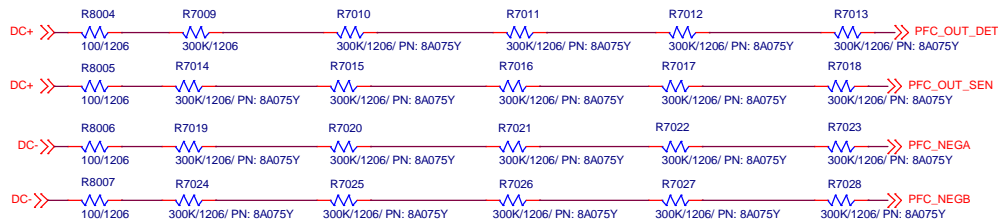
## TO CLLC TRANSFORMER

# SECONDARY

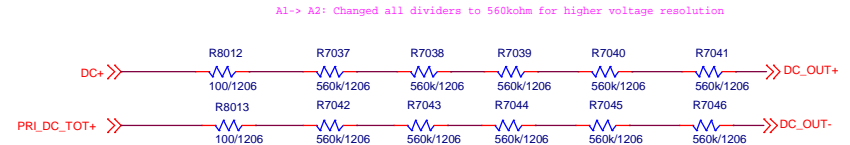
AC (flywire to XFMR)  
470V peak/45A max



PQI6528+PQ6554+PQI6534.4  
158uH/29uH  
Lrp=35uH / Lrs=21uH  
8:21:9:8

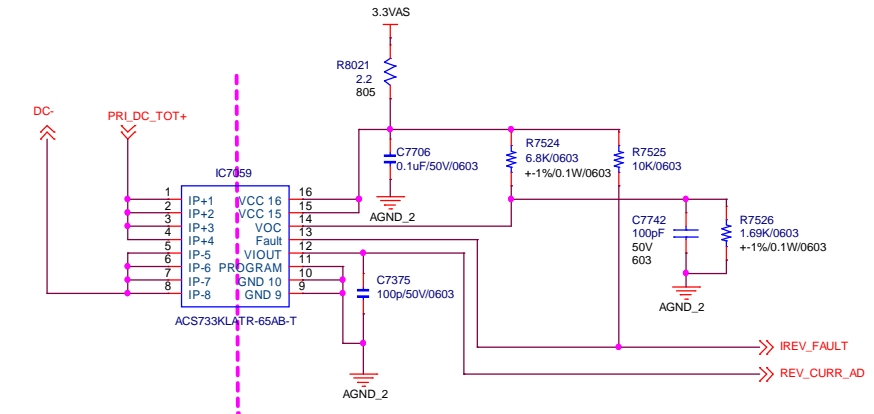
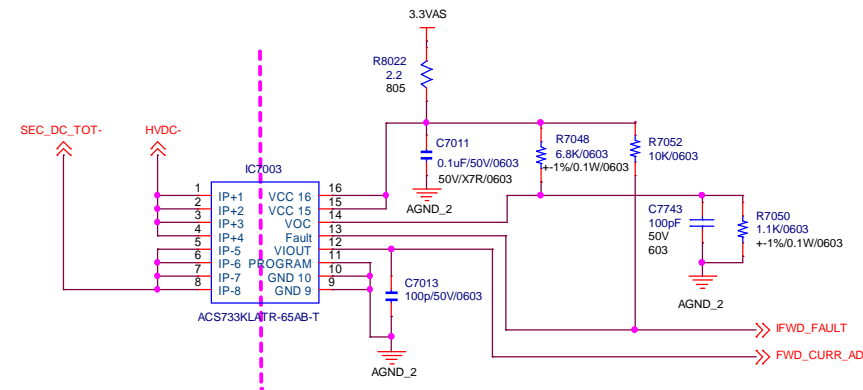
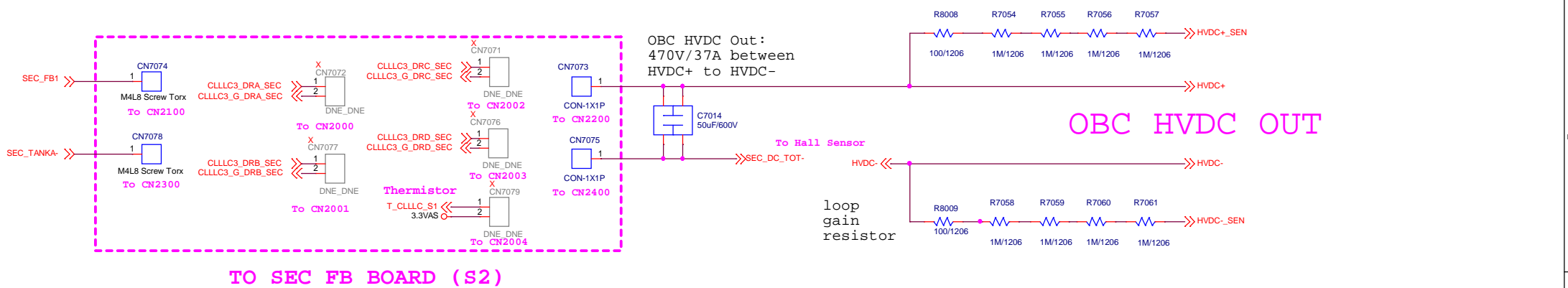


To Primary MCU



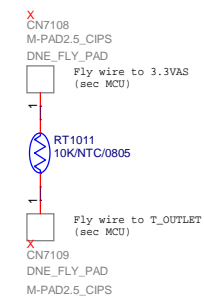
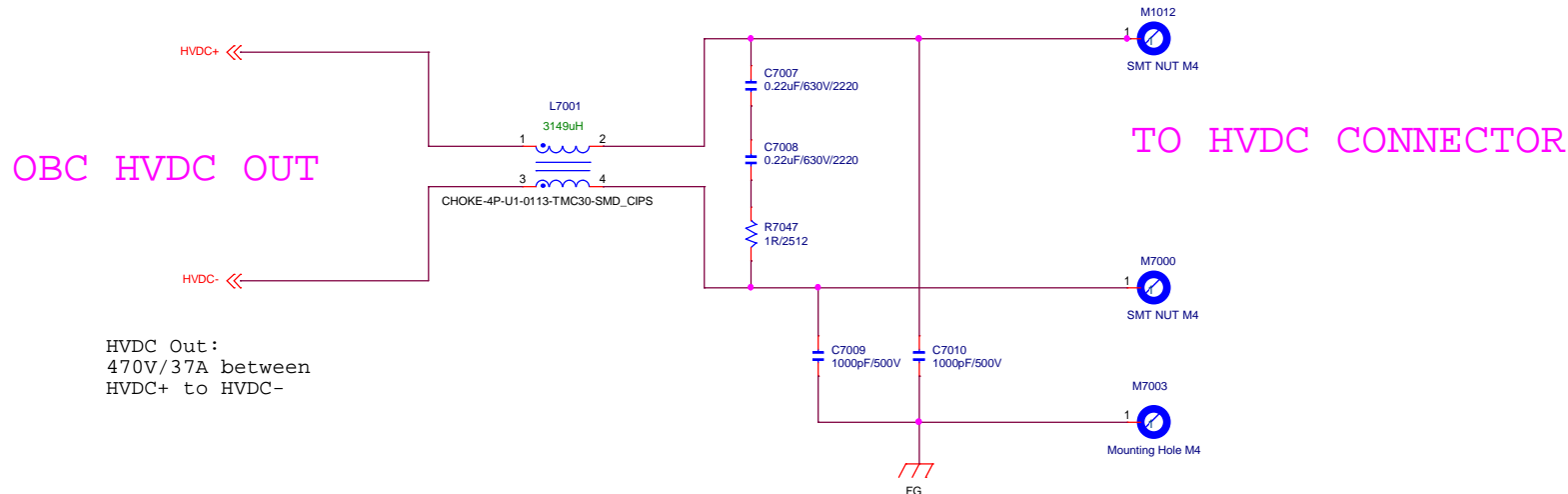
To Secondary MCU

MB		
Title		
04 CLLC DC/DC Connectors		
Size	Document Number	Rev
A3	<Doc>	A2
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CLLLC FORWARD DC CURRENT SENSE

CLLLC REVERSE DC CURRENT SENSE

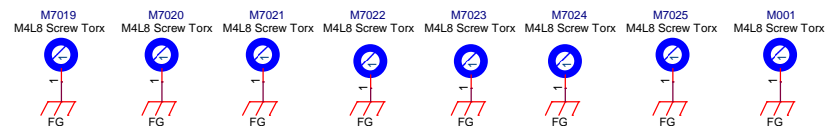
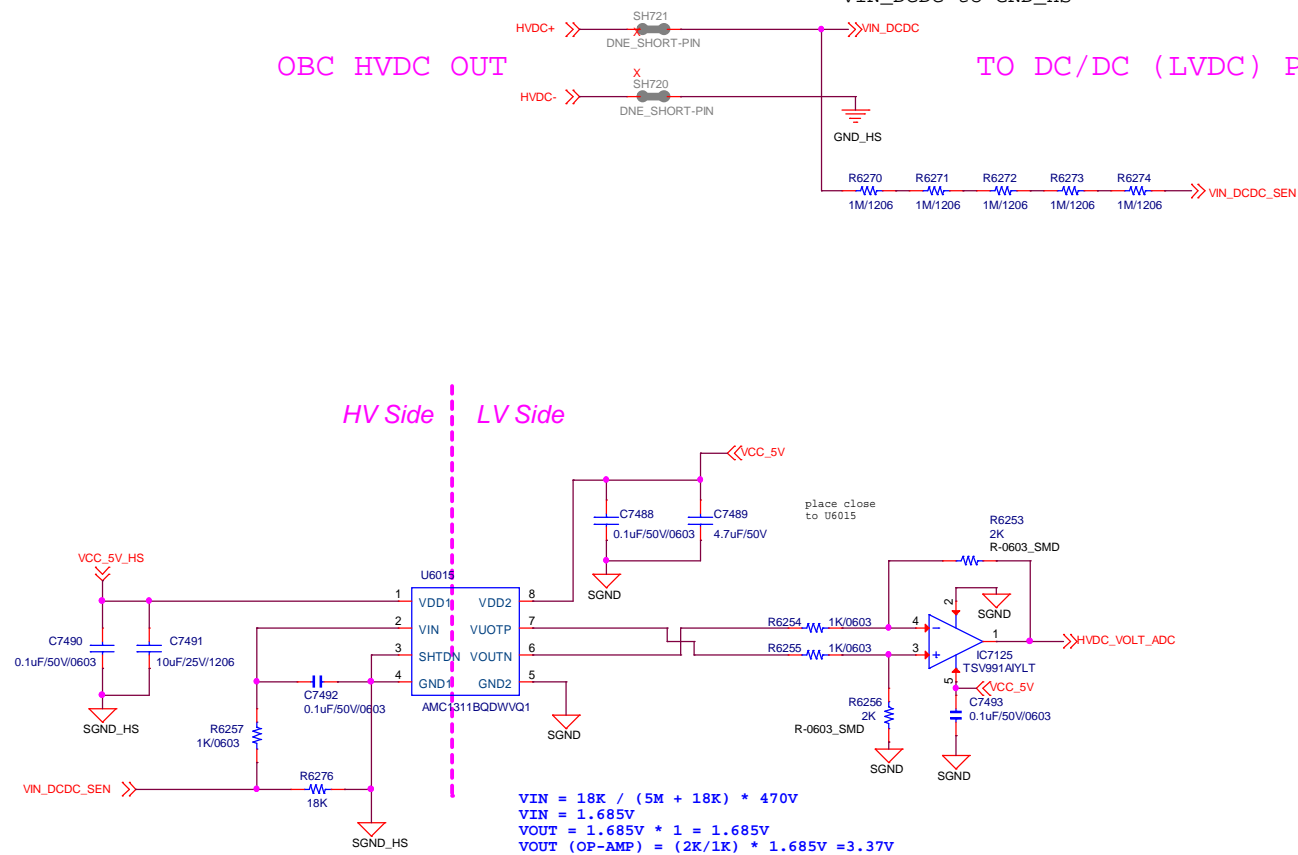


OBC HVDC Out:  
470V/12A between  
HVDC+ to HVDC-

DC/DC Primary:  
470V/12A between  
VIN\_DCDC to GND\_HS

OBC HVDC OUT

TO DC/DC (LVDC) PRIMARY



# DC/DC (LVDC) SECONDARY

CS\_LIM = 2.06V (Based on Datasheet)

$I = P / V$

$I = 3600 / 250$

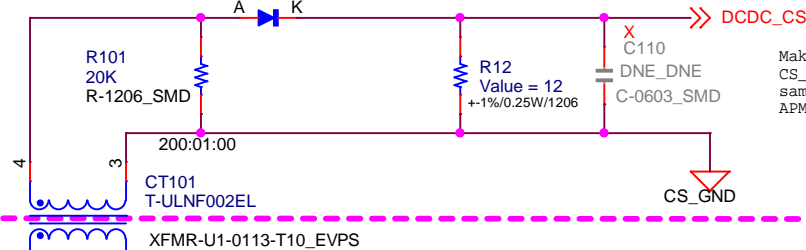
$I = 14.4A$  (RMS)

$I = 20.36A$  (PEAK)

$V = 20.36 * (1/125) = 0.16292V$

$V = 0.16292V * 10 = 1.6292V$

D101  
PMEG10010ELRX  
D-SOD123FL-KA\_SMD



LV Side

HV Side

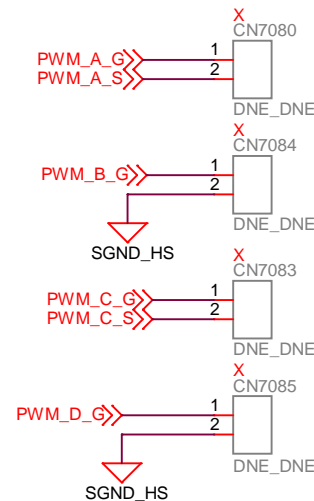
VIN\_DCDC >>

DC/DC Primary:  
470V/12A between  
VIN\_DCDC to GND\_HS

C111A C111C  
2.2n/1000V

C111B C111D  
2.2n/1000V

GND\_HS



TO PSFB  
BRD A HOLE

470V  
BOTTOM

TO T201  
SHIM\_A

TO PSFB  
BRD B HOLE

470V  
TOP

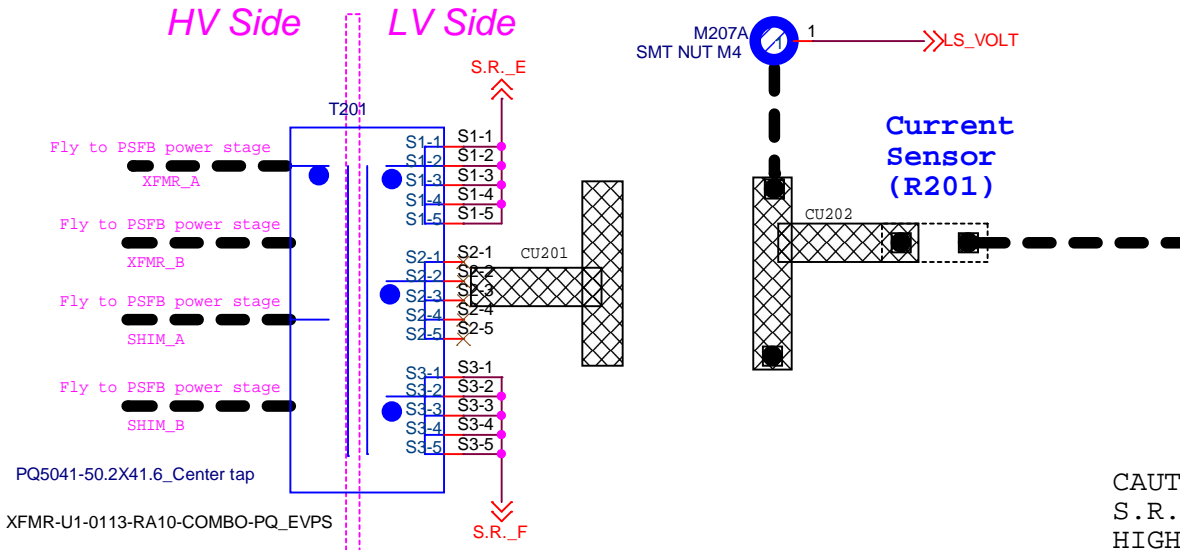
TO T201  
XFMR\_B

TO PSFB  
GND\_HS

# DC/DC (LVDC) PRIMARY

MB		
Title	07 PSFB Power Stage	
Size	Document Number	Rev A2
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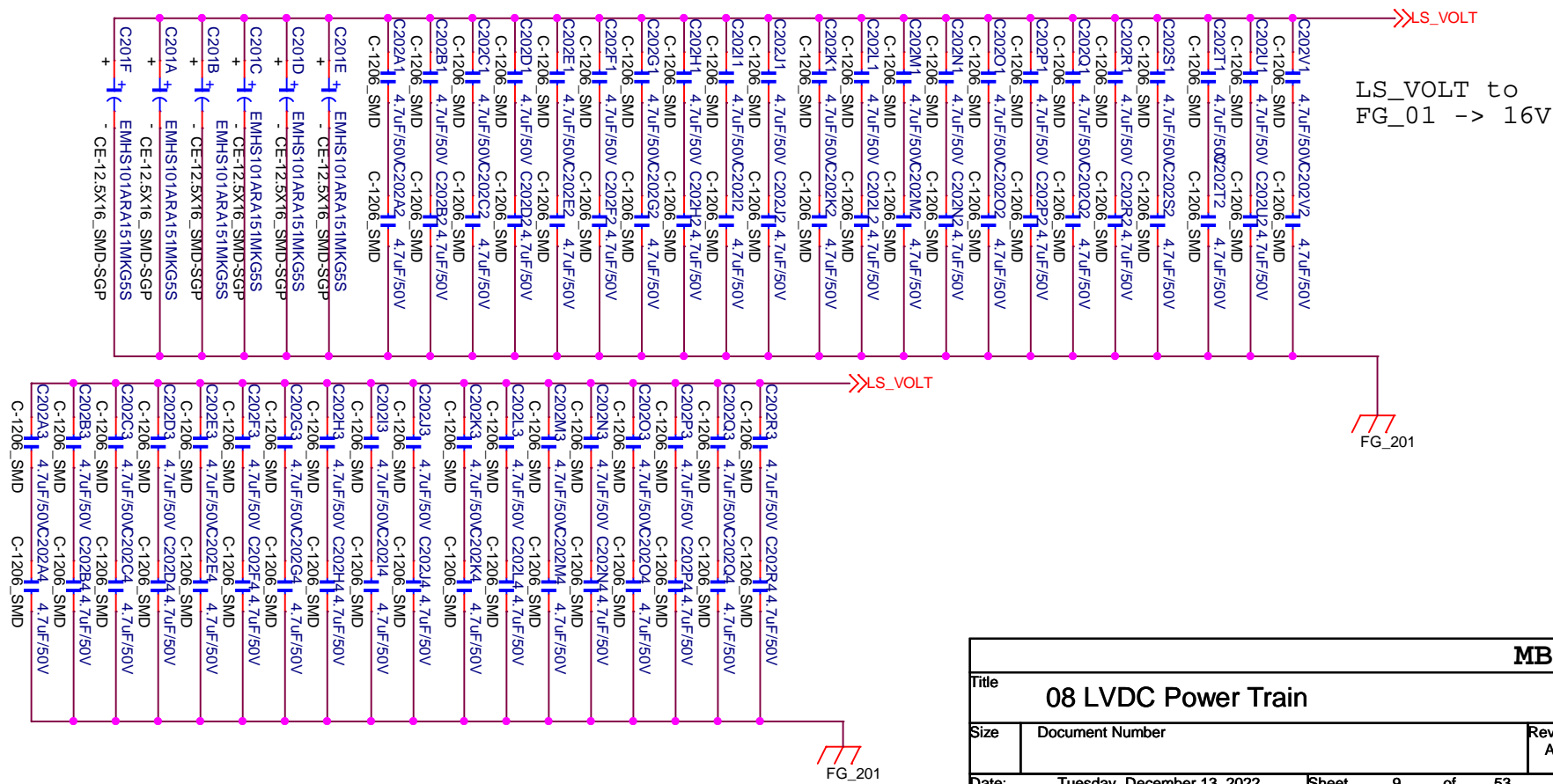
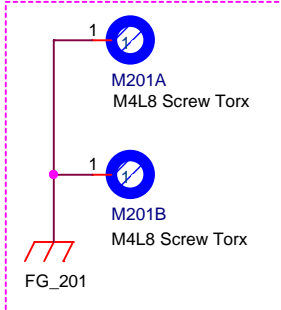


# DC/DC (LVDC) SECONDARY

NOTE:  
Inductor L201 to be changed  
-> get new Footprint and design from Magnetics and update

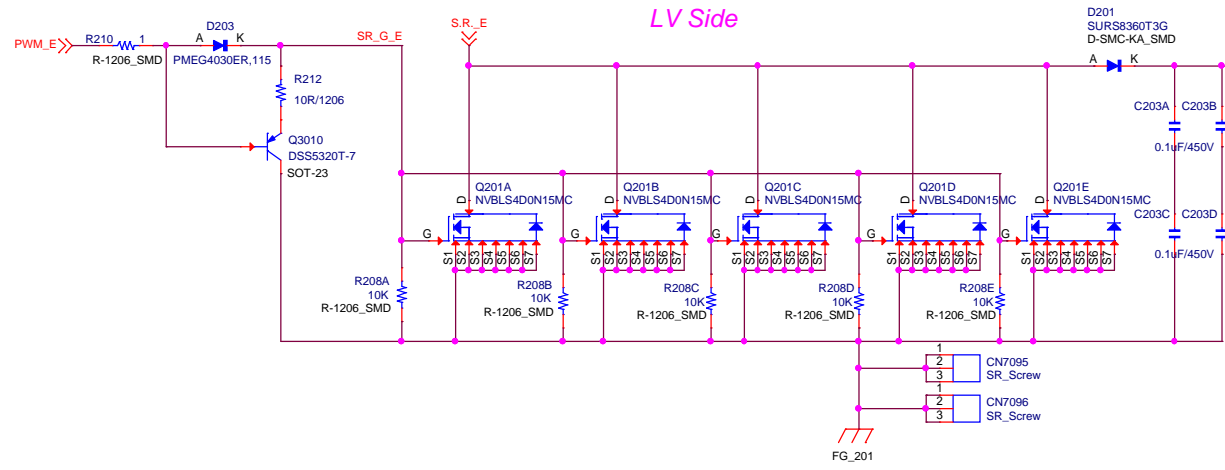
CAUTION on LV SIDE  
S.R.\_E to FG\_201 and S.R.\_F to FG\_201  
HIGH CURRENT OUTPUT (130.5A each -> Total 261A)  
16V max

## Output CAP Grounding



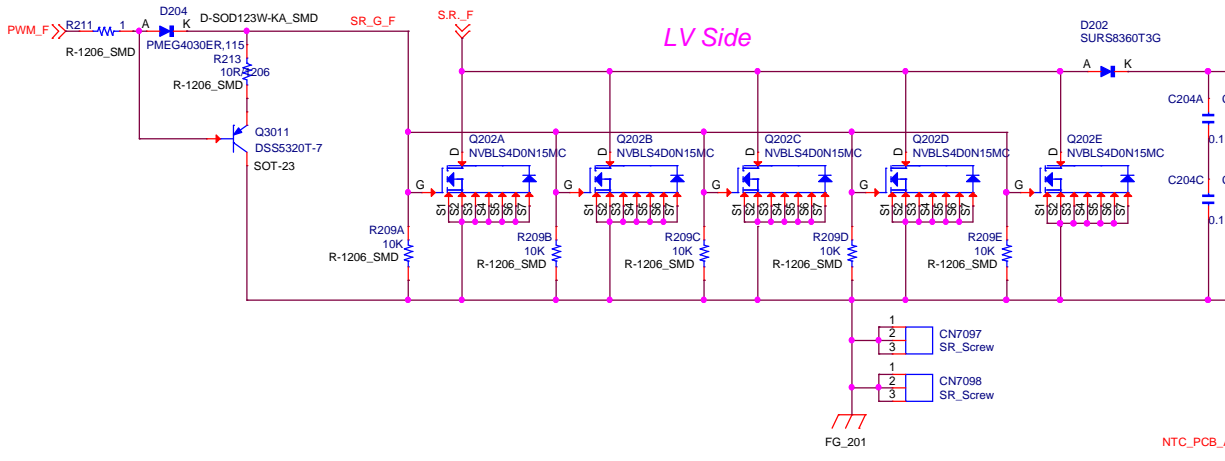
MB		
Title		
08 LVDC Power Train		
Size	Document Number	Rev
		A2
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S.R. DRIVER MOSFET



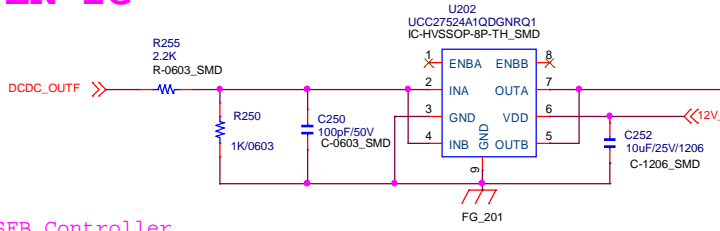
CAUTION on LV SIDE  
S.R.\_E to FG\_201 and S.R.\_F to FG\_201  
HIGH CURRENT OUTPUT (130.5A each -> Total 261A)  
16V max

DC/DC (LVDC) SECONDARY



Temp sensor for SR

S.R. DRIVER IC

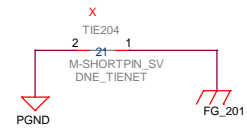
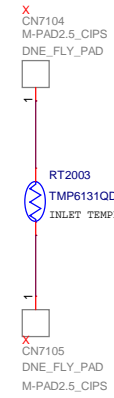
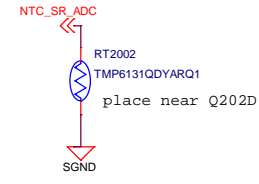
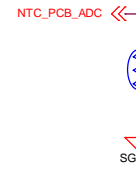
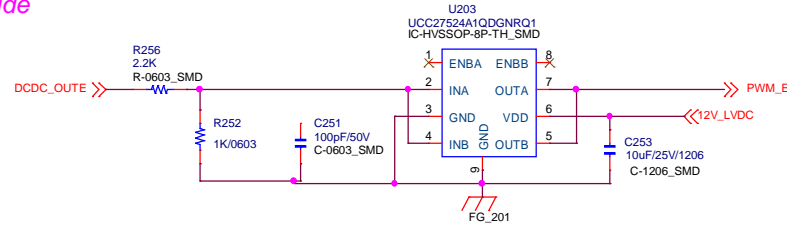


Input from PSFB Controller

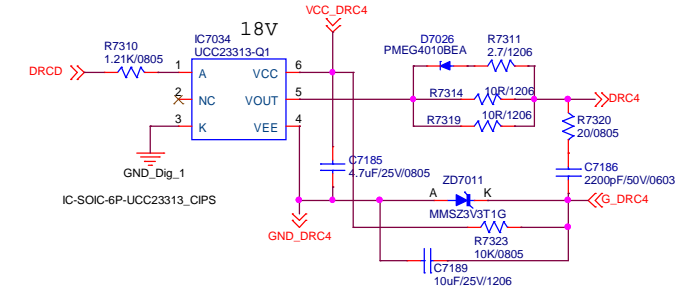
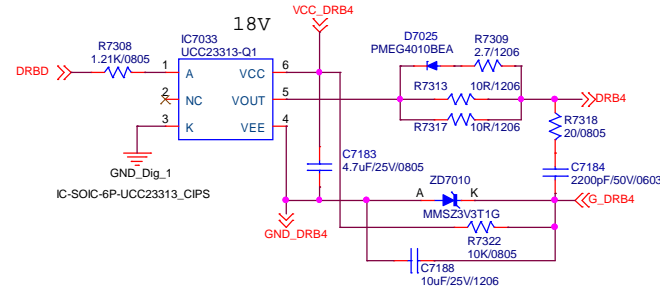
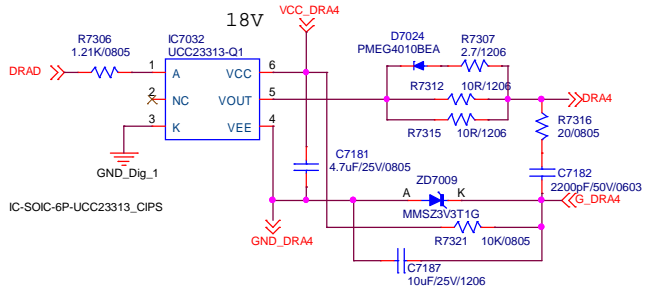
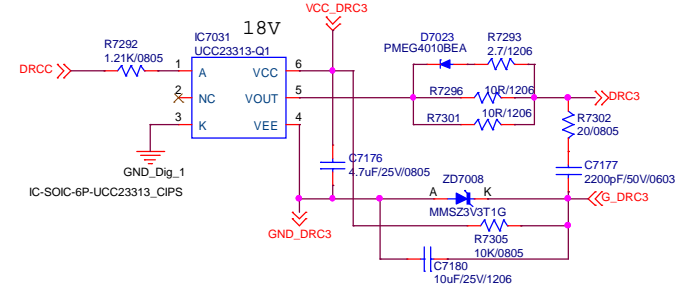
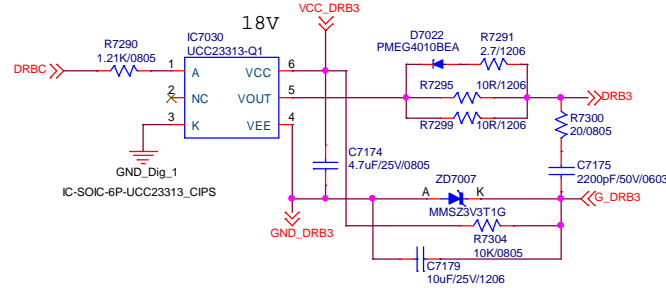
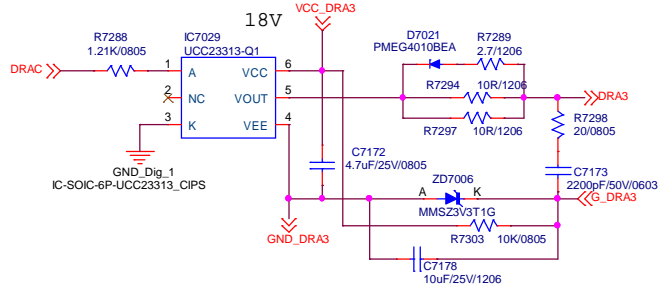
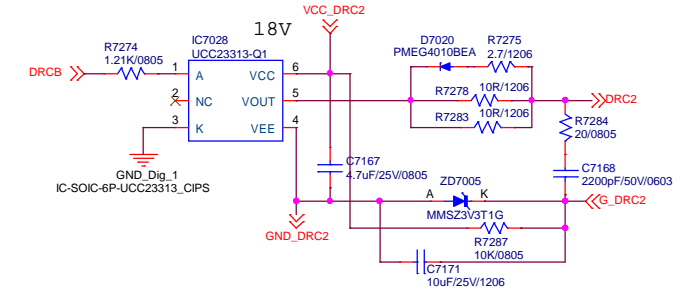
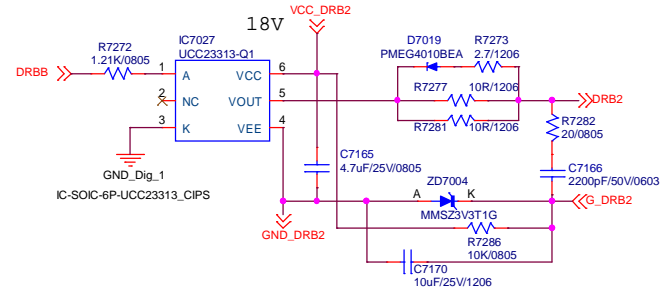
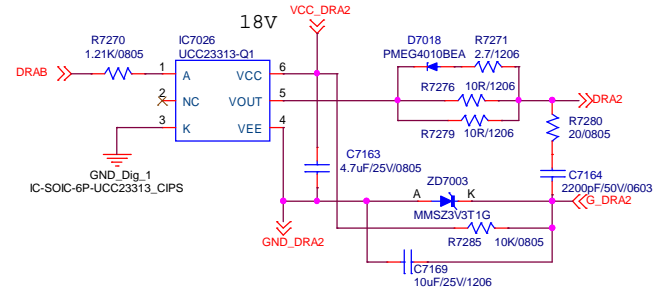
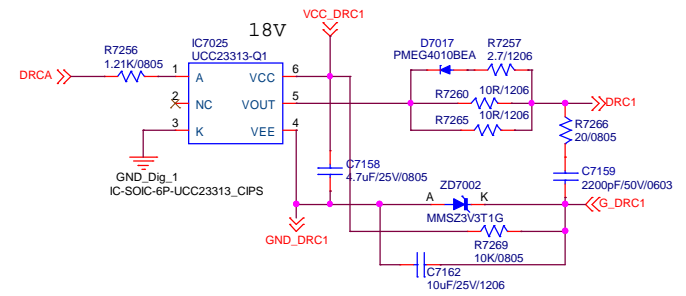
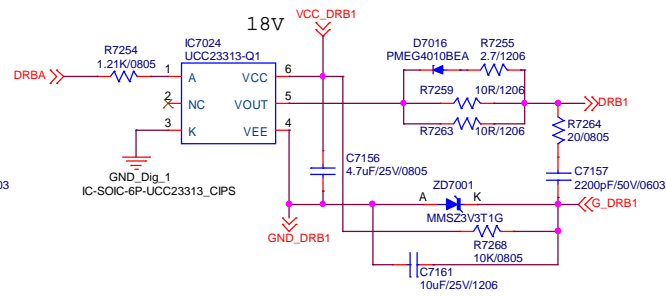
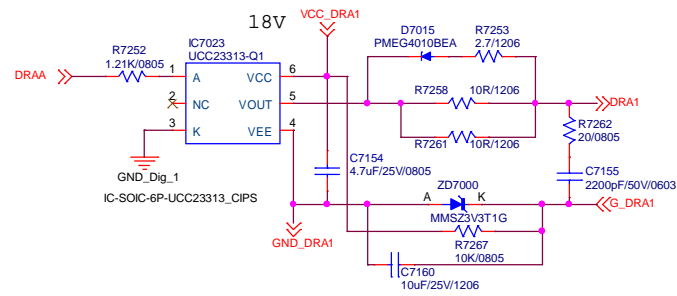
Output to S.R. MOSFETs

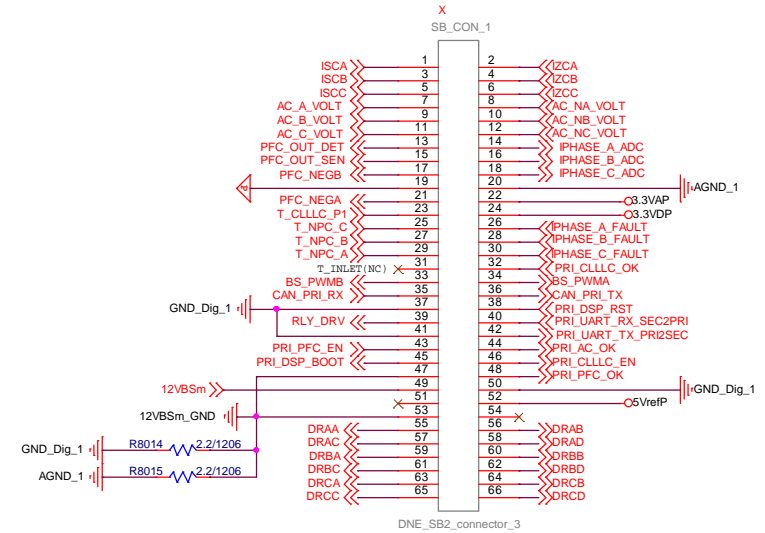
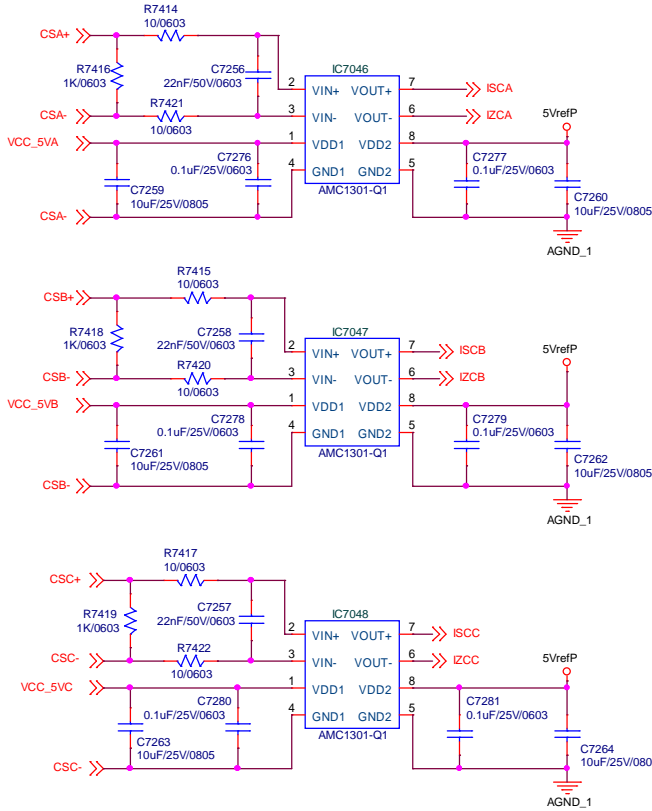
LV Side

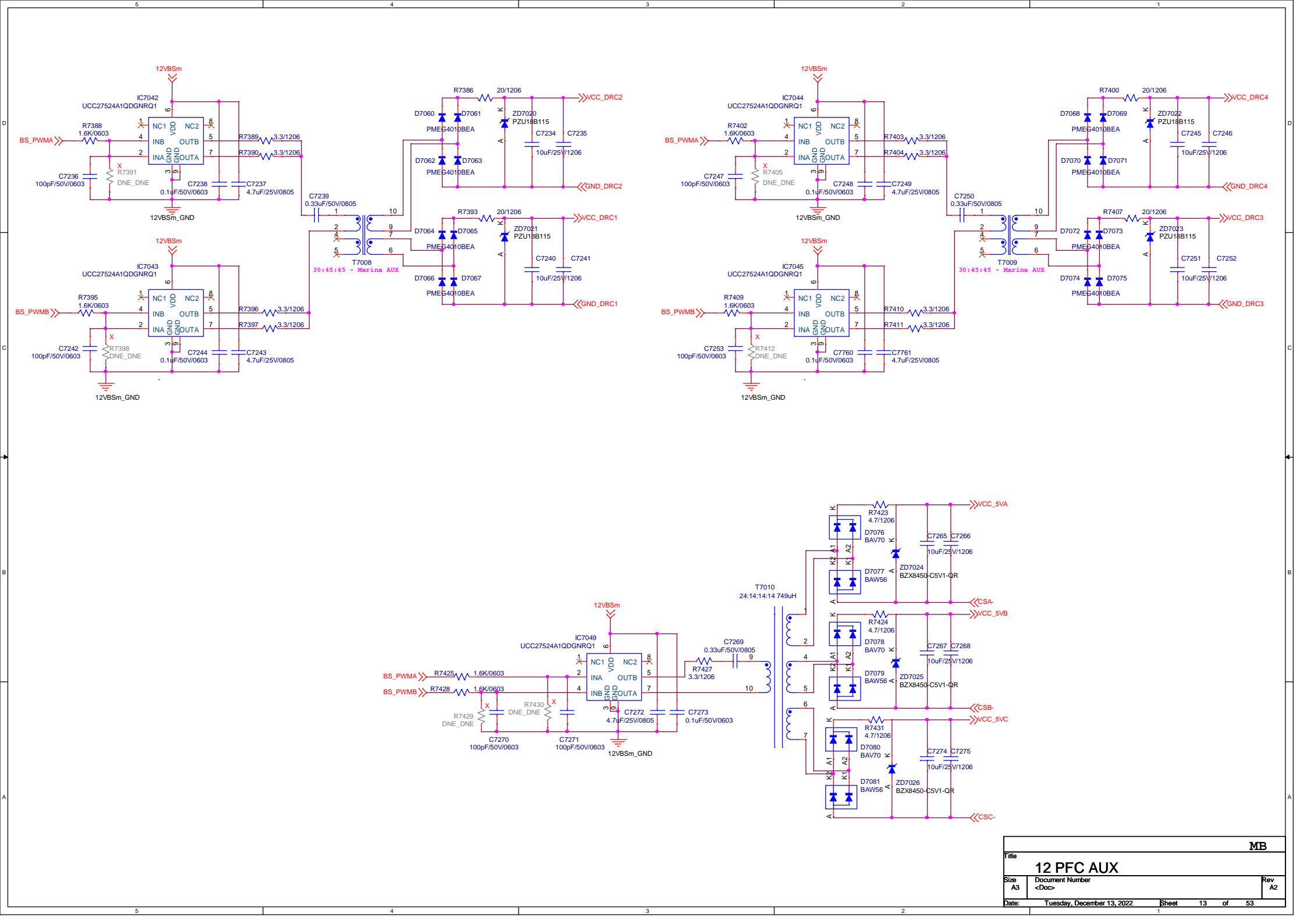
LV Side



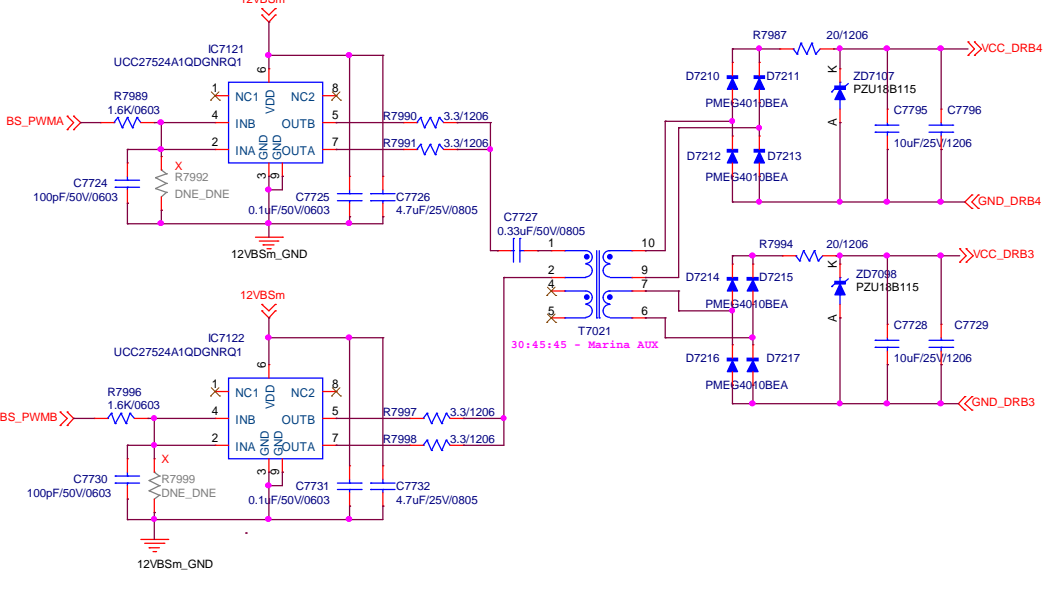
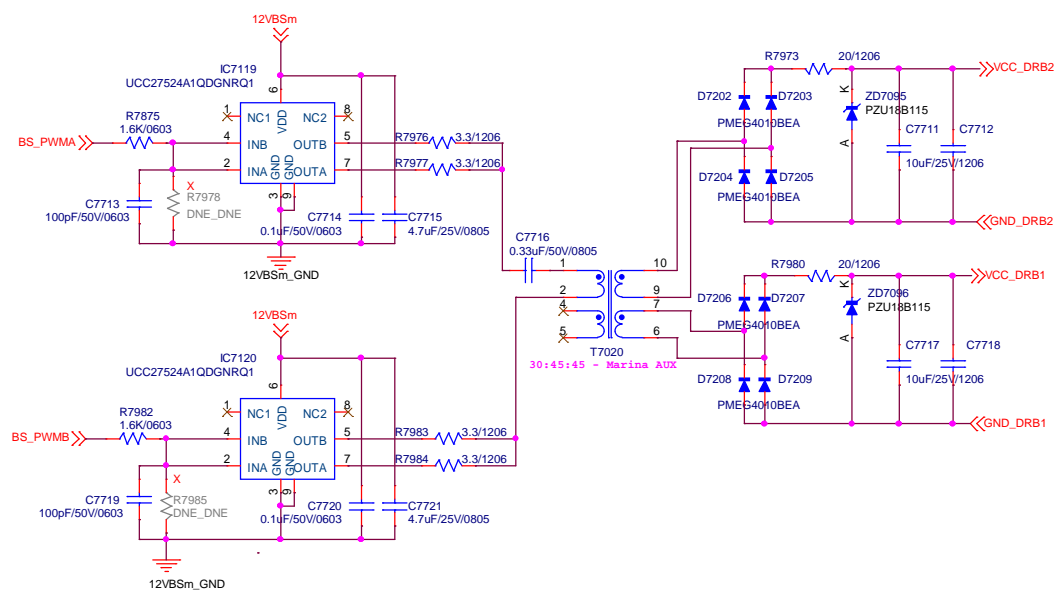
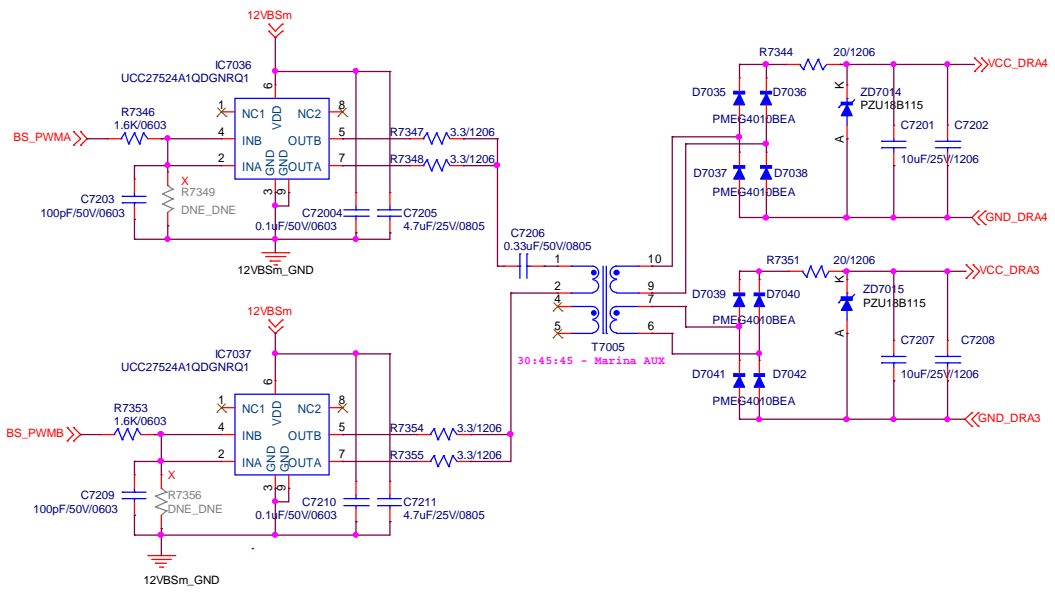
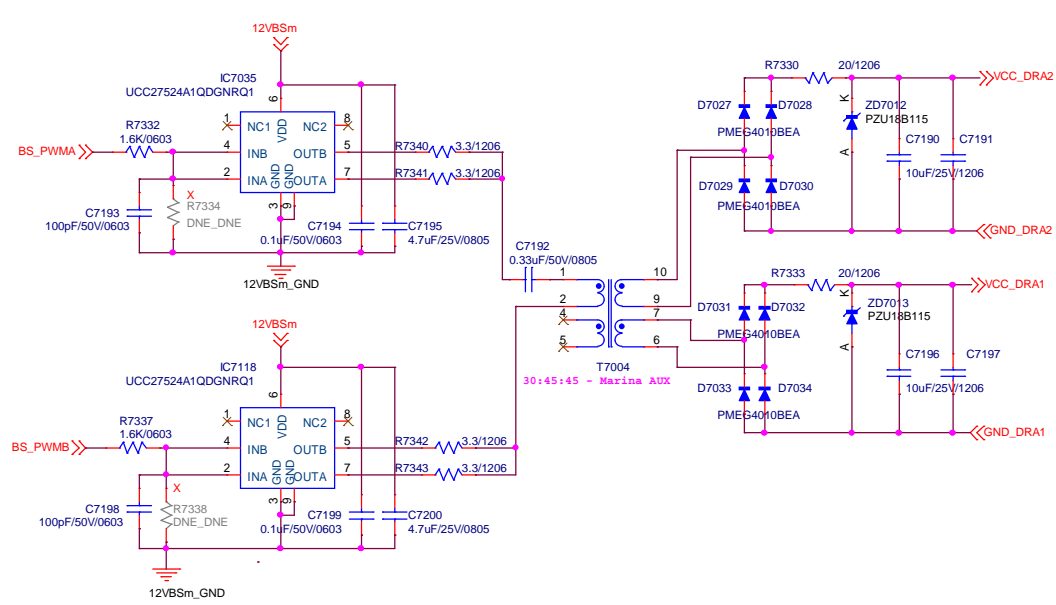
MB		
Title 09 SR MOSFET		
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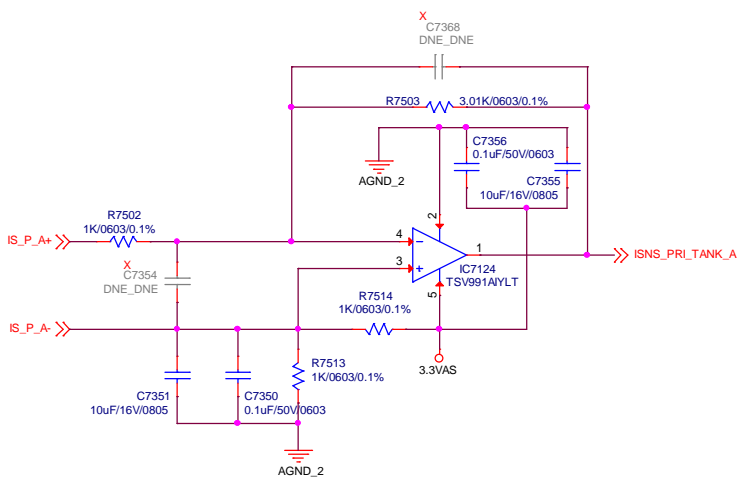




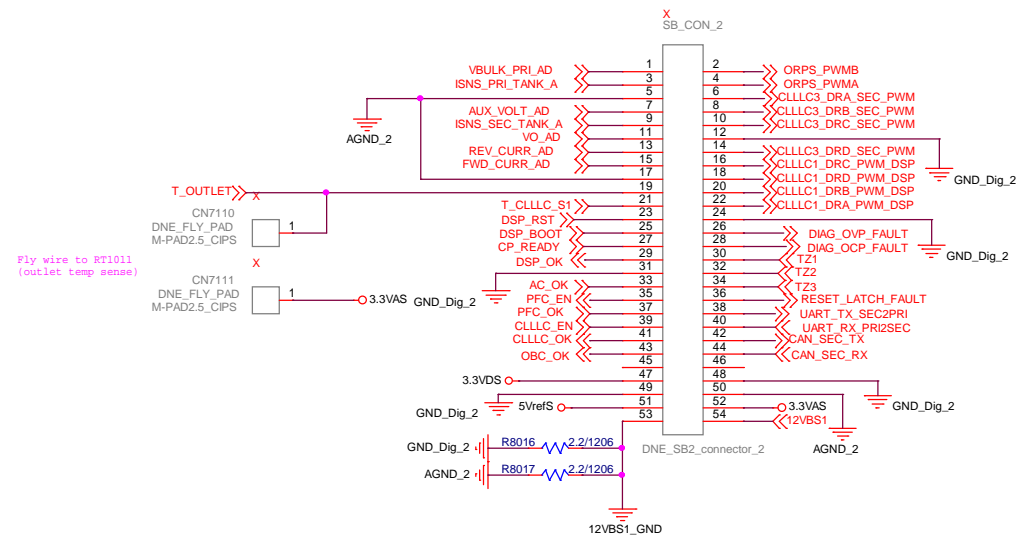
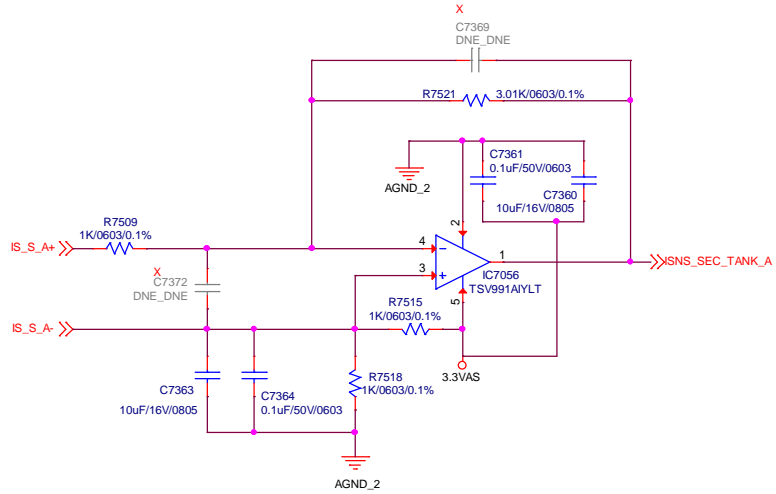
MB		
Title		
12 PFC AUX		
Size	Document Number	Rev
A3	<Doc>	A2
Date: Tuesday, December 13, 2022		
Sheet 13 of 53		



# CLLLC PRI TANK CURRENT SENSE



# CLLLC SEC TANK CURRENT SENSE



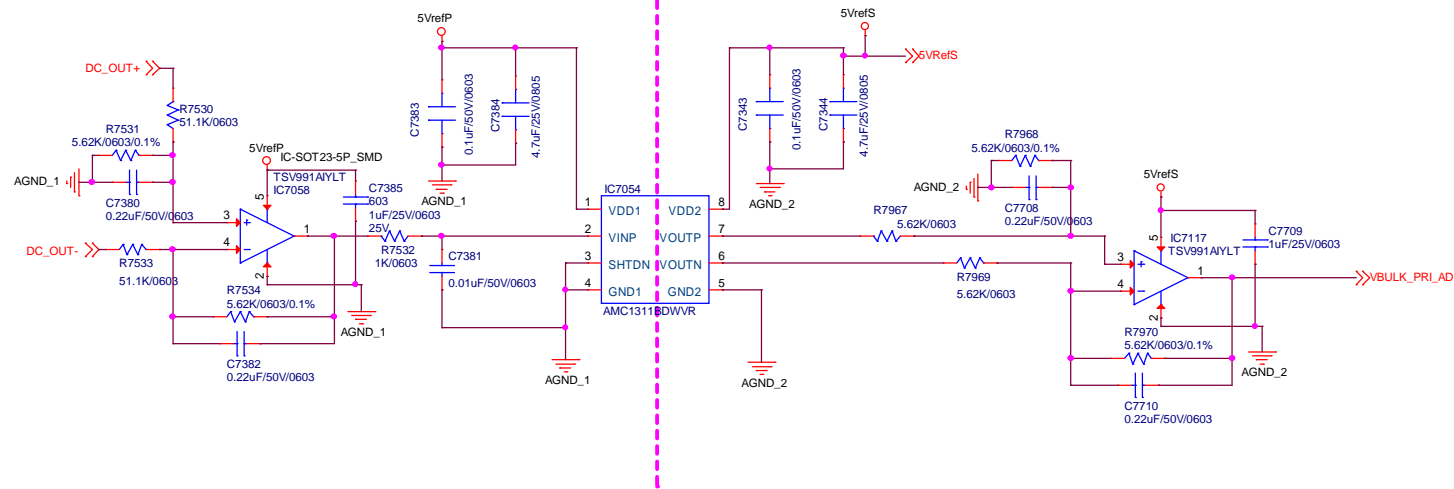
TO SECONDARY MCU (S7)

Title			14 CLLC CS and Connector
Size	Document Number	Rev	A2
A3			
Date:	Tuesday, December 13, 2022	Sheet	15 of 53

# PRIMARY DC-LINK VOLTAGE SENSE

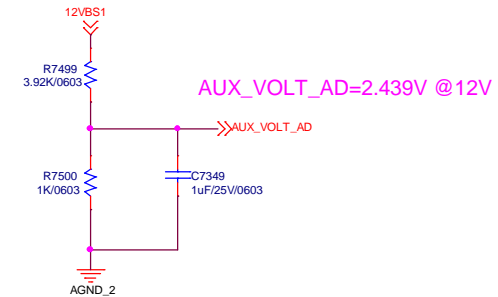
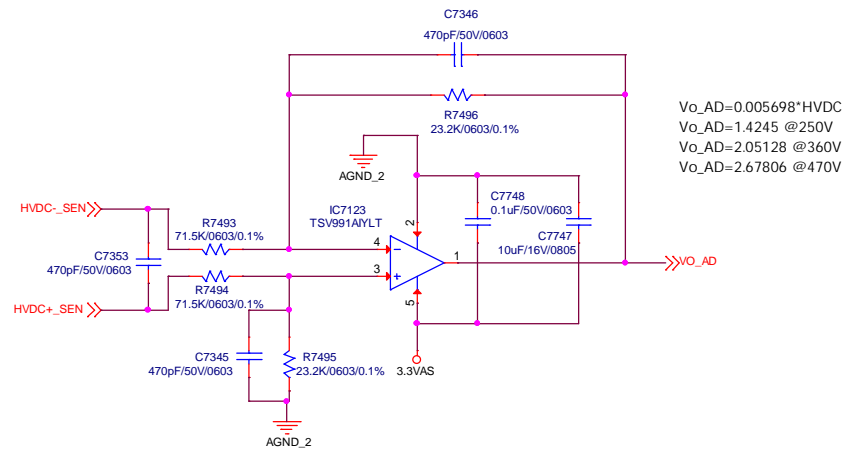
## PRIMARY

## SECONDARY



# HVDC VOLTAGE SENSE

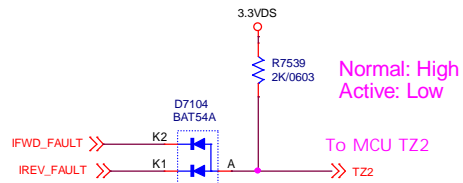
# 12V STANDBY VOLTAGE SENSE



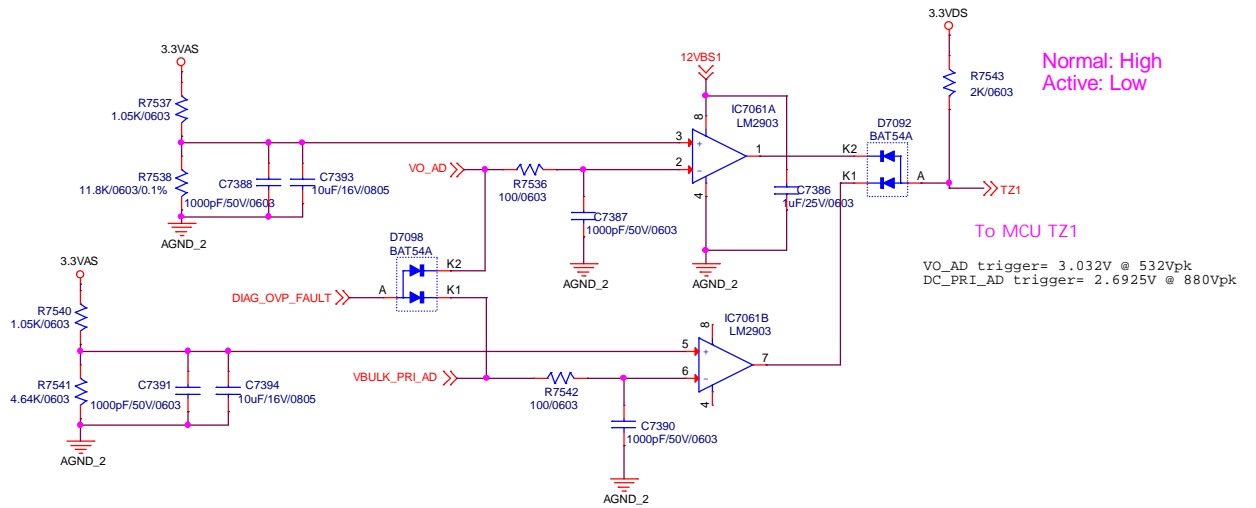
MB		
Title	15 Secondary Voltage Sense	
Size	Document Number	Rev
A3	<Doc>	A2
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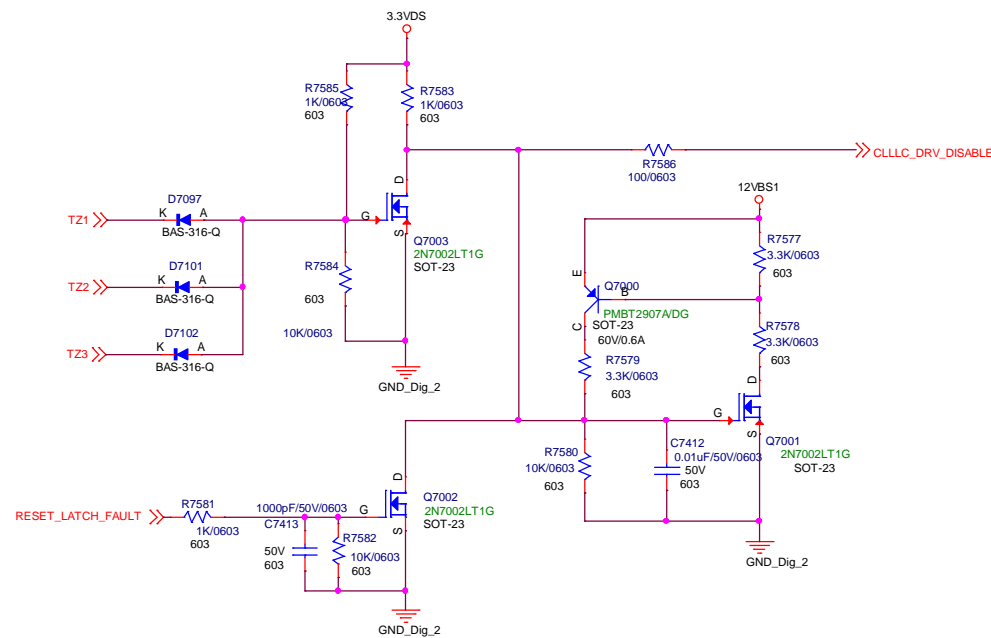
## CLLLC DC CURRENT HW OCP



## CLLLC DC VOLTAGE HW OVP

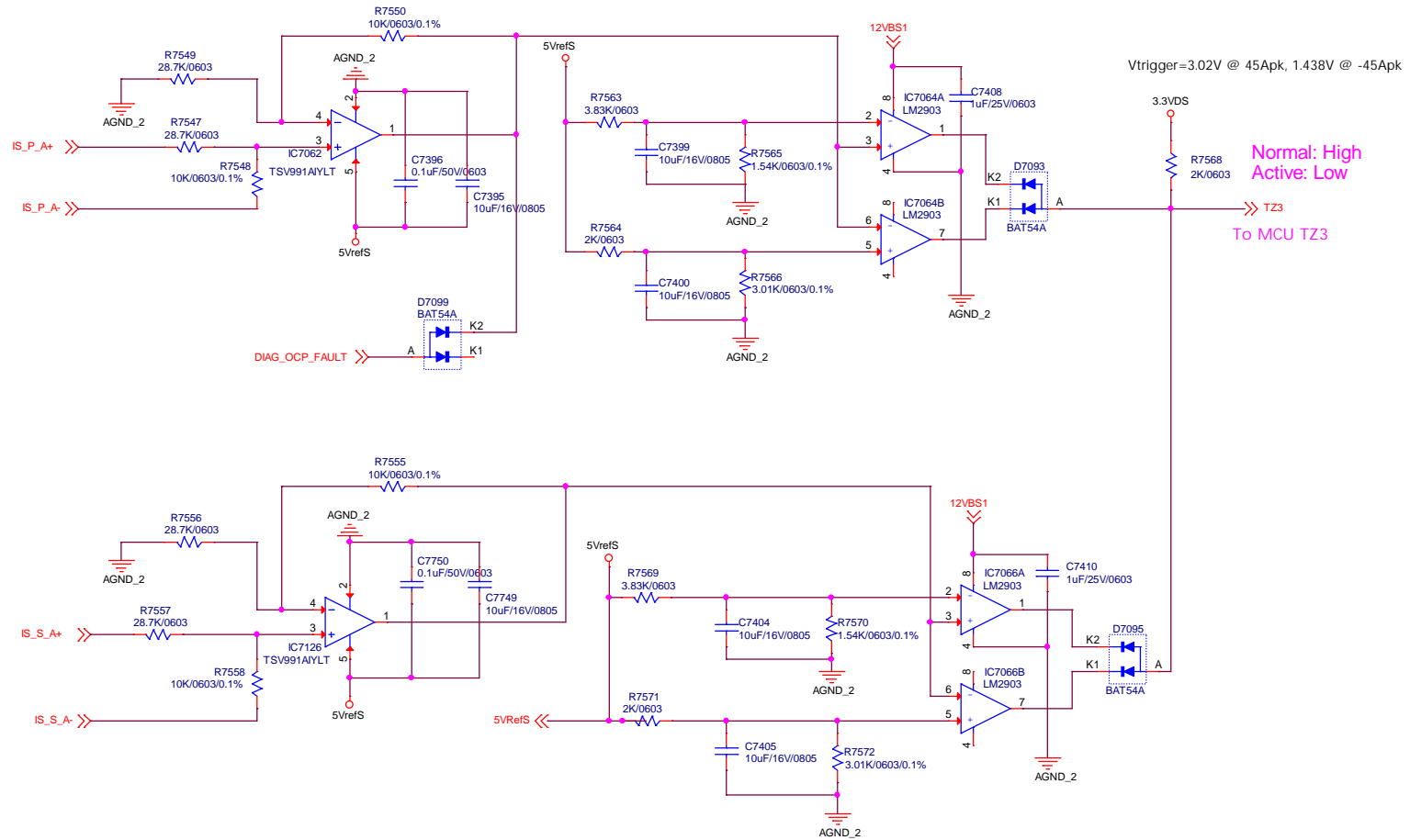


## HW LATCH CIRCUIT DRIVE DISABLE



MB		
Title	16 Secondary Protection	
Size	Document Number	Rev
A3	<Doc>	A2
Date:	Tuesday, December 13, 2022	Sheet 17 of 53

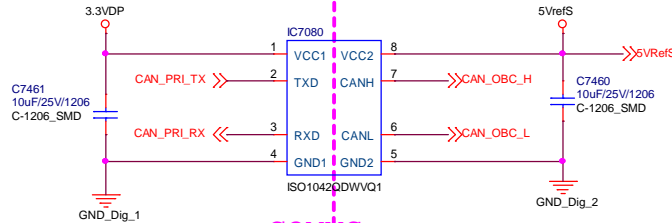
# CLLLC TANK CURRENT HW OCP



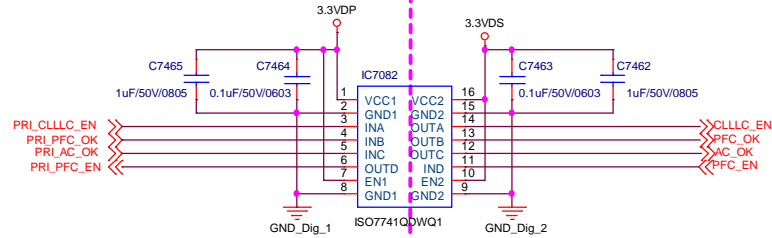
# PRIMARY

## INTERNAL PRIMARY CAN

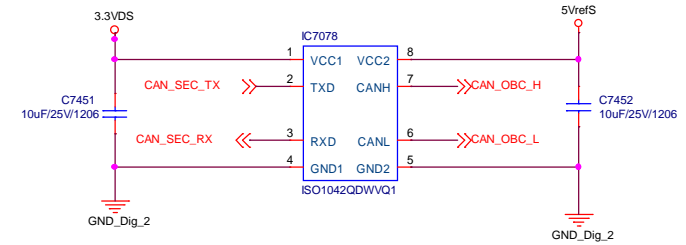
## SECONDARY



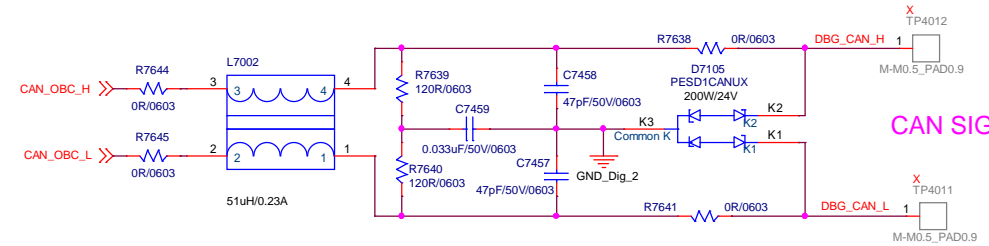
## COMMS



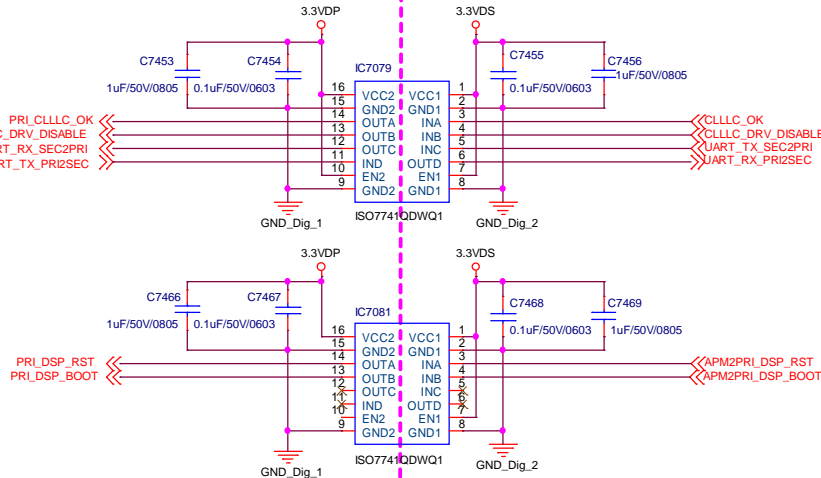
## INTERNAL SECONDARY CAN



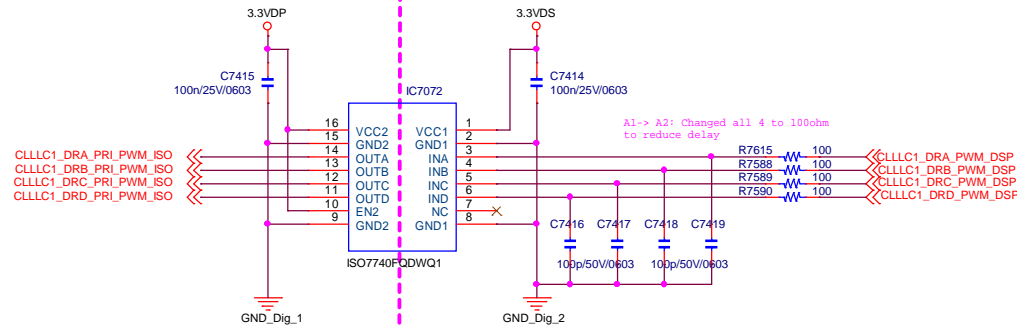
## OBC CAN DEBUG SIGNALS



## CAN SIGNAL



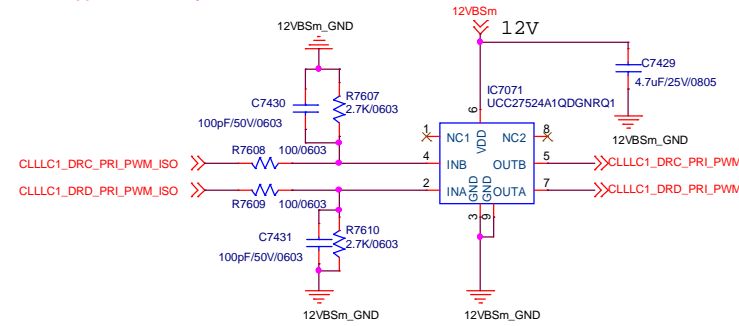
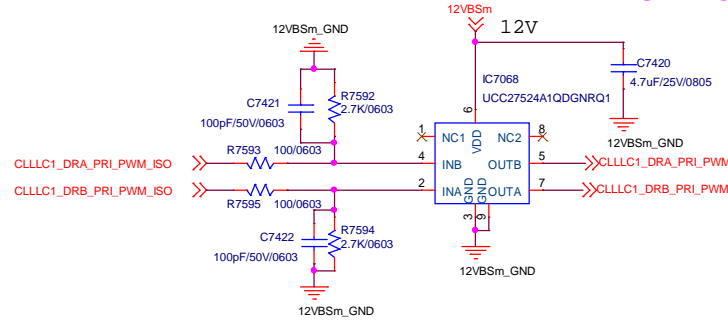
## CLLLC GATE DRIVE SIGNAL ISOLATION



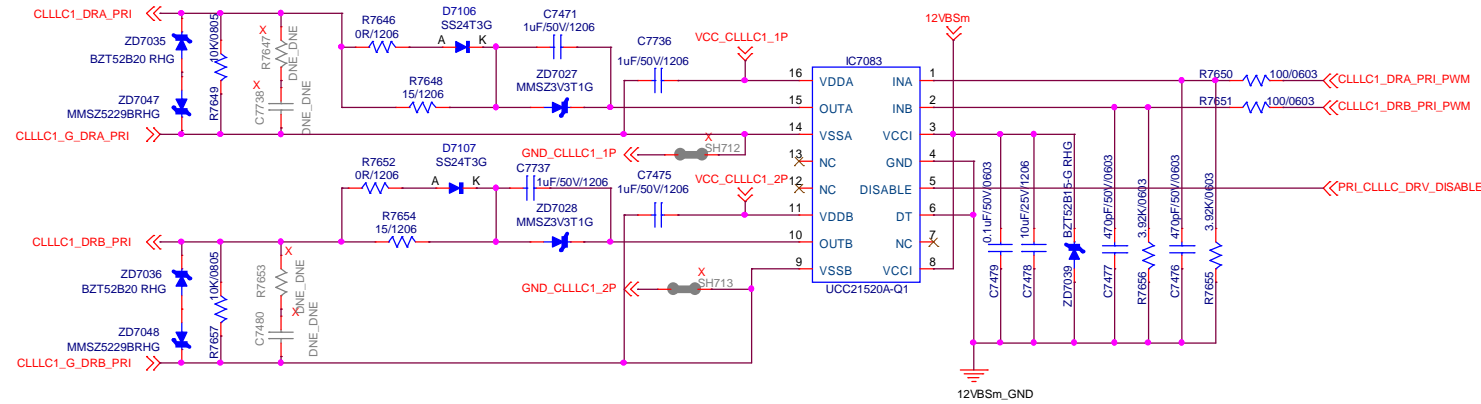
Title	MB
Size	A3
Document Number	Sec Internal Comms
Date:	Tuesday, December 13, 2022
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Rev	A2

# PRIMARY

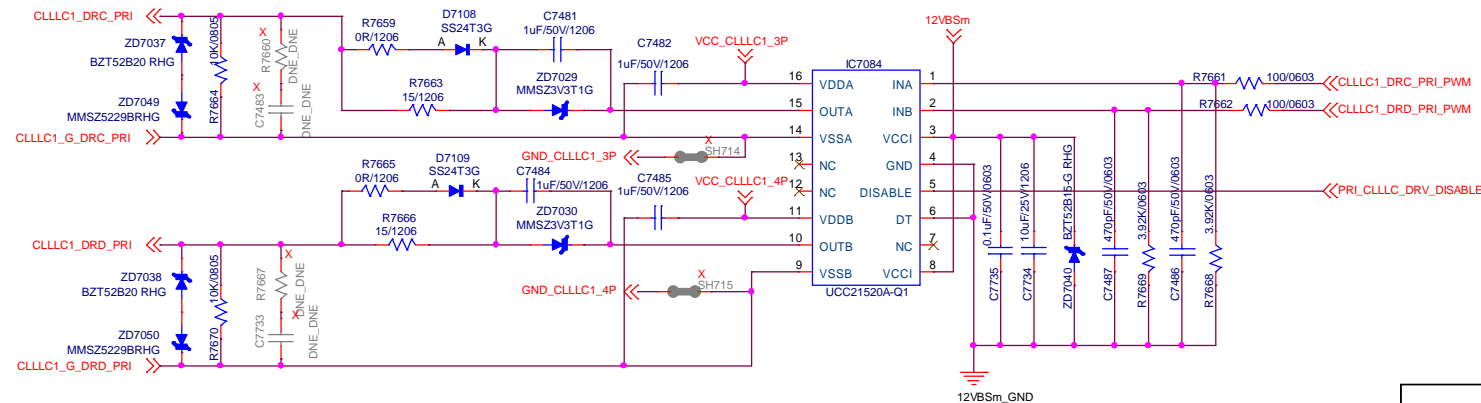
## CLLLC PRI PWM DRIVER 1



## CLLLC PRI PWM DRIVER 2



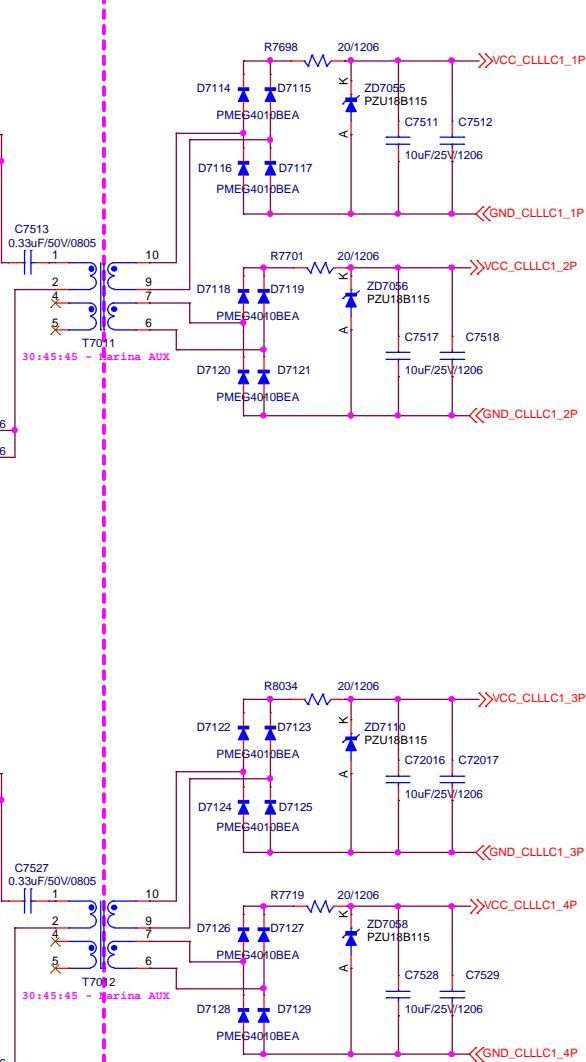
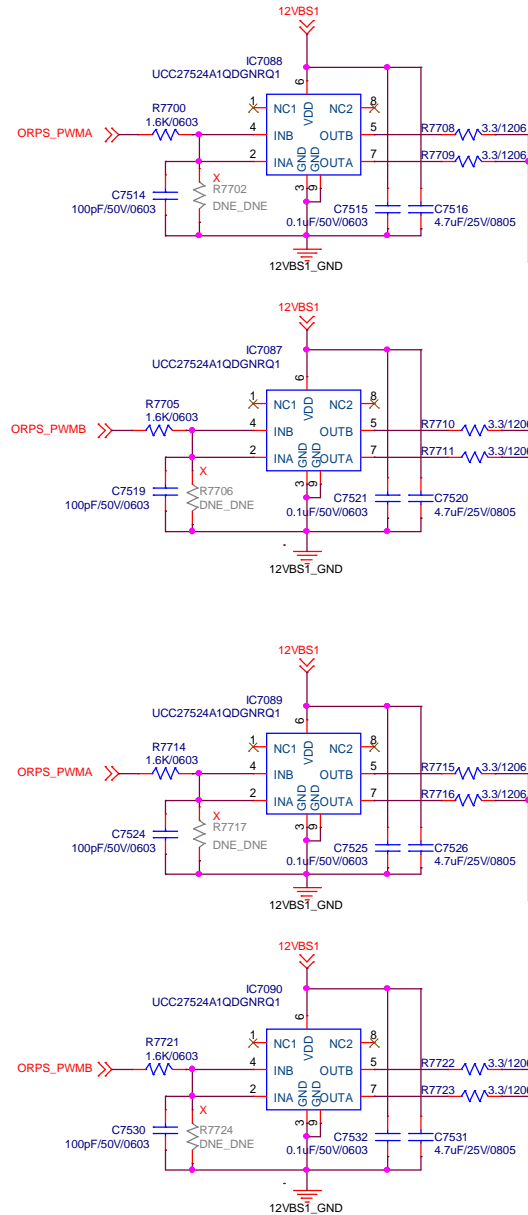
TO CLLLC  
PRIMARY MOSFETS



MB		
Title	19 Primary Full Bridge Driver	
Size	Document Number	Rev
A3	<Doc>	A2
Date:	Tuesday, December 13, 2022	Sheet 20 of 53

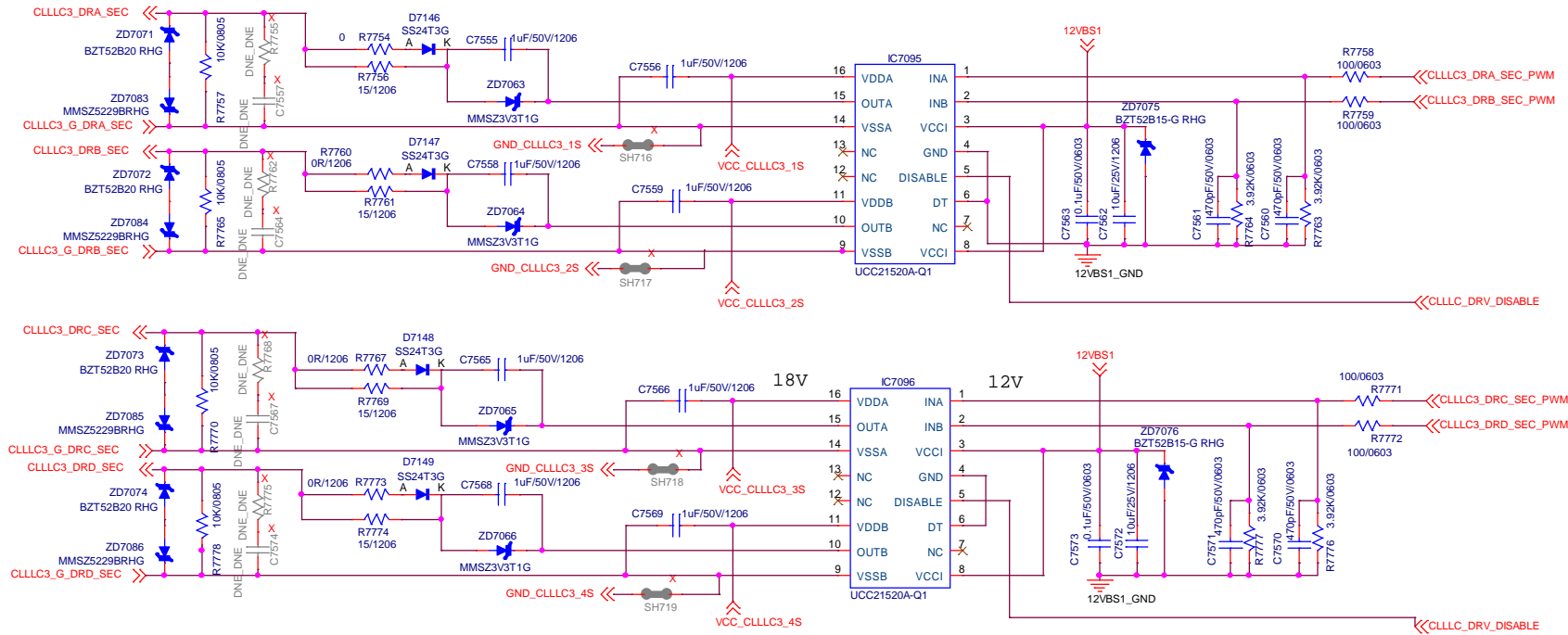
# SECONDARY

# PRIMARY



MB		
Title	20 Primary Full Bridge Bias	
Size A3	Document Number <Doc>	Rev A2
Date:	Tuesday, December 13, 2022	Sheet 21 of 53

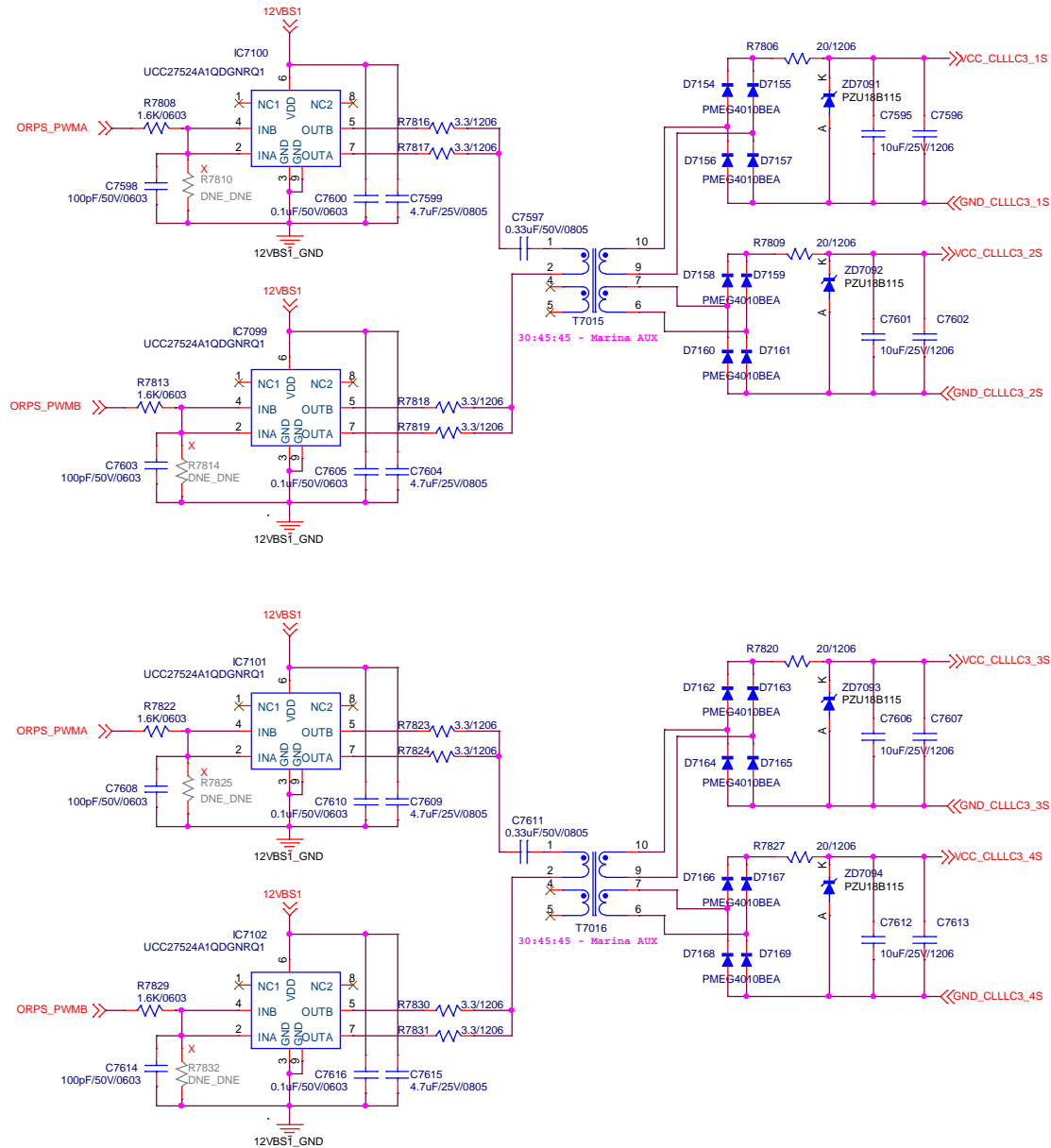
## SECONDARY



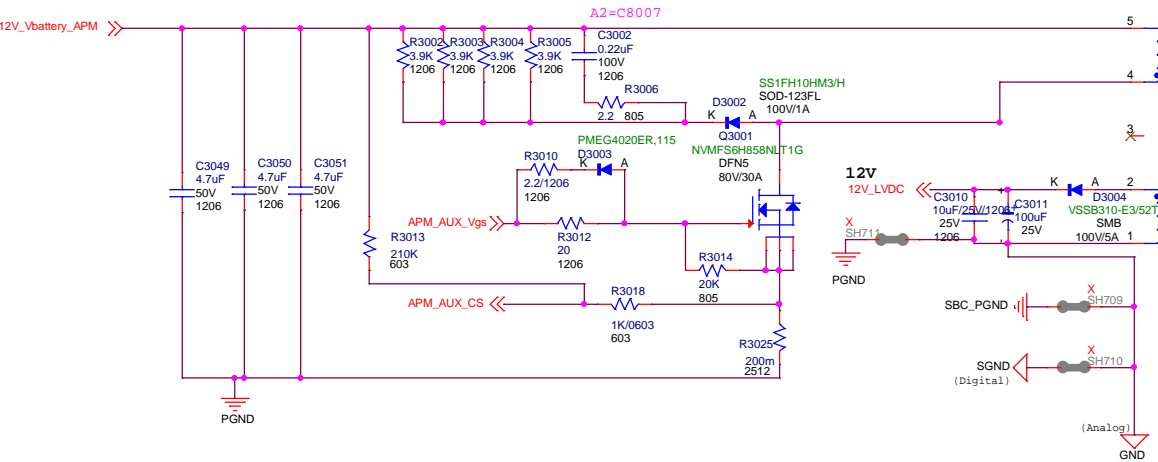
TO SECONDARY CLLLC\_A MOSFETS

MB			
Title	21 Secondary Full Bridge Driver		
Size	Document Number	Rev	
A3	<Doc>	A2	
Date:	Tuesday, December 13, 2022	Sheet	22 of 53

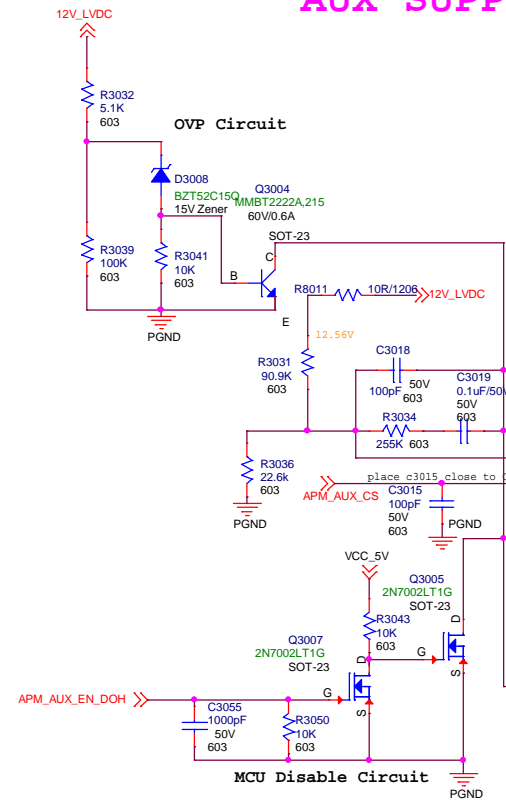
# SECONDARY



## Flyback for APM

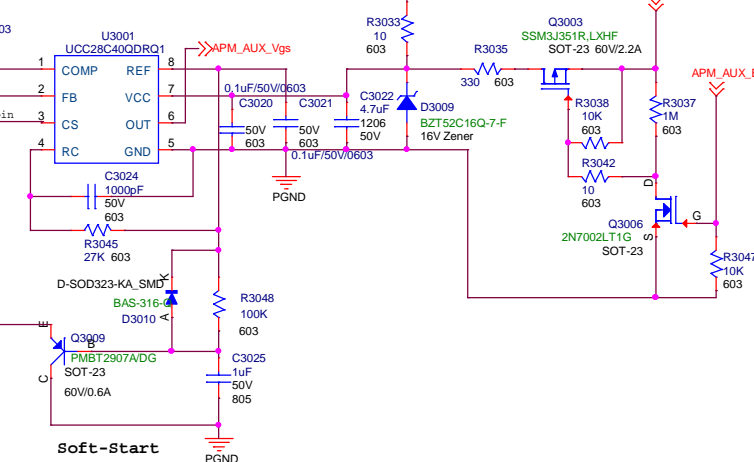


## USER & APM LVDC AUX SUPPLY

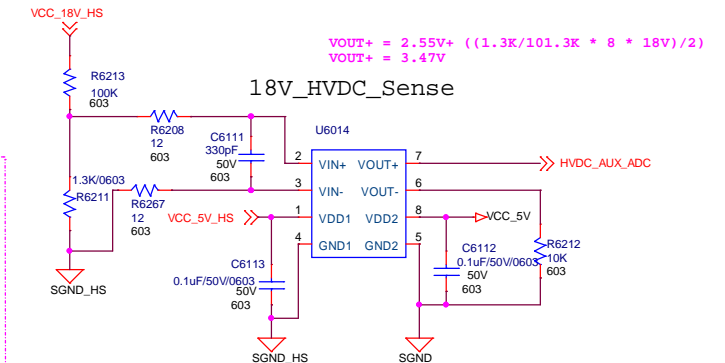
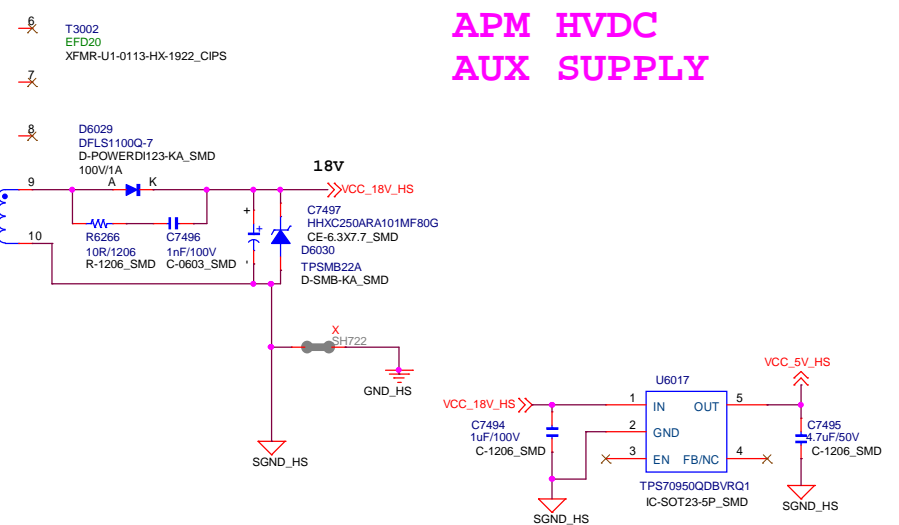


## Flyback for APM

Constant  $F_s=60\text{kHz}$



## APM HVDC AUX SUPPLY



## 18V\_HVDC\_Sense

$$V_{OUT+} = 2.55V + ((1.3K/101.3K * 8 * 18V)/2)$$

$$V_{OUT+} = 3.47V$$

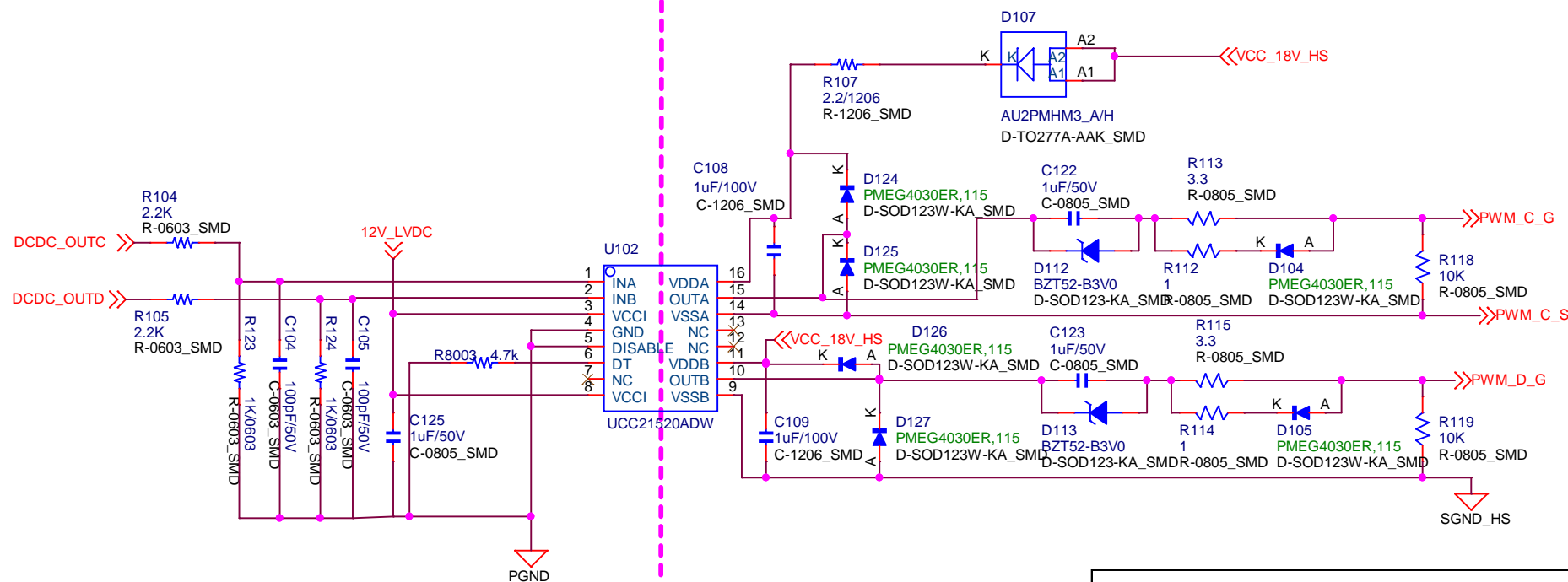
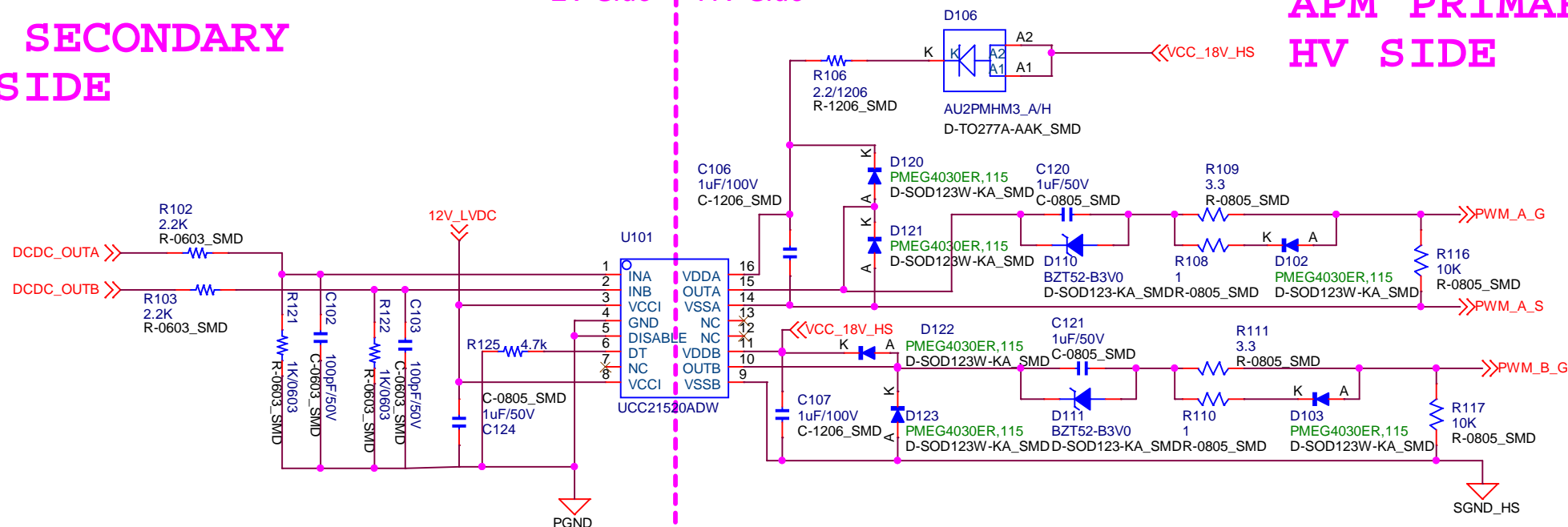
MB		
Title	23 APM Aux Supply	
Size	Document Number	Rev A2
Date	Tuesday, December 13, 2022	Sheet 24 of 53



# APM SECONDARY LV SIDE

LV Side HV Side

# APM PRIMARY HV SIDE

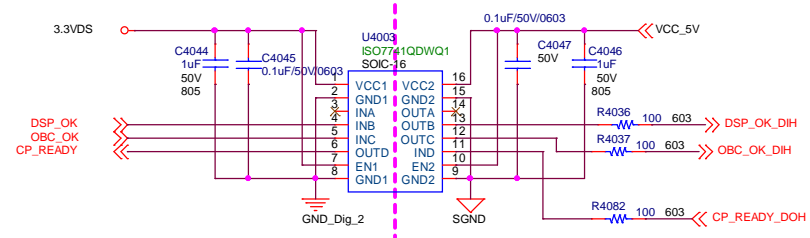


MB		
Title		
24 APM HV MOSFET Driver		
Size	Document Number	Rev
		A2
Date:	Tuesday, December 13, 2022	Sheet 25 of 53

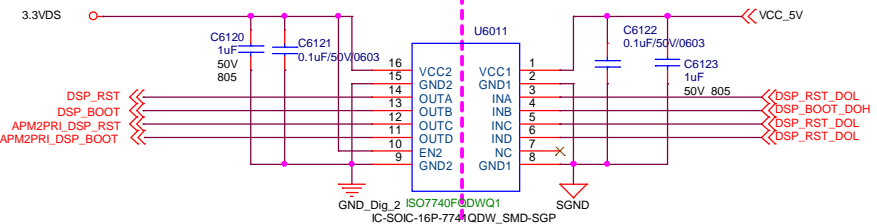
# OBC HVDC SIDE

# APM SECONDARY LV SIDE

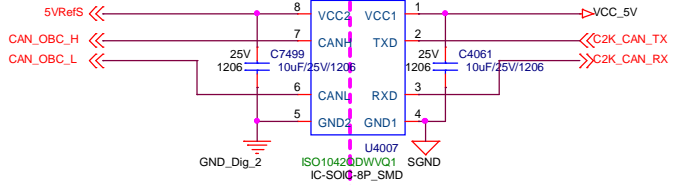
## OBC--LVDC



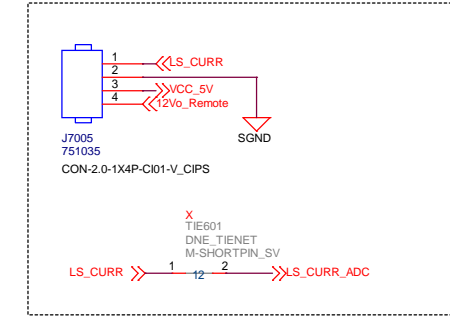
## OBC--LVDC



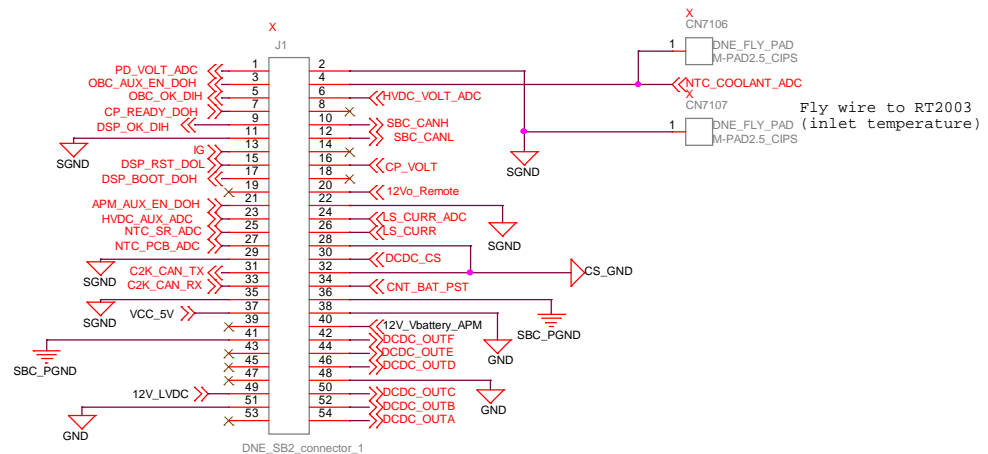
## DSP debug COMM



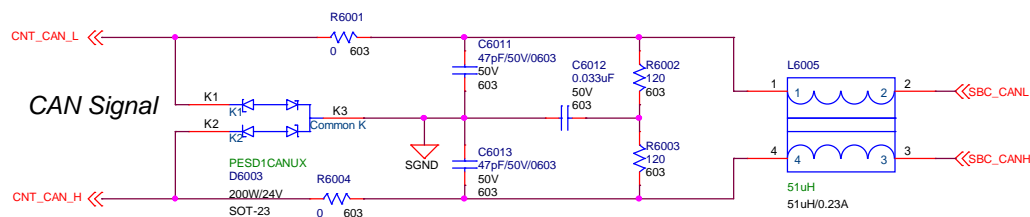
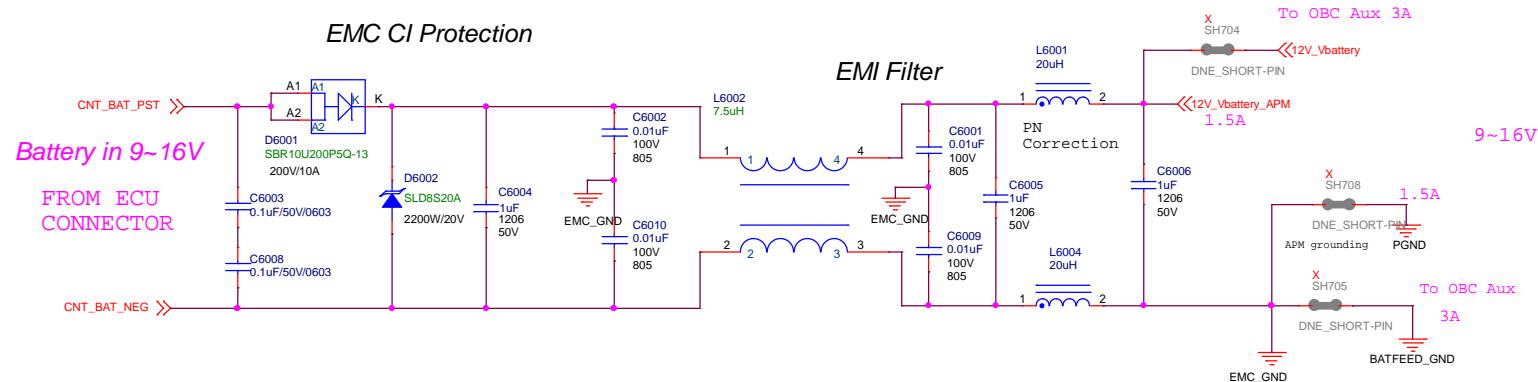
# LV Current sensor connector



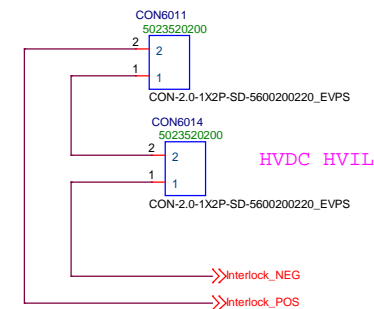
# TO APM SUB BOARD



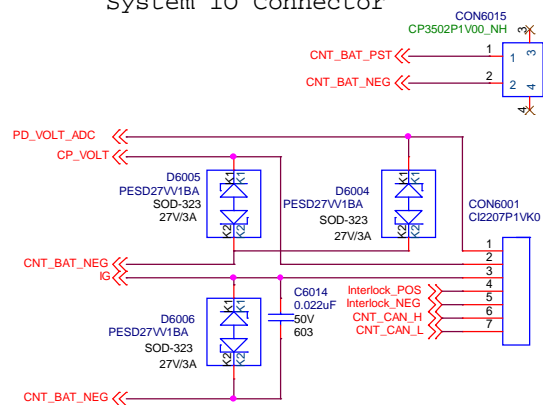
# APM SECONDARY LV SIDE



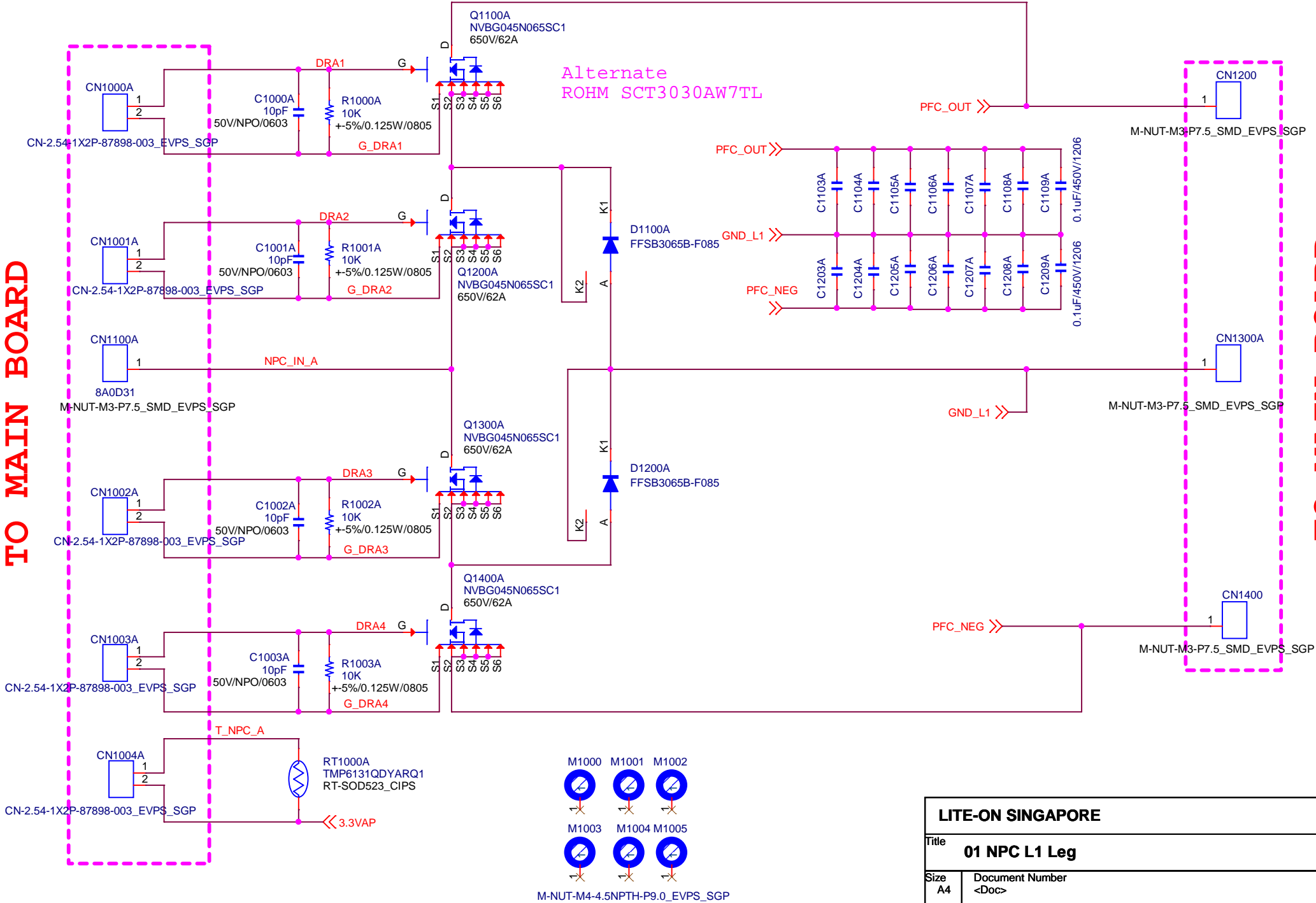
## HVIL Connectors



## System IO Connector



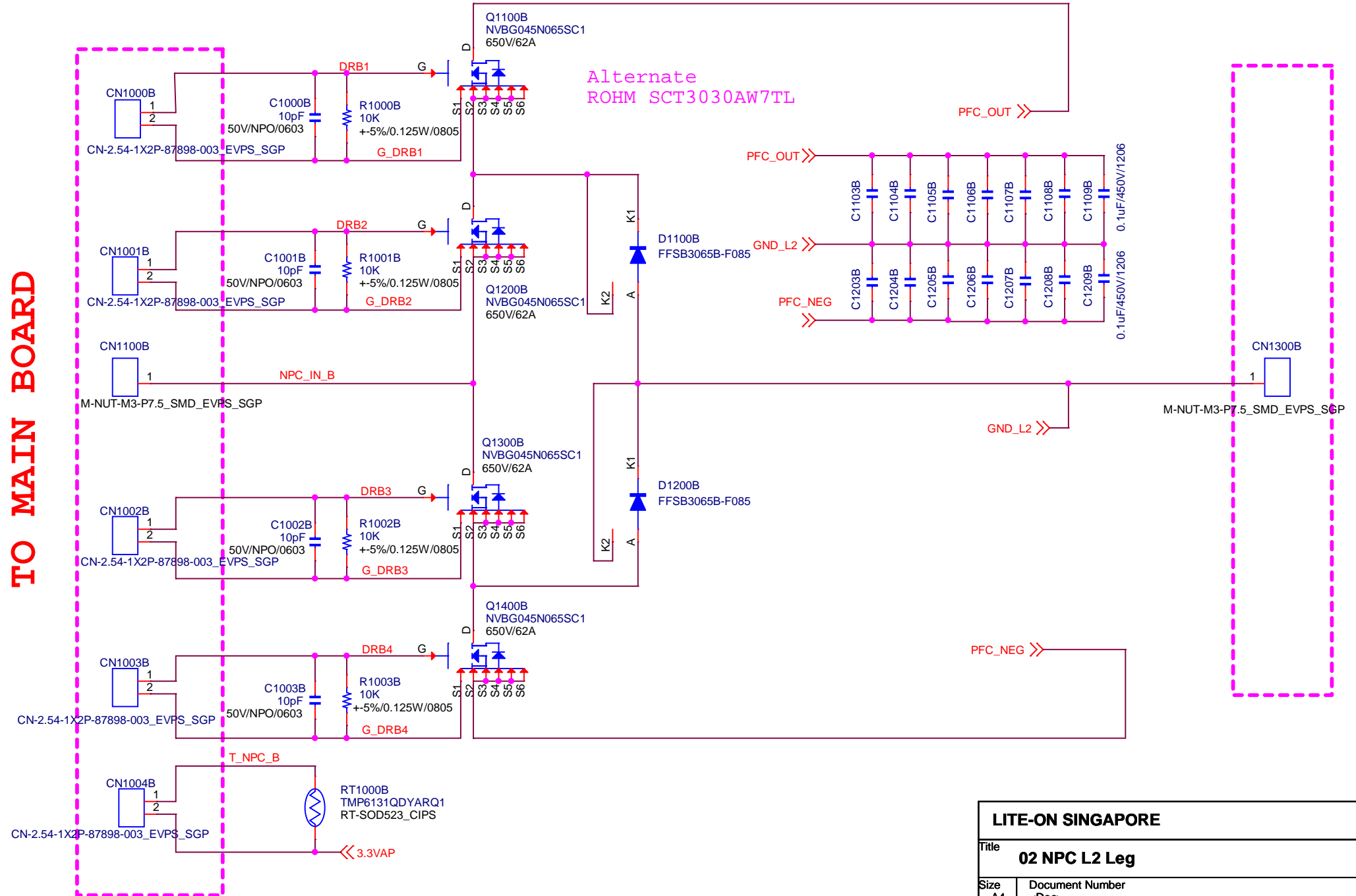
NPC LEG BOARD (11kW OBC) L1



LITE-ON SINGAPORE			S1
Title			01 NPC L1 Leg
Size	Document Number		Rev
A4	<Doc>		A2
Date:	Tuesday, December 13, 2022	Sheet	28 of 53

# NPC LEG BOARD (11kW OBC) L2

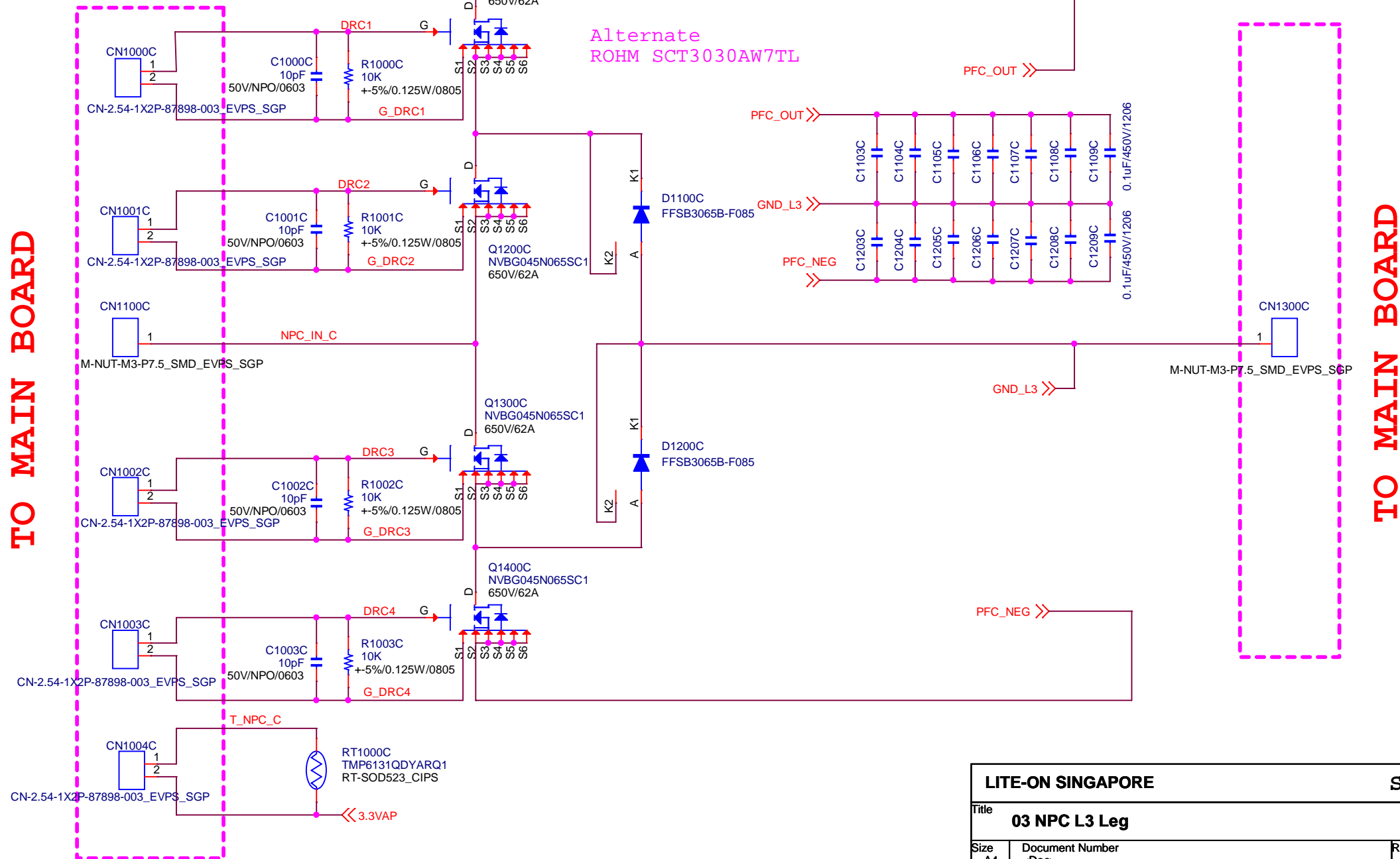
TO MAIN BOARD



TO MAIN BOARD

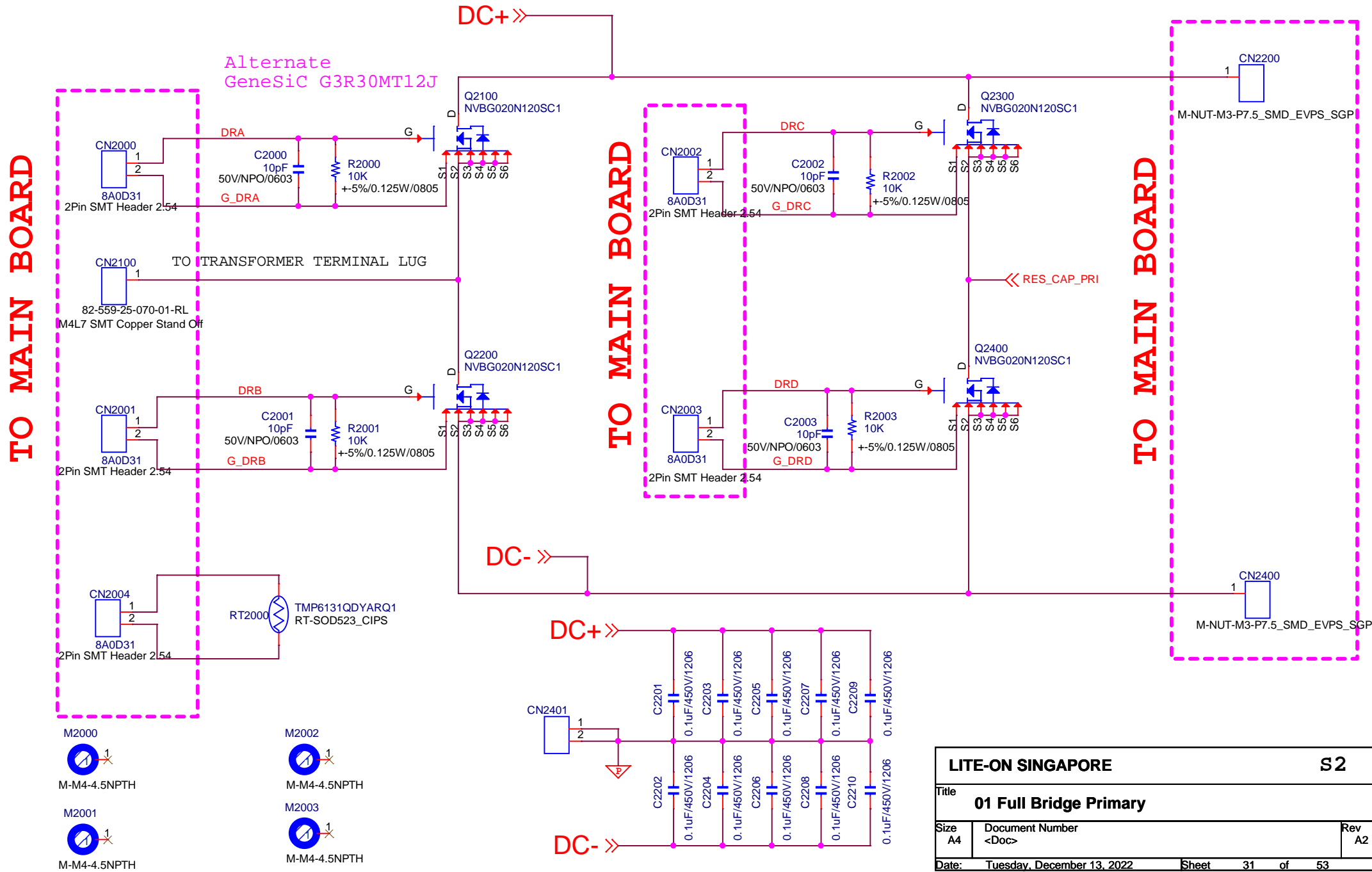
LITE-ON SINGAPORE			S1
Title 02 NPC L2 Leg			
Size A4	Document Number <Doc>		Rev A2
Date:	Tuesday, December 13, 2022	Sheet 29 of 53	

# NPC LEG BOARD (11kW OBC) L3



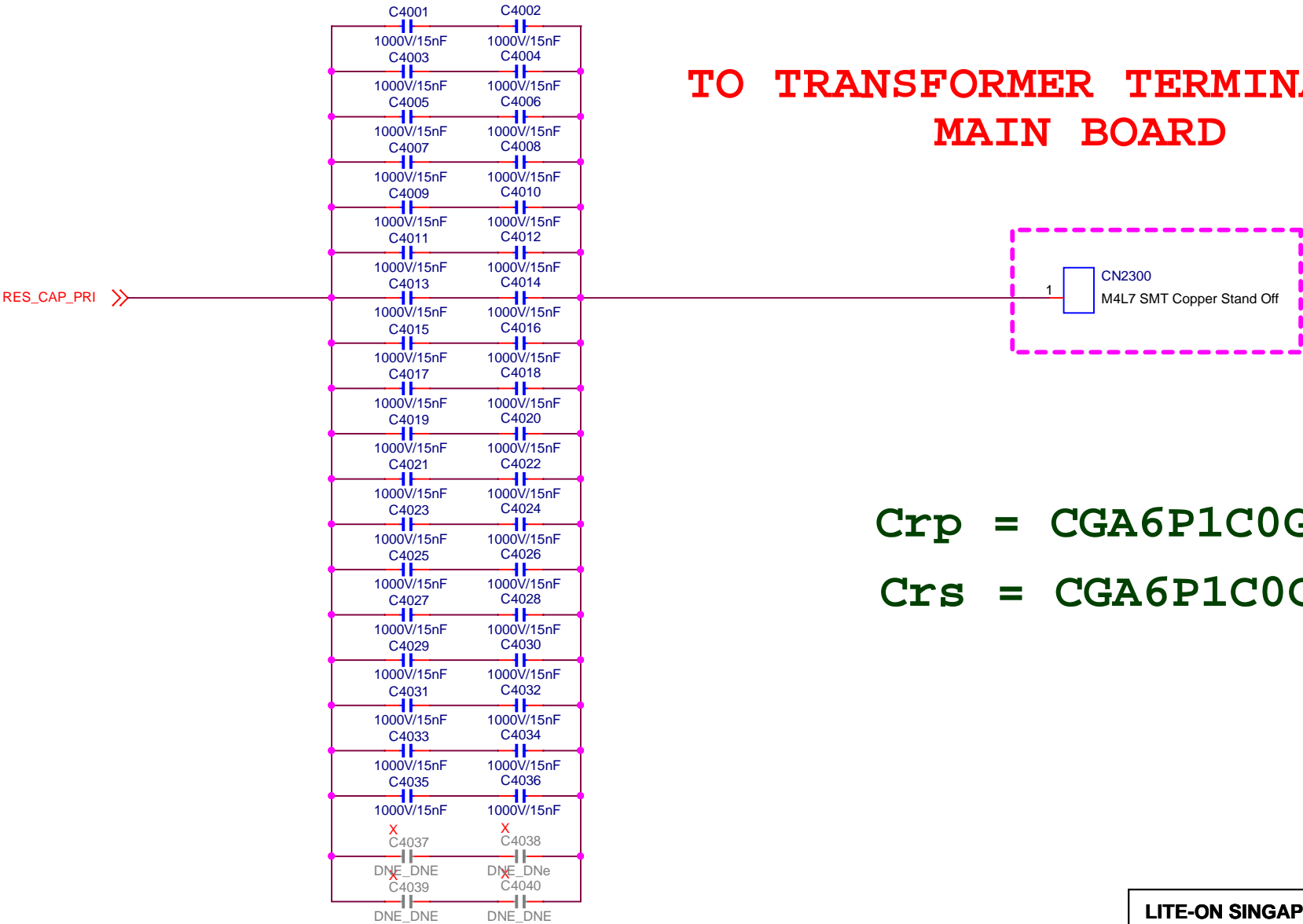
LITE-ON SINGAPORE			S1
Title 03 NPC L3 Leg			
Size A4	Document Number <Doc>		Rev A2
Date:	Tuesday, December 13, 2022	Sheet 30 of 53	

## FULL BRIDGE BOARD (11kW OBC)



Crp = 135nF = (15nF/2)\*18 - 36pc - 2 series, 18 parallel  
Crs = 220nF = (22nF/2)\*20 - 40pc - 2 series, 20 parallel

TO TRANSFORMER TERMINAL LUG  
MAIN BOARD



Crp = CGA6P1C0G3A153J250AC  
Crs = CGA6P1C0G3A223J250AC

LITE-ON SINGAPORE			S2
Title 02 Res Caps Primary			
Size A4	Document Number <Doc>		Rev A2
Date:	Tuesday, December 13, 2022	Sheet	32 of 53



# BULK CAPACITOR BOARD (11kW OBC)

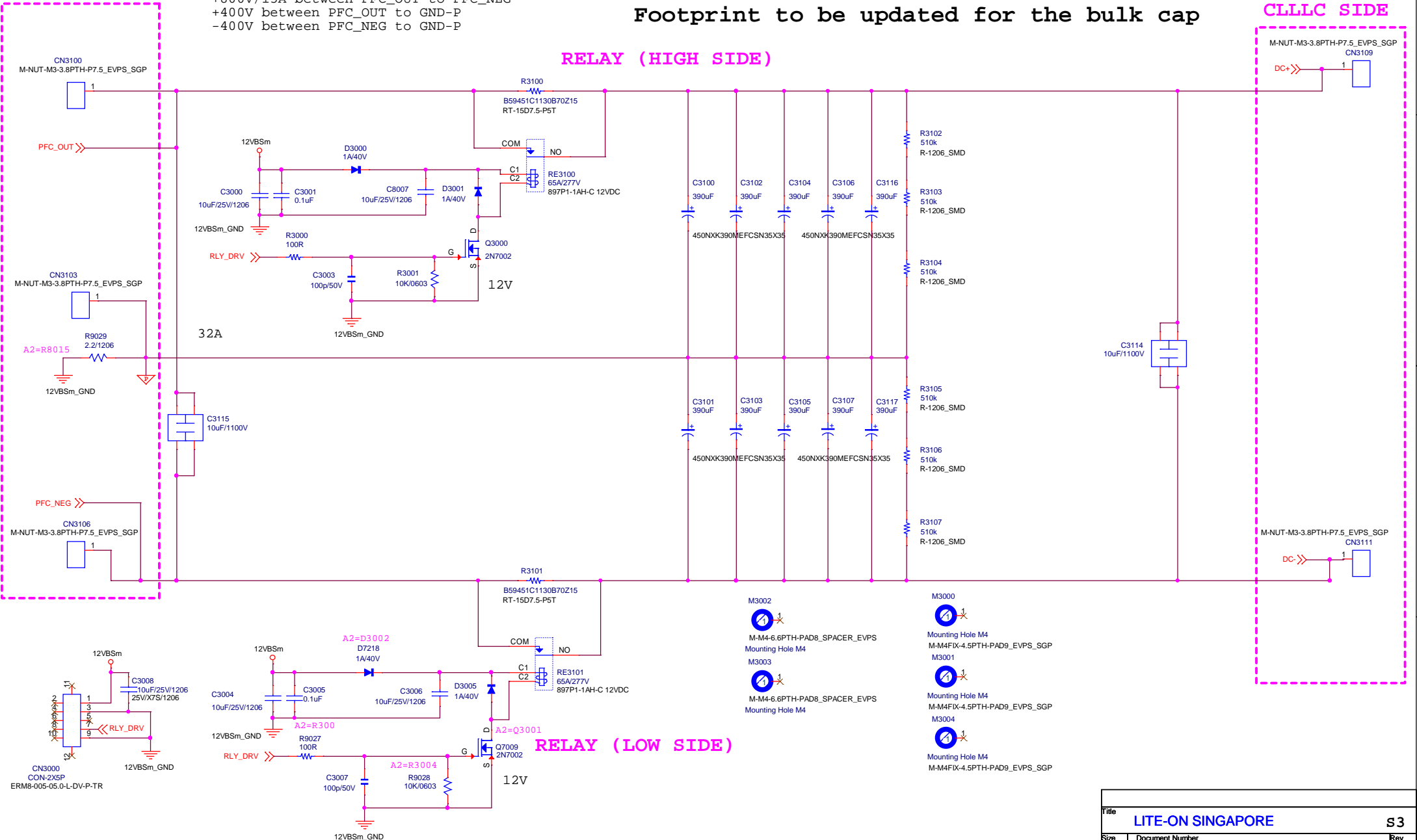
TO MAIN BOARD  
NPC SIDE

DC link:  
+800V/15A between PFC\_OUT to PFC\_NEG  
+400V between PFC\_OUT to GND-P  
-400V between PFC\_NEG to GND-P

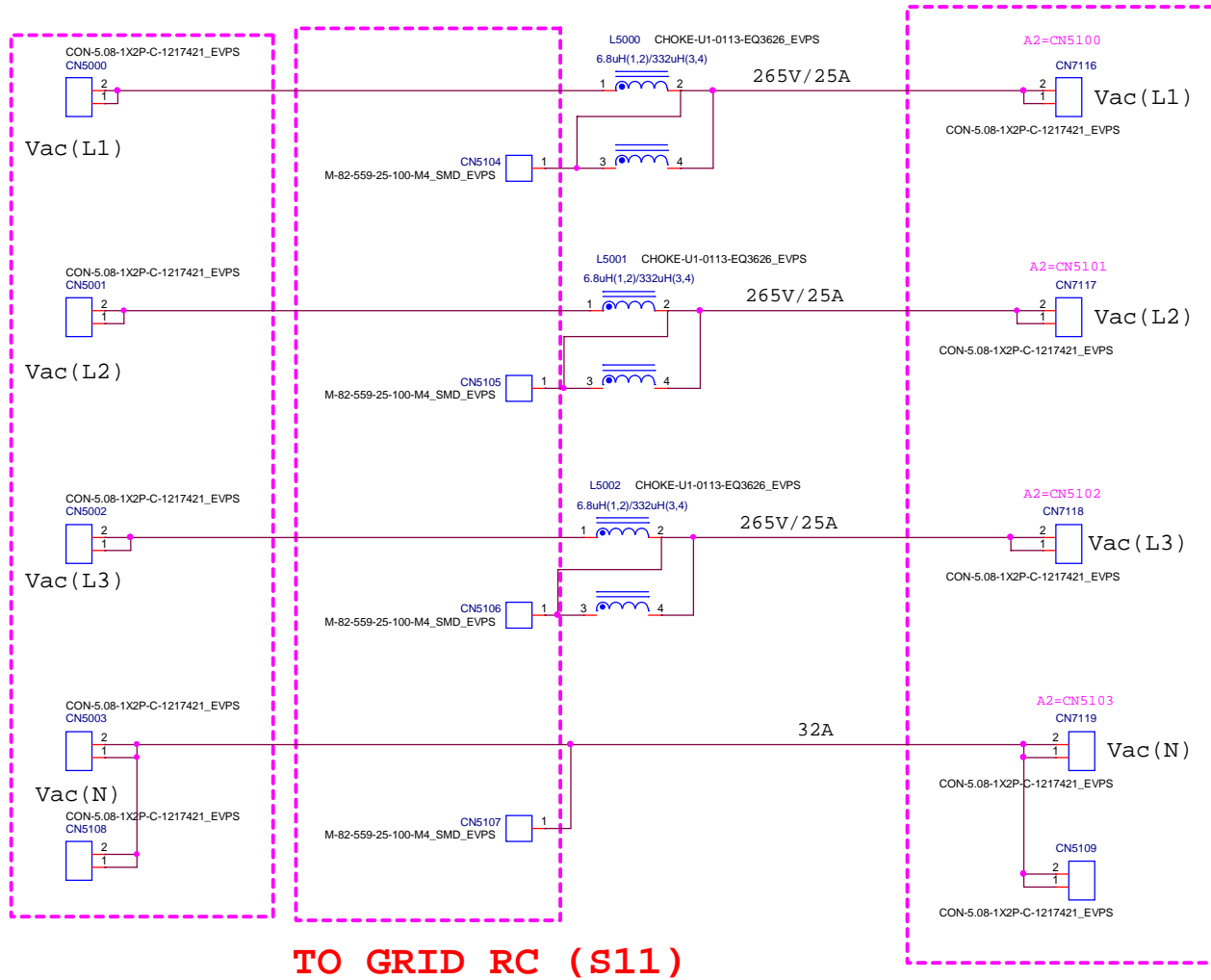
Note:  
Footprint to be updated for the bulk cap

TO MAIN BOARD  
CLLLC SIDE

RELAY (HIGH SIDE)



TO AC EMI SIDE (MB)



TO NPC SIDE (MB)



Mounting Hole M4  
M-M4-6.6PTH-PAD8\_SPACER\_EVPS



Mounting Hole M4  
M-M4-6.6PTH-PAD8\_SPACER\_EVPS



Mounting Hole M4  
M-M4-SCREW\_CIPS



Mounting Hole M4  
M-M4-SCREW\_CIPS



Mounting Hole M4  
M-M4-SCREW\_CIPS



Mounting Hole M4  
M-M4-SCREW\_CIPS



Mounting Hole M4  
M-82-559-25-100-M4\_SMD\_EVPS



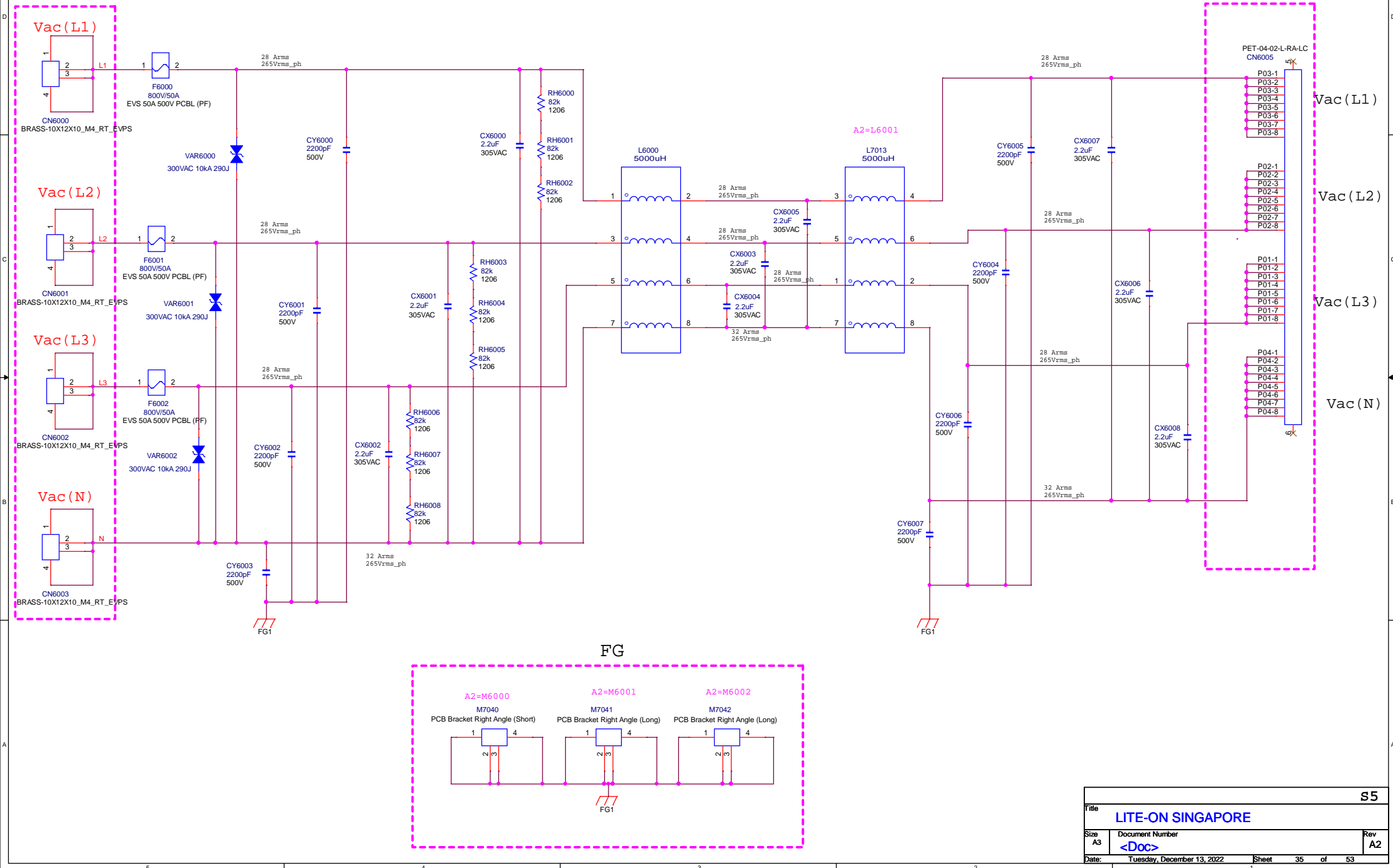
Mounting Hole M4  
M-82-559-25-100-M4\_SMD\_EVPS

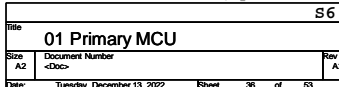
LITE-ON SINGAPORE			S4
Title 01 PFC Choke			
Size A3	Document Number <Doc>		Rev A2
Date:	Tuesday, December 13, 2022	Sheet	34 of 53

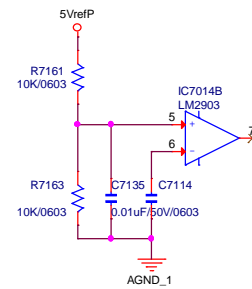
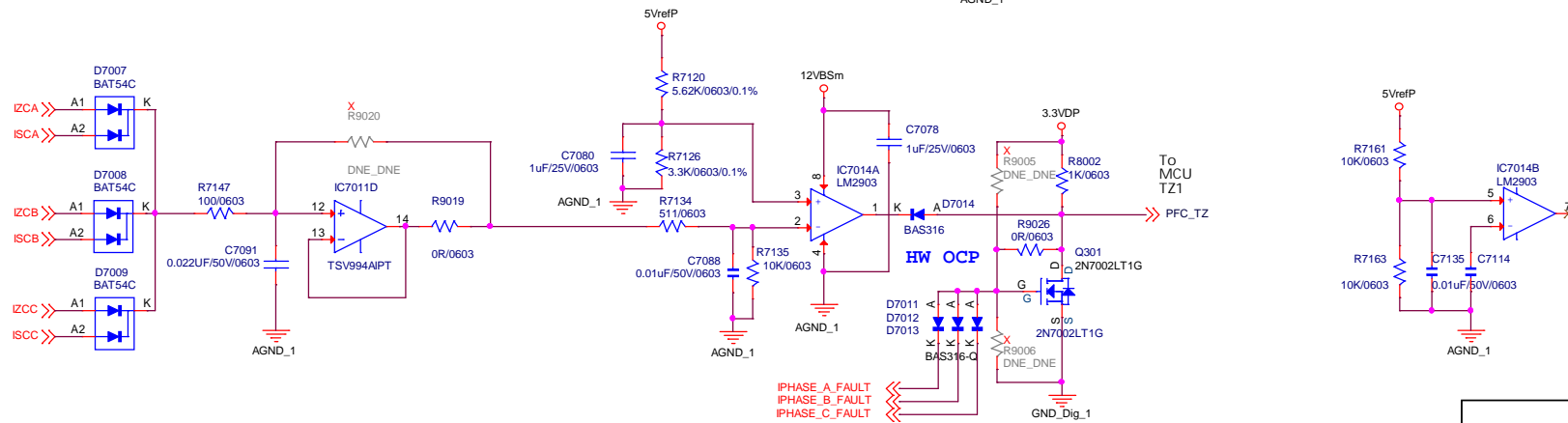
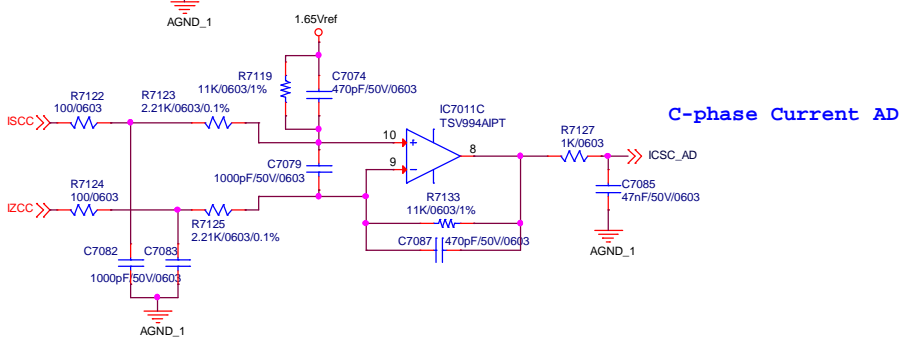
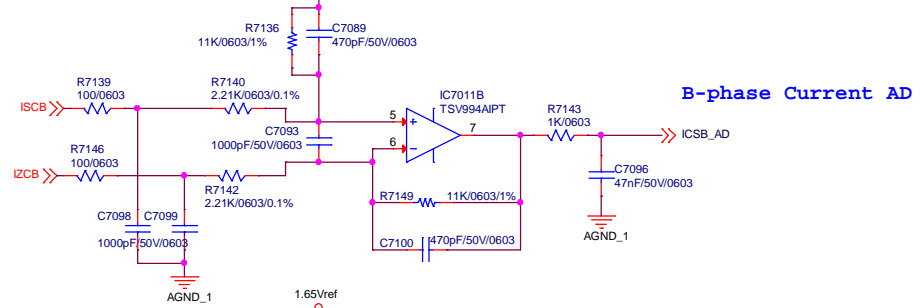
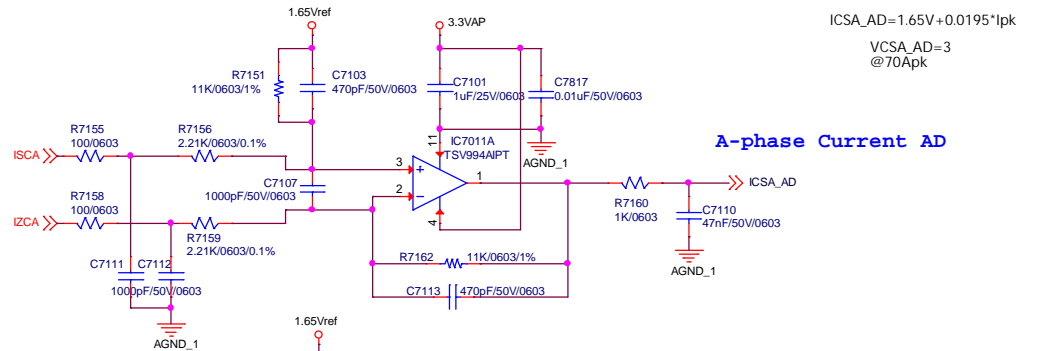
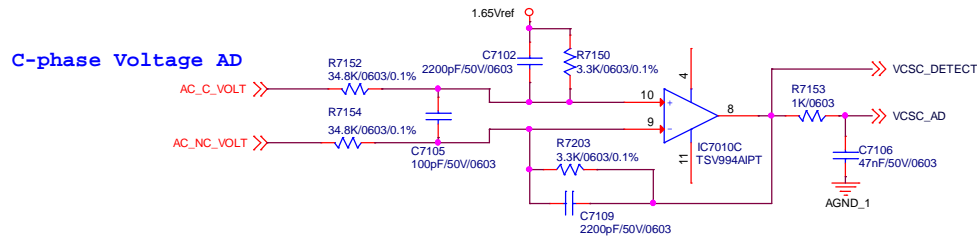
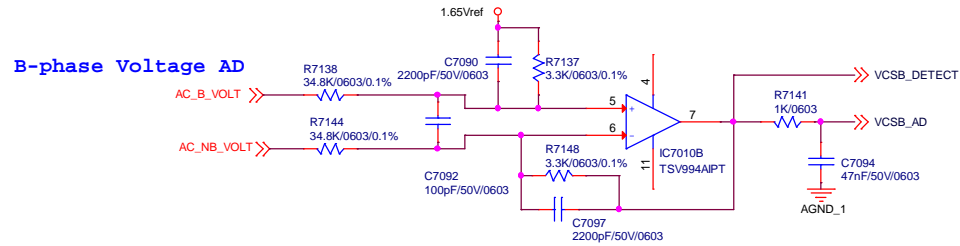
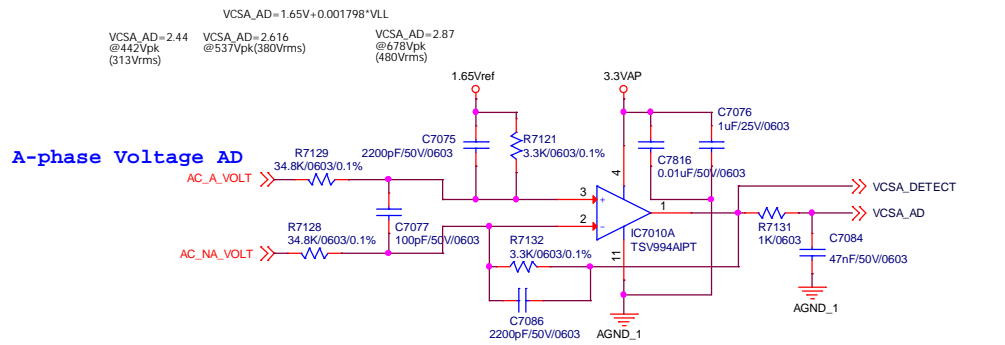
# AC EMI BOARD (11kW OBC)

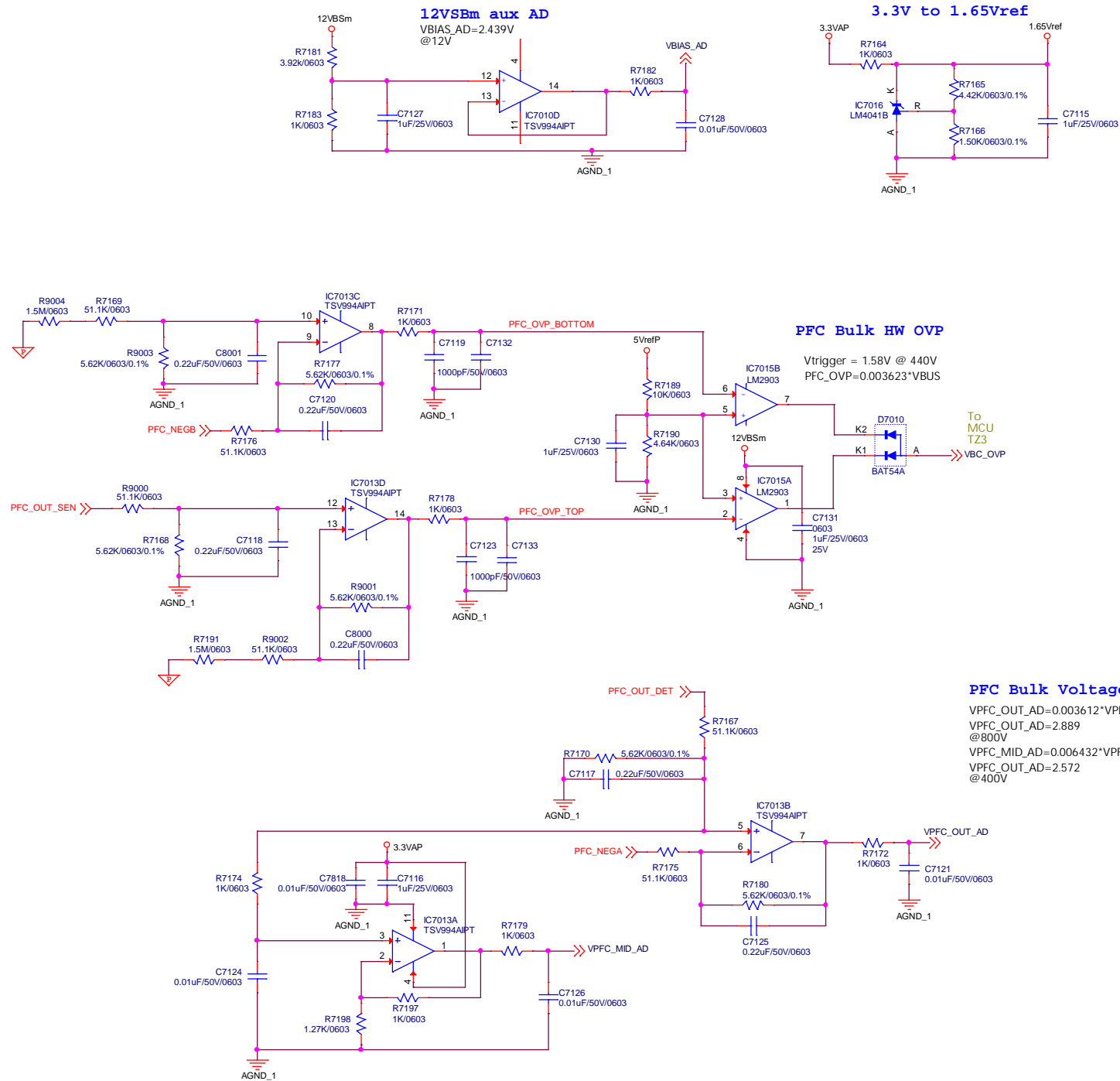
TO PFC LCL BOARD

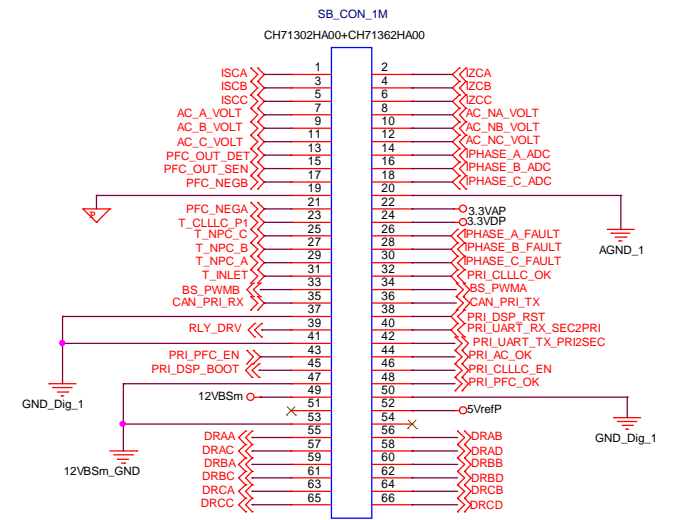
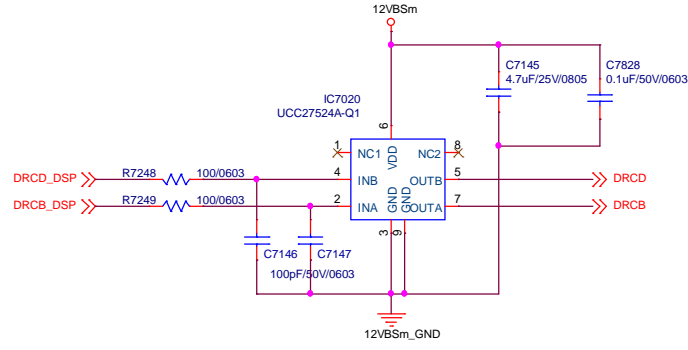
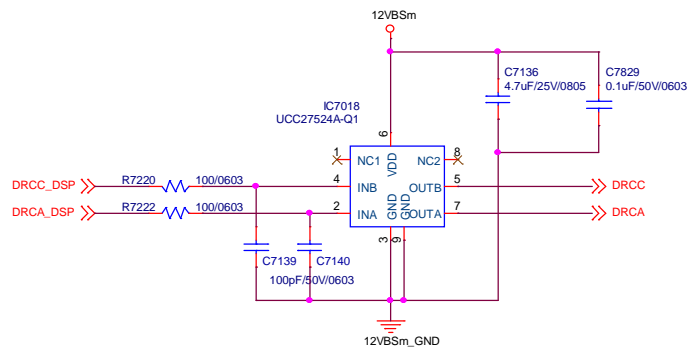
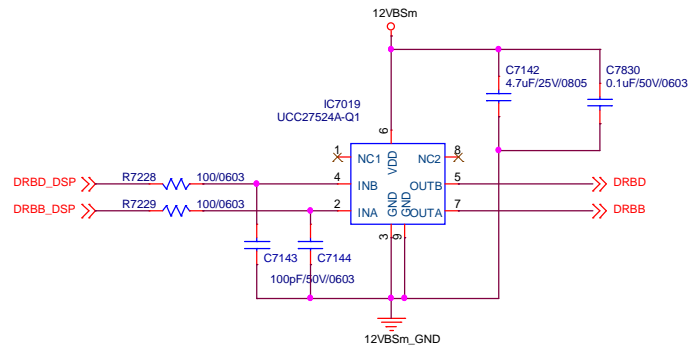
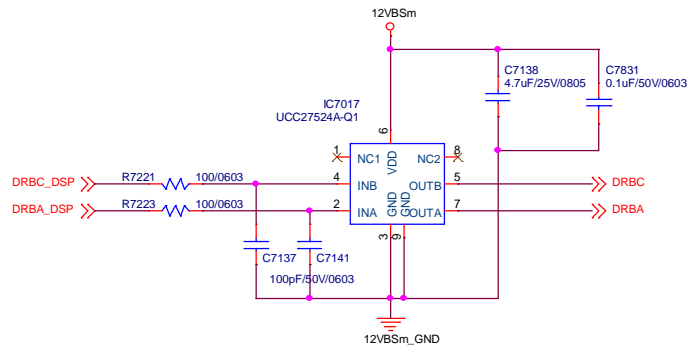
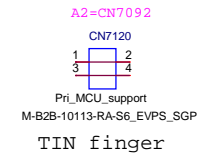
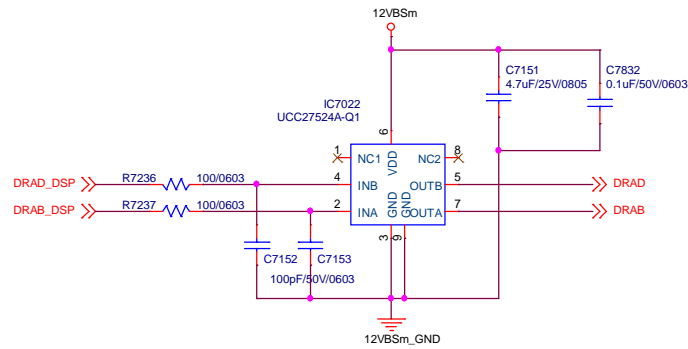
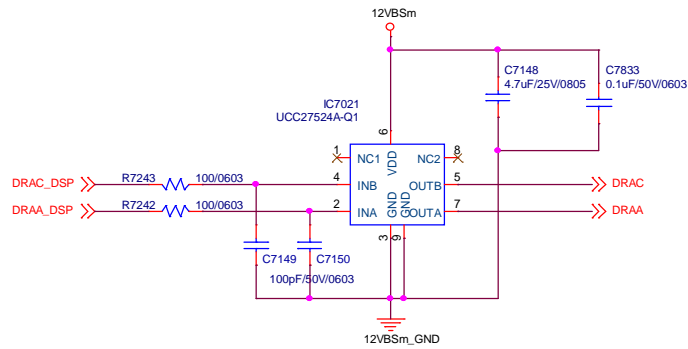
Vac input from external  
3 phase 5 wire









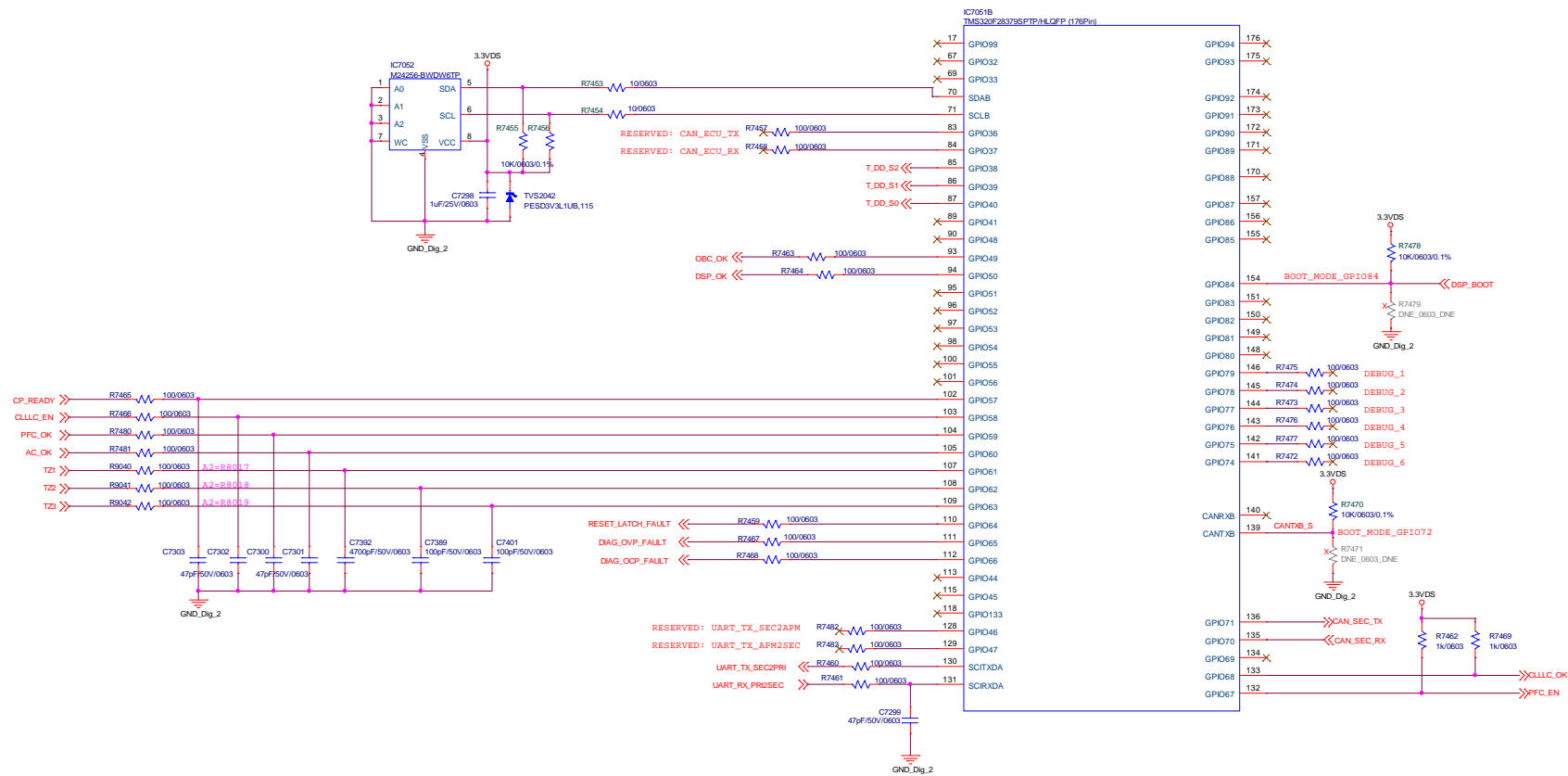




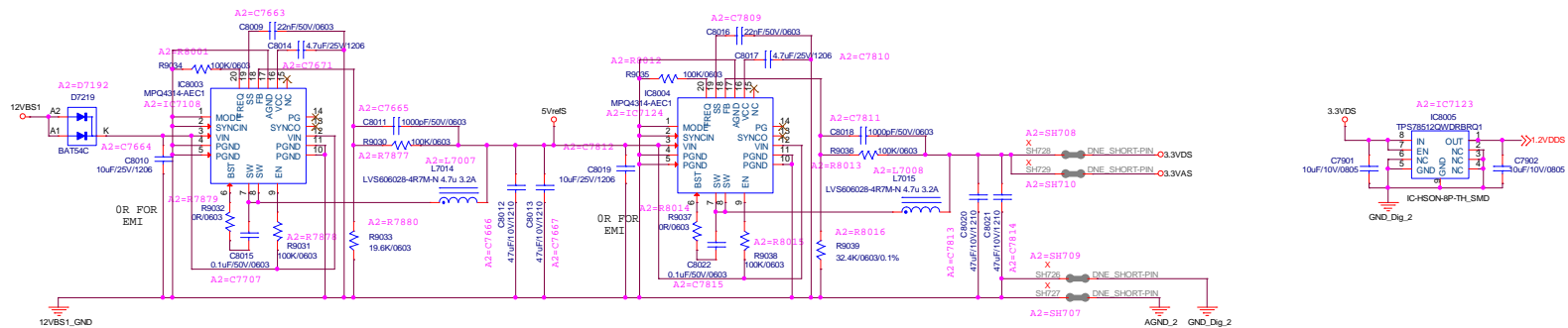


# SECONDARY MCU

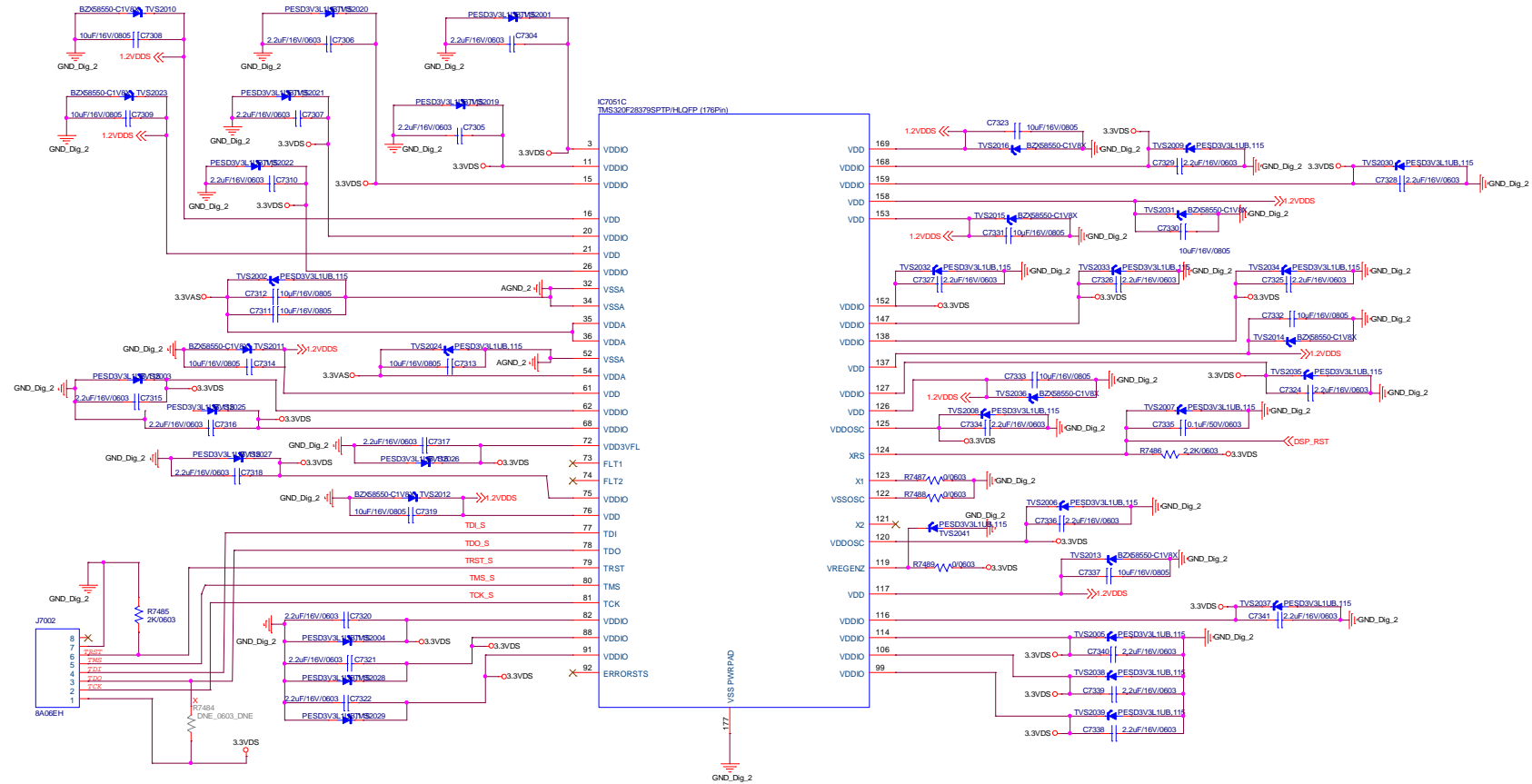
## TMS320F28379S/LQFP176\_b



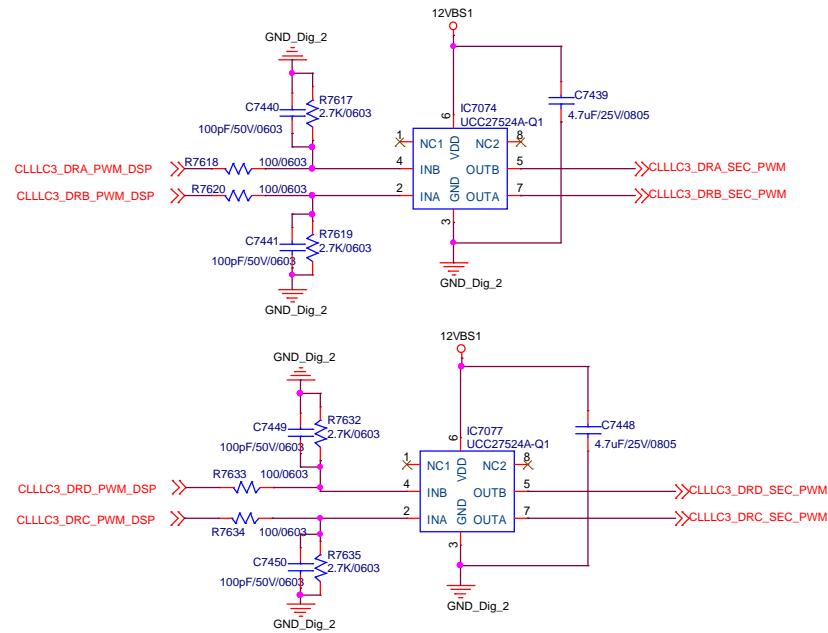
## SECONDARY BIAS SUPPLY



TMS320F28379S/LQFP176\_c



# CLLLC\_A SEC PWM driver



A2=CN7092

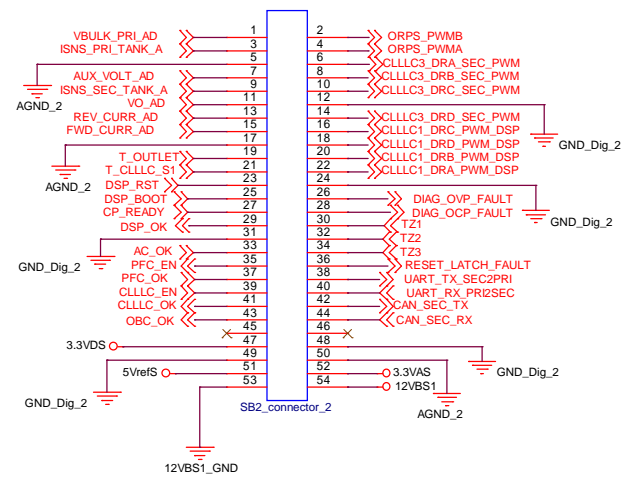
TIN finger

CN7121



Pri\_MCU\_support  
M-B2B-10113-RA-S2\_EVPS

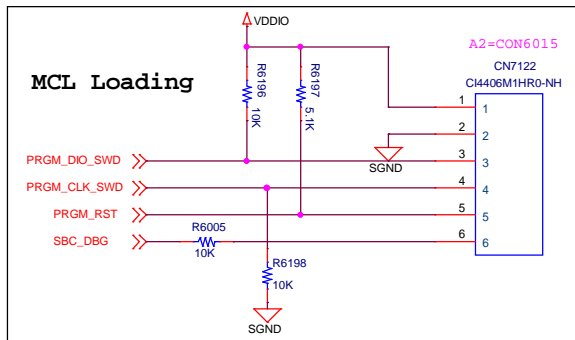
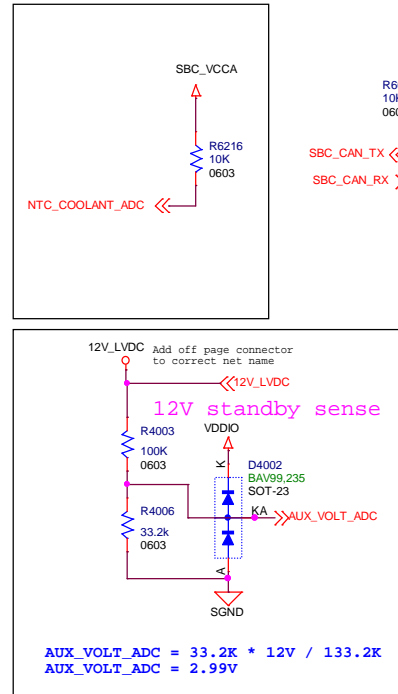
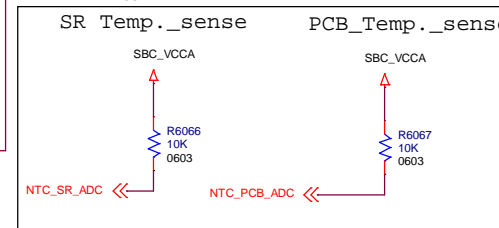
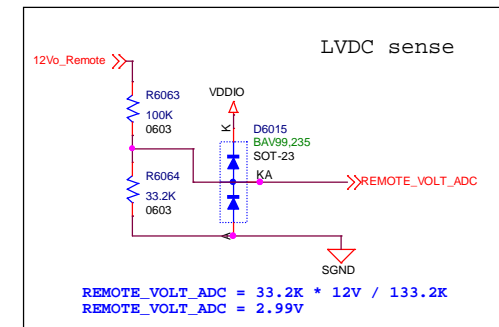
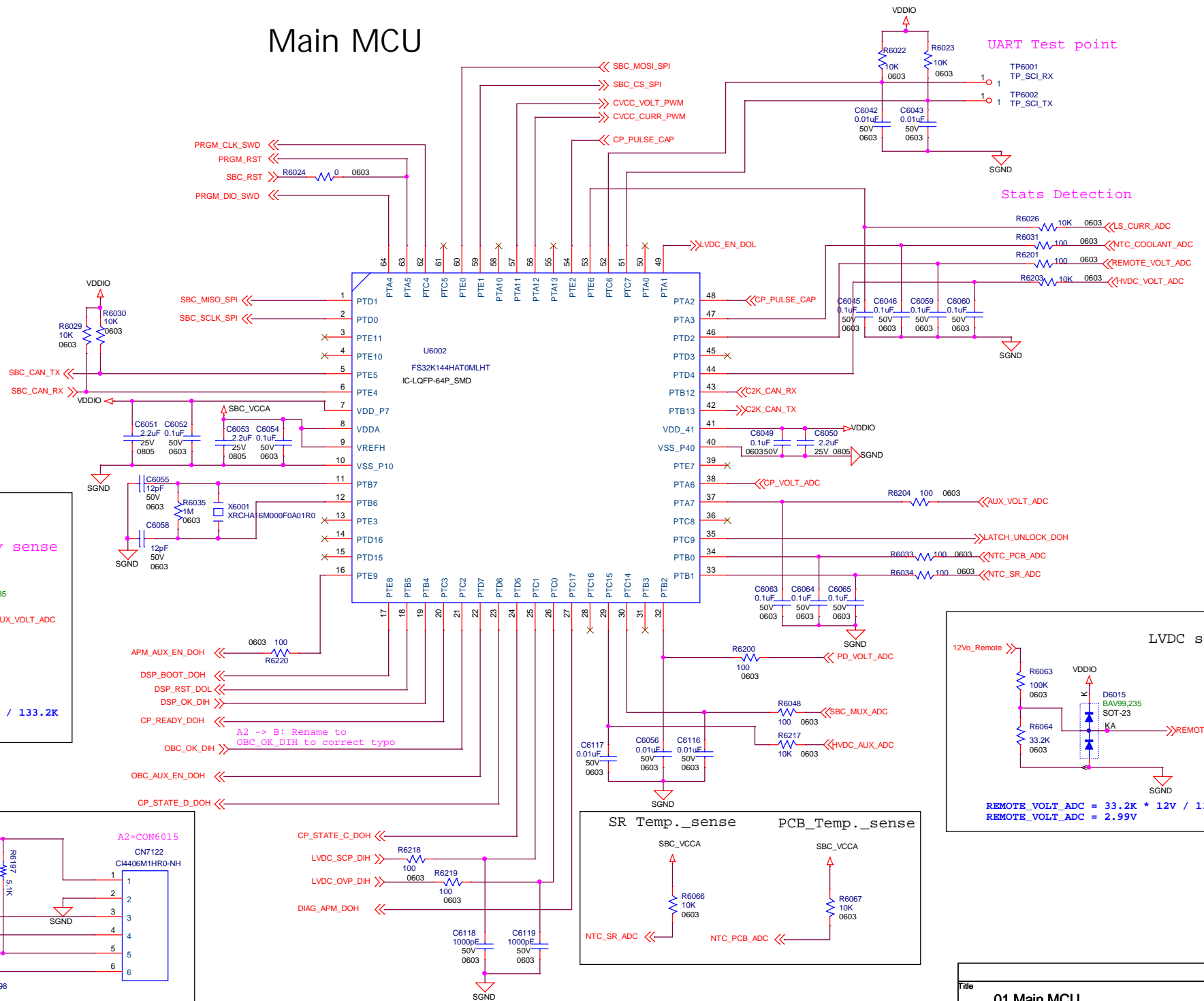
A2=SB\_CON\_2  
SB\_CON\_2A



TO MB

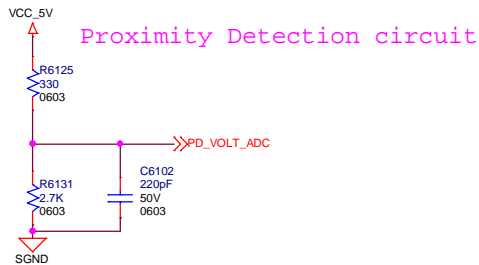
S7		
Title	04 Secondary_12V_DRIVER	
Size	Document Number	Rev
A3		A2
Date:	Tuesday, December 13, 2022	Sheet 43 of 53

## Main MCU

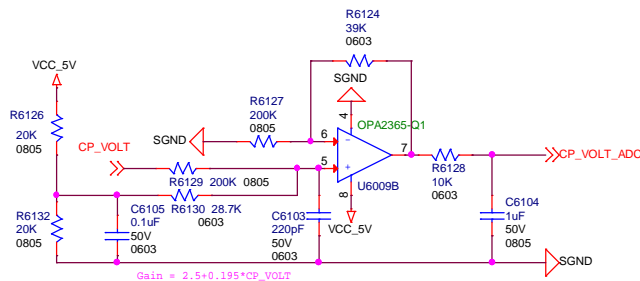




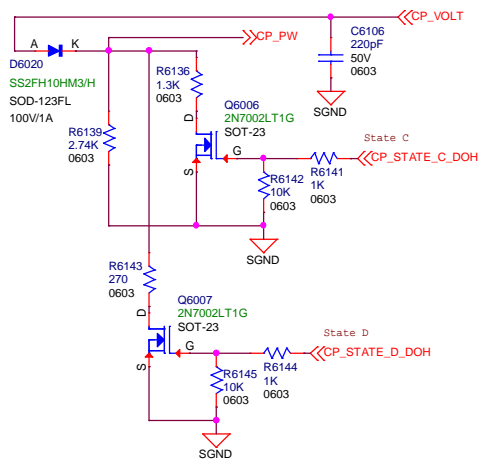




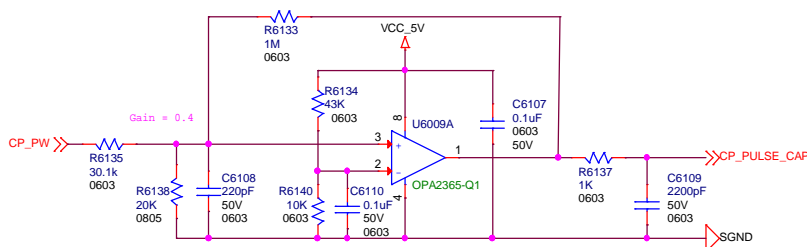
## Control Pilot Peak Voltage Measurement



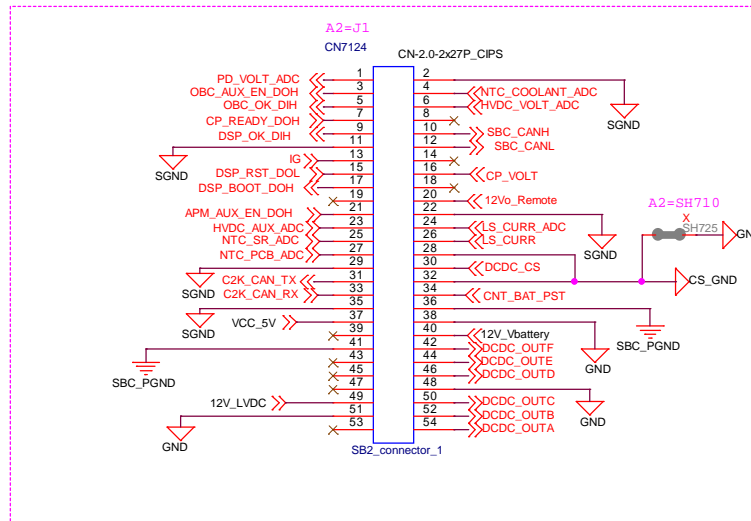
## Control pilot circuit



## Control Pilot Frequency Measurement



## TO APM MAIN BOARD



## TIN finger

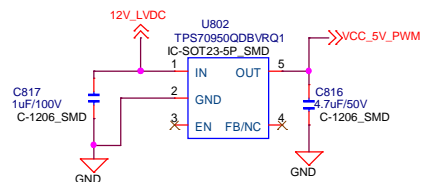
A2=CN7092

CN7123

1 2

3 4

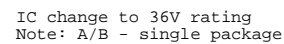
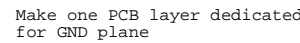
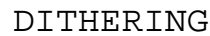
Pri\_MCU\_support  
M-B2B-10113-RA-S2\_EVPS



s8

Title				04 Control Pilot
Size	Document Number			Rev A
	<Doc>			
Date:	Tuesday, December 13, 2022	Sheet	47	of 53

Prioritize capacitors to be closed to IC pin  
Next priority are the resistors



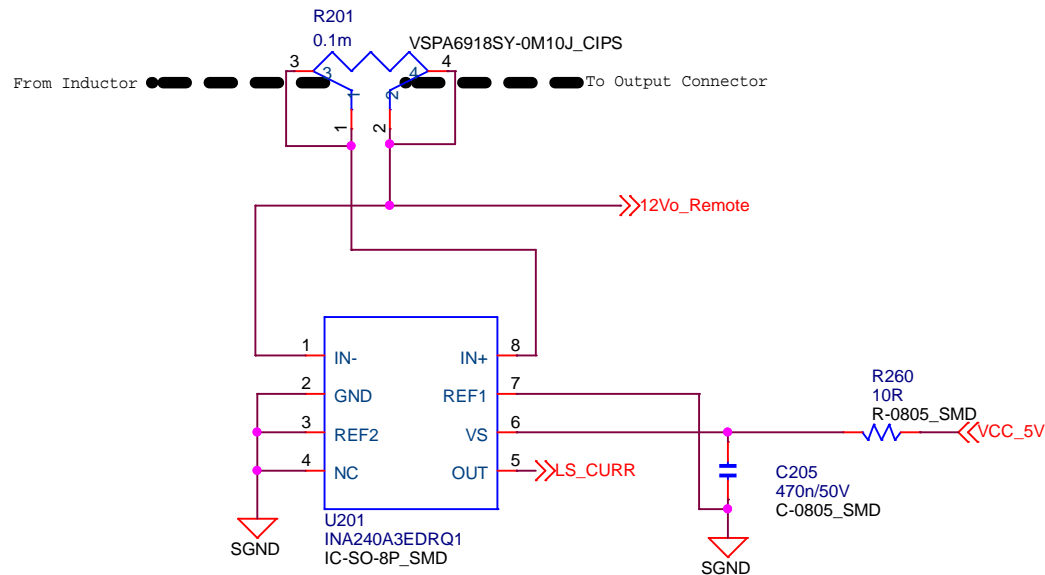


$$I_{OUT} = 3600 / 13.8$$

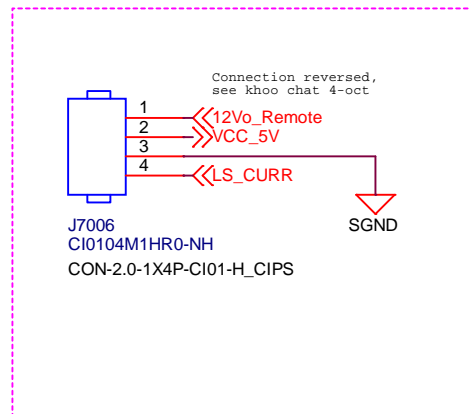
$$I_{OUT} = 260.87A$$

$$I_{OUT\_OCP} = 320A$$

$$V_{OUT} (U22) = 320 * (0.1m) * 100V/V = 3.2V$$



## LV Current sensor connector



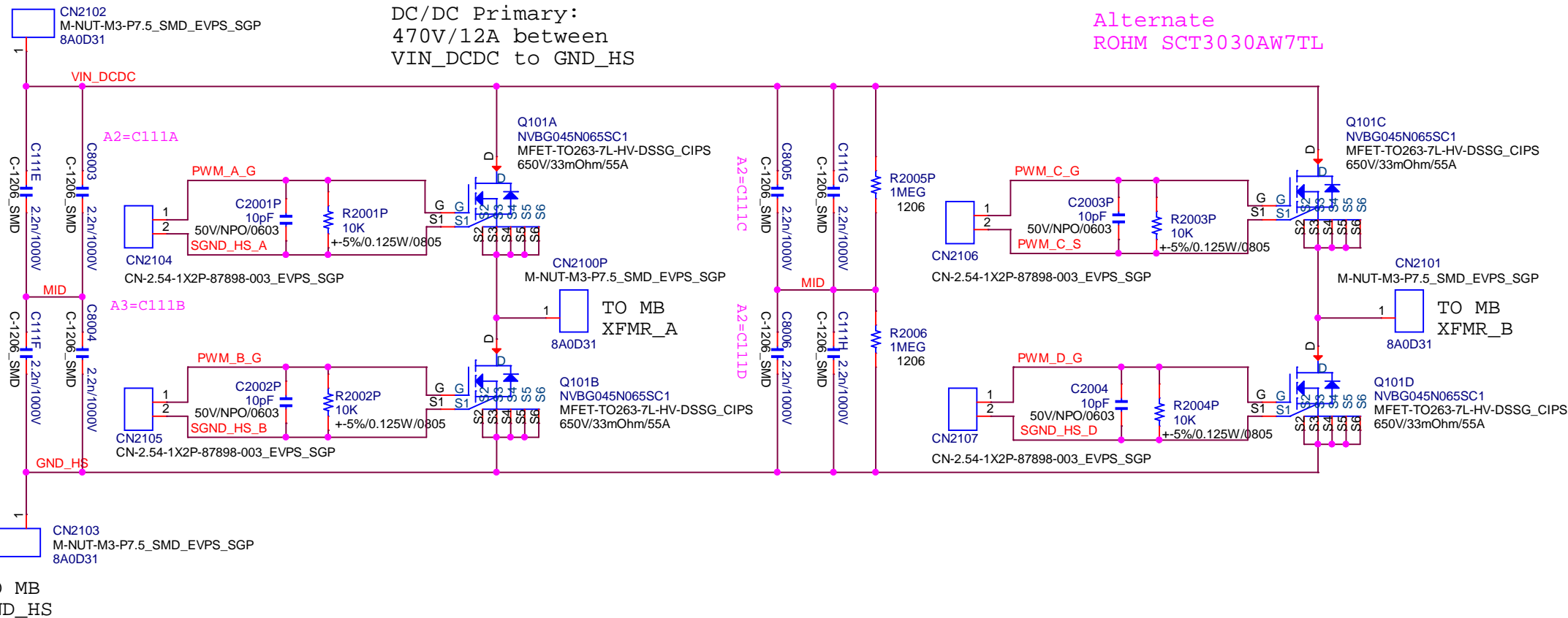
s9		
Title		
01 LV Current Sensor		
Size	Document Number	Rev
	<Doc>	A2
Date:	Tuesday, December 13, 2022	Sheet 49 of 53

# APM PSFB BOARD

TO MB  
VIN\_DCDC

DC/DC Primary:  
470V/12A between  
VIN\_DCDC to GND\_HS

Alternate  
ROHM SCT3030AW7TL



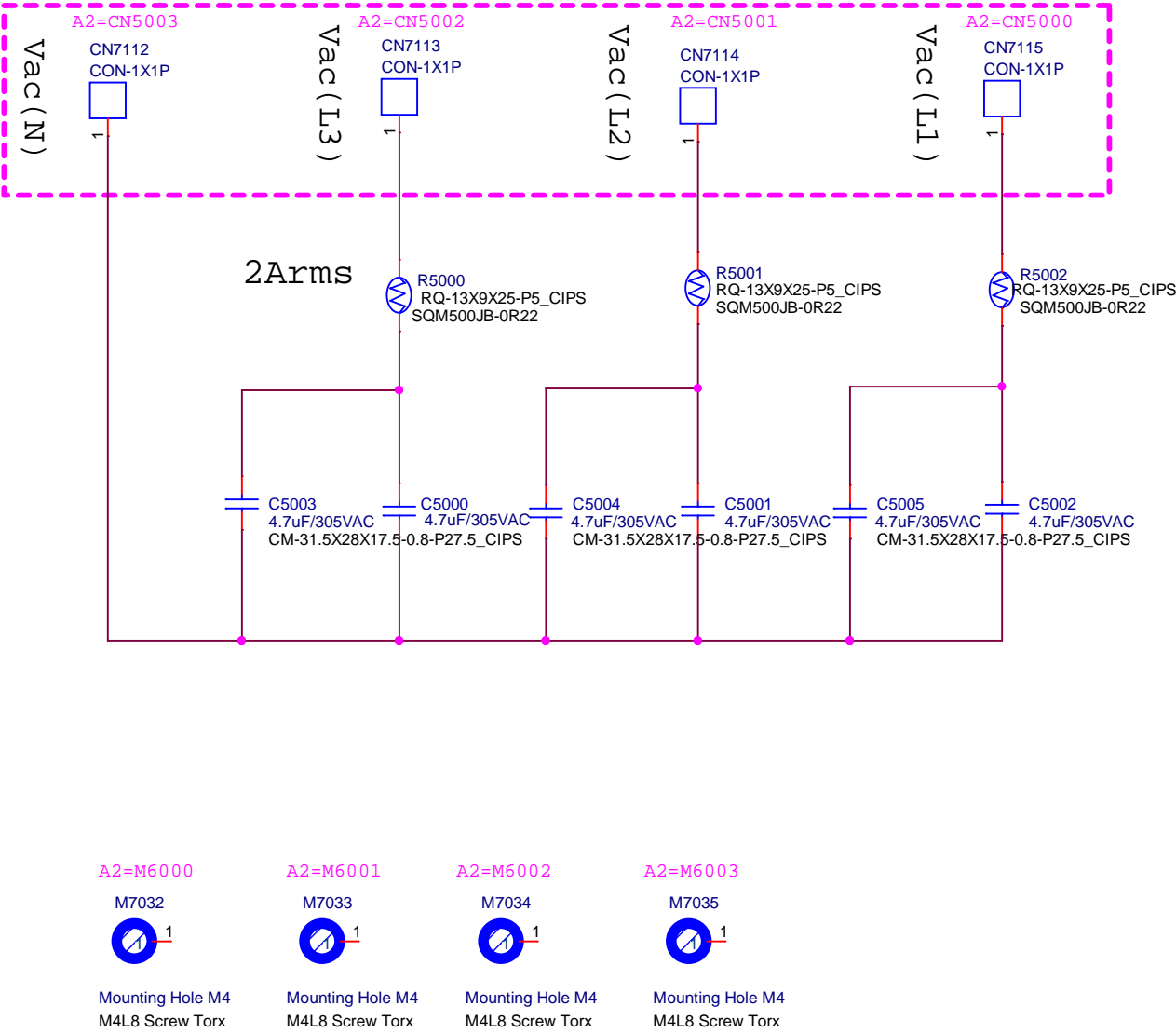
TO MB  
GND\_HS

M7028 A2=M2000  
M-NUT-M4-4.5NPTH-P9.0\_EVPS\_SGP  
M7029 A2=M2001  
M-NUT-M4-4.5NPTH-P9.0\_EVPS\_SGP

M7030 A2=M2002  
M-NUT-M4-4.5NPTH-P9.0\_EVPS\_SGP  
M7031 A2=M2003  
M-NUT-M4-4.5NPTH-P9.0\_EVPS\_SGP

S10		
Title	01 APM PSFB Primary	
Size	Document Number	Rev A2
Date:	Tuesday, December 13, 2022	Sheet 50 of 53

TO PFC CHOKE BOARD (S4)



LITE-ON SINGAPORE

Title  
01 Grid RC Filter

Size  
A4

Document Number  
<Doc>

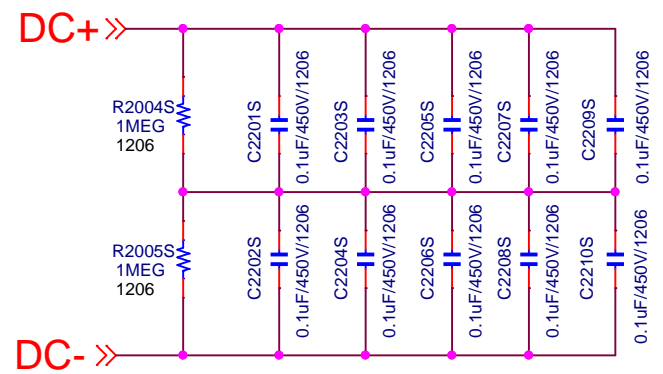
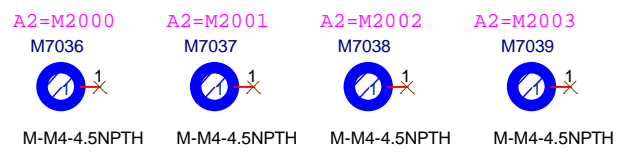
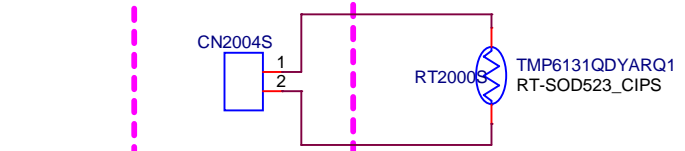
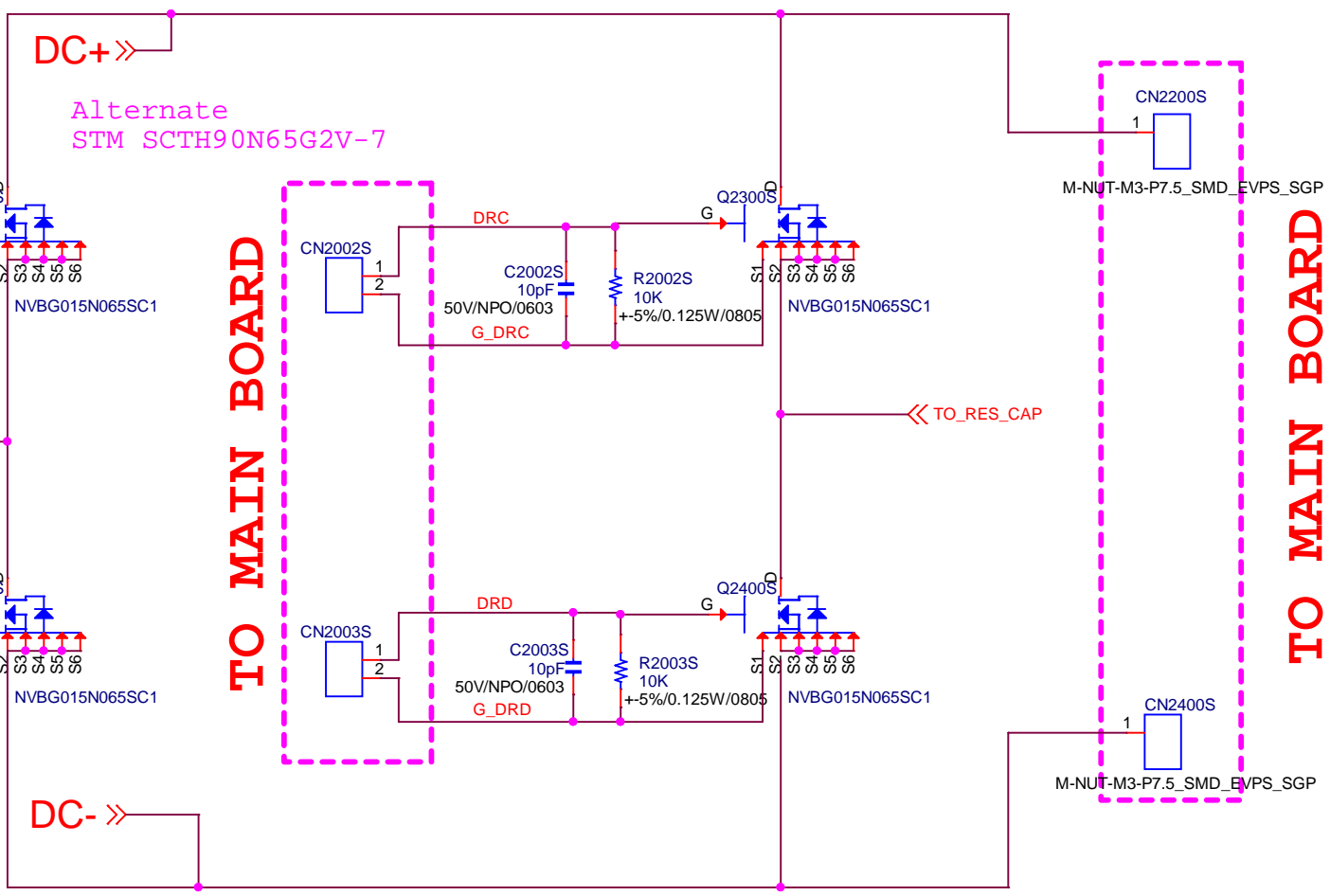
Rev  
A2

# FULL BRIDGE BOARD - SEC (11kW OBC)

TO MAIN BOARD

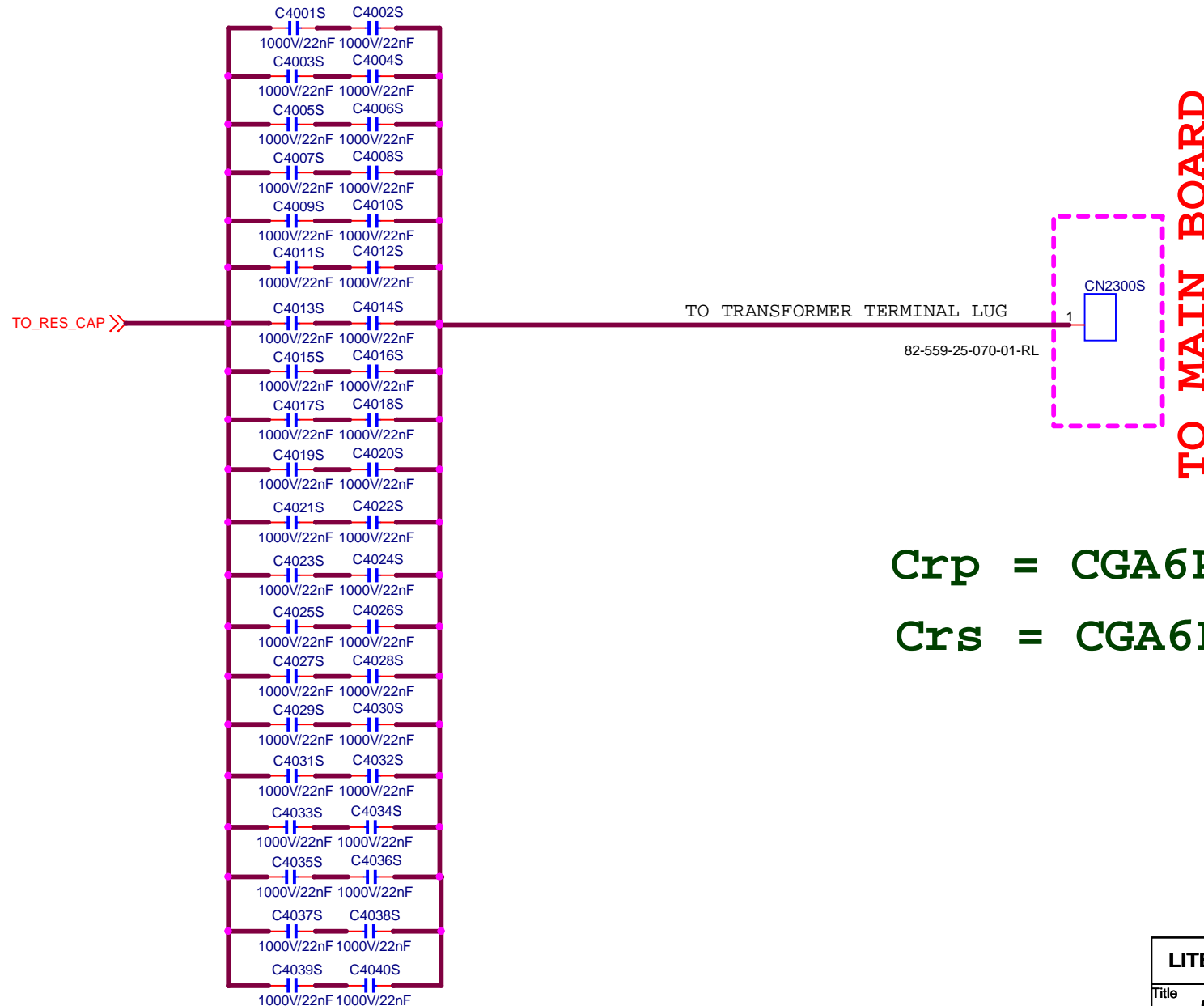
TO MAIN BOARD

TO MAIN BOARD



LITE-ON SINGAPORE			S12
Title 01 Full Bridge Secondary			
Size A4	Document Number <Doc>		Rev A2
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Crp = 135nF = (15nF/2)\*18 - 36pc - 2 series, 18 parallel  
 Crs = 220nF = (22nF/2)\*20 - 40pc - 2 series, 20 parallel



Crp = CGA6P1C0G3A153J250AC  
 Crs = CGA6P1C0G3A223J250AC

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