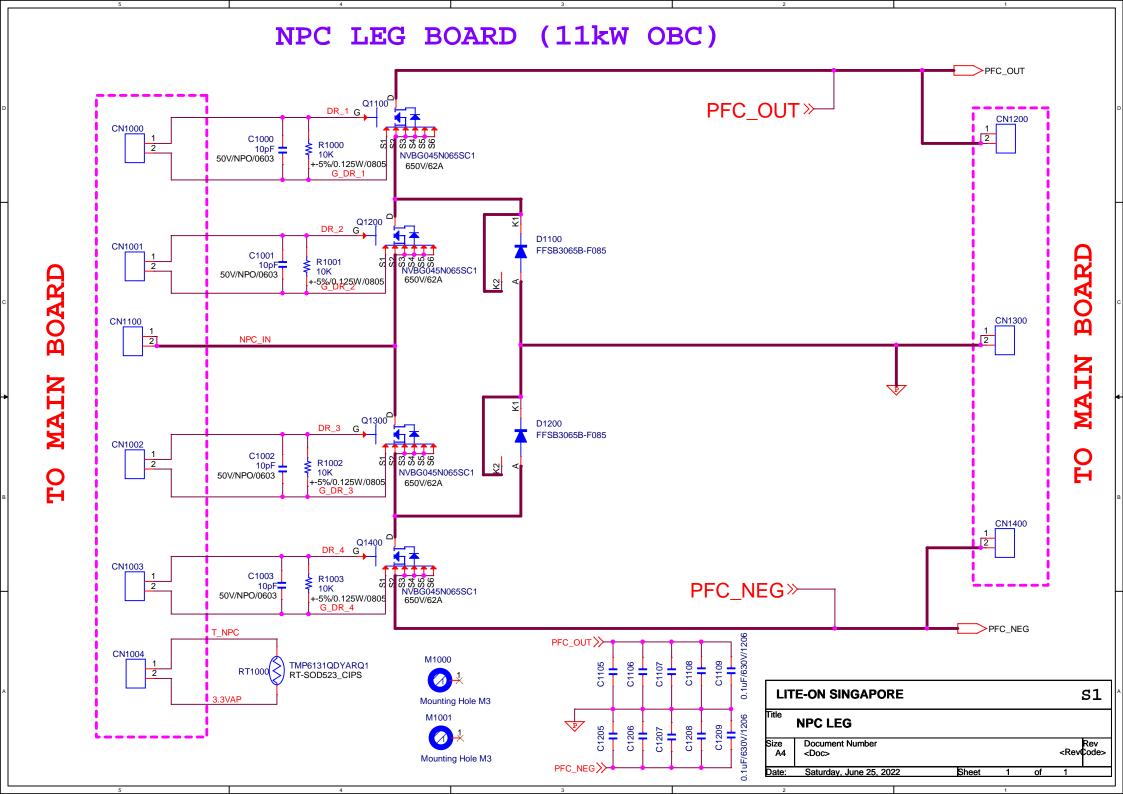
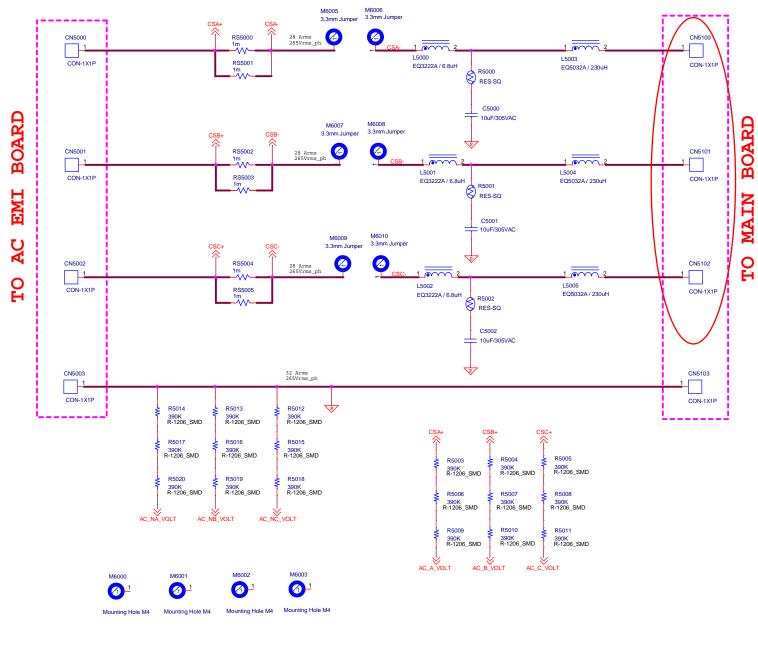


SECONDARY 12VBS1 R7806 20/1206 B7807 ->>VCC_CLLLC3_1S IC7100 UCC27524A-Q1 10K/0603 D7154 PMEG4010BEA D3Z18BF-7/ iPower:820414 NC1 NC2 8 D7155 C7596 10uF/25V/1206 ORPS_PWMA >>---\/\/-INB OUTB C7595 10uF/25V/1206 INA S OUTA D7156 T D7157 MEG4010BEA PMEG4010BEA R7810 C7600 open/0603 100pF/50V/0603 0.1uF/50V/0603 -(GND_CLLLC3_1S 4.7uF/25V/0805 C7597 Value = T8*4*4C 0.33uF|50V/0805 T7015 R7811 R7809 20/1206 GND_Dig_2 2.4/0805 GND_HVDC ->>VCC_CLLLC3_2S 7158 D7159 PMEG4010BEA 12VBS1 PMEG4010BEA C7601 C7602 10uF/25V/1206 Value 1-1 = NP1 D7160 UX PMEG4010BEA D7161 PMEG4010BEA R7812 10K/0603 R7818 3.3/1206 R7813 1.6K/0603 ORPS_PWMB>> - VV 1 NC1 0 NC2 8 GND_CLLLC3_2S 4 INB OUTB R7819 3.3/1206 Qpen/0603 C7603 Qpen/0603 R7815 Qpen/0603 Qpen/0603 Qpen/0603 Qpen/0605 Qpen C7604 4.7uF/25V/0805 GND_Dig_2 2.4/0805 GND_HVDC 12VBS1 R7820 20/1206 R7821 ->>VCC_CLLLC3_3S IC7101 ω UCC27524A-Q 10K/0603 R7822 1.6K/0603 NC1 O NC2 8 D7163 D3Z18BF-7/ iPower:820414 3.3/1206 PMEG4010BEA INB OUTB 7 10uF/25V/1206 3.3/1206 C7608 R7825 100pF/50V/0603 open/06 C7609 4.7uF/25V/0805 open/0603 -(⟨GND_CLLLC3_3S C7611 Value = T8*4*4C 0.33uF/50V/0805 T7016 R7826 R7827 20/1206 GND_Dig_2 2.4/0805 GND_HVDC ->>VCC_CLLLC3_4S D7167 PMEG4010BEA D3Z18BF-7/ iPower:8204 4 C7612 C7613 C7612 10uF/25V/1206 Value 1-1 = NP1 D7168 D7169 PMEG4010BEA PMEG4010BEA R7828 ω IC7102 UCC27524A-Q1 NC1 NC2 8 B7830 B7829 3.3/1206 -(GND_CLLLC3_4S ORPS_PWMB 1.6K/0603 4 INB OUTB R7831 3.3/1206 pen/0603 C7614 P0 100pF/50V/0603 C7616 0.1uF/50V/0603 R7833 GND_Dig_2 2.4/0805 C7615 4.7uF/25V/0805 GND HVDC B.7 FB_Sec_AUX_1 Size A3 Tuesday, July 05, 2022 Sheet 16 of

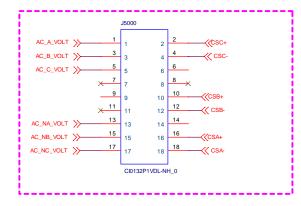


PFC LCL BOARD (11kW OBC)



Location(L1,L2,L3) need align with Main Board in A2 sample

TO MAIN BOARD



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Title	PFC LCL BOARD						
Size A3	Document Number <doc></doc>				<rev< th=""><th>Rev Code></th></rev<>	Rev Code>	
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BULK CAPACITOR BOARD (11kW OBC)

