

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, TELANGANA STATE, INDIA



CONSOLIDATED SYLLABUS OF B.TECH. ELECTRICAL & ELECTRONICS ENGINEERING



CSE070147B
Name: SUDHAKAR INDU
Hall Ticket No: 17X61A0233
Serial No: 2148301526
Year of Admission: 2017-2018

Name of the College: EKAJITS KOTHAJURU
Month & Year of First Exam: September, 2021
Class Awarded: FIRST CLASS

Sl. No.	Subject Title	I SEMESTER			II SEMESTER			III SEMESTER			IV SEMESTER		
		THEORY	LAB	MARKS	THEORY	LAB	MARKS	THEORY	LAB	MARKS	THEORY	LAB	MARKS

1	MATHEMATICS - I	5	0	5	1	ENGINEERING GRAPHICS	5	0	5	1
2	ENGINEERING MATHS	10	0	2	2	MATHEMATICS - II	5	0	5	1
3	ENGINEERING MECHANICS	5	0	5	1	ENGINEERING CHEMISTRY (LAB)	5	0	5	1
4	BASIC ELECTRICAL COMMUNICATION SKILLS (LAB)	10	0	2	2	ENGINEERING PHYSICS - I	5	0	5	1
5	ENGINEERING PHYSICS	5	0	5	1	ENGINEERING PHYSICS - II	5	0	5	1
6	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING	5	0	5	1	COMPUTER PROGRAMMING IN C	5	0	5	1
7	PROFESSIONAL COMMUNICATION IN ENGLISH	5	0	5	1	COMPUTER PROGRAMMING IN C++	5	0	5	1
8	ENGINEERING PHYSICS - I	5	0	5	1					
9	ENGINEERING PHYSICS - II	5	0	5	1					
10	COMPUTER PROGRAMMING IN C	5	0	5	1					
11	COMPUTER PROGRAMMING IN C++	5	0	5	1					
12	ENGINEERING PHYSICS - I	5	0	5	1					
13	ENGINEERING PHYSICS - II	5	0	5	1					
14	COMPUTER PROGRAMMING IN C	5	0	5	1					
15	COMPUTER PROGRAMMING IN C++	5	0	5	1					

1	ELECTRONIC DEVICES	5	0	5	1	1	INTRODUCTION TO MICROCONTROLLER	5	0	5	1	1	POWER ELECTRONICS	5	0	5	1
2	ELECTRONIC CIRCUITS	5	0	5	1	2	POWER SYSTEM ANALYSIS	5	0	5	1	2	POWER SYSTEM ANALYSIS	5	0	5	1
3	MATHEMATICS - II	5	0	5	1	3	LINEAR AND DIGITAL IC APPLICATIONS	5	0	5	1	3	LINEAR AND DIGITAL IC APPLICATIONS	5	0	5	1
4	ELECTRONIC CIRCUITS	5	0	5	1	4	POWER SYSTEM ANALYSIS	5	0	5	1	4	POWER SYSTEM ANALYSIS	5	0	5	1
5	ELECTRONIC CIRCUITS	5	0	5	1	5	POWER SYSTEM ANALYSIS	5	0	5	1	5	POWER SYSTEM ANALYSIS	5	0	5	1
6	NETWORKS	5	0	5	1	6	POWER SYSTEM ANALYSIS	5	0	5	1	6	POWER SYSTEM ANALYSIS	5	0	5	1
7	ELECTRONIC DEVICES & CIRCUITS LAB	5	0	5	1	7	POWER SYSTEM ANALYSIS	5	0	5	1	7	POWER SYSTEM ANALYSIS	5	0	5	1
8	NETWORKS LAB	5	0	5	1	8	POWER SYSTEM ANALYSIS	5	0	5	1	8	POWER SYSTEM ANALYSIS	5	0	5	1
9	ELECTRONIC DEVICES & CIRCUITS LAB	5	0	5	1	9	POWER SYSTEM ANALYSIS	5	0	5	1	9	POWER SYSTEM ANALYSIS	5	0	5	1
10	NETWORKS LAB	5	0	5	1	10	POWER SYSTEM ANALYSIS	5	0	5	1	10	POWER SYSTEM ANALYSIS	5	0	5	1
11	ELECTRONIC DEVICES & CIRCUITS LAB	5	0	5	1	11	POWER SYSTEM ANALYSIS	5	0	5	1	11	POWER SYSTEM ANALYSIS	5	0	5	1
12	NETWORKS LAB	5	0	5	1	12	POWER SYSTEM ANALYSIS	5	0	5	1	12	POWER SYSTEM ANALYSIS	5	0	5	1

1	POWER SYSTEM ANALYSIS	5	0	5	1	1	POWER SYSTEM ANALYSIS	5	0	5	1	1	POWER SYSTEM ANALYSIS	5	0	5	1
2	POWER SYSTEM ANALYSIS	5	0	5	1	2	POWER SYSTEM ANALYSIS	5	0	5	1	2	POWER SYSTEM ANALYSIS	5	0	5	1
3	POWER SYSTEM ANALYSIS	5	0	5	1	3	POWER SYSTEM ANALYSIS	5	0	5	1	3	POWER SYSTEM ANALYSIS	5	0	5	1
4	POWER SYSTEM ANALYSIS	5	0	5	1	4	POWER SYSTEM ANALYSIS	5	0	5	1	4	POWER SYSTEM ANALYSIS	5	0	5	1
5	POWER SYSTEM ANALYSIS	5	0	5	1	5	POWER SYSTEM ANALYSIS	5	0	5	1	5	POWER SYSTEM ANALYSIS	5	0	5	1
6	POWER SYSTEM ANALYSIS	5	0	5	1	6	POWER SYSTEM ANALYSIS	5	0	5	1	6	POWER SYSTEM ANALYSIS	5	0	5	1
7	POWER SYSTEM ANALYSIS	5	0	5	1	7	POWER SYSTEM ANALYSIS	5	0	5	1	7	POWER SYSTEM ANALYSIS	5	0	5	1
8	POWER SYSTEM ANALYSIS	5	0	5	1	8	POWER SYSTEM ANALYSIS	5	0	5	1	8	POWER SYSTEM ANALYSIS	5	0	5	1
9	POWER SYSTEM ANALYSIS	5	0	5	1	9	POWER SYSTEM ANALYSIS	5	0	5	1	9	POWER SYSTEM ANALYSIS	5	0	5	1
10	POWER SYSTEM ANALYSIS	5	0	5	1	10	POWER SYSTEM ANALYSIS	5	0	5	1	10	POWER SYSTEM ANALYSIS	5	0	5	1
11	POWER SYSTEM ANALYSIS	5	0	5	1	11	POWER SYSTEM ANALYSIS	5	0	5	1	11	POWER SYSTEM ANALYSIS	5	0	5	1
12	POWER SYSTEM ANALYSIS	5	0	5	1	12	POWER SYSTEM ANALYSIS	5	0	5	1	12	POWER SYSTEM ANALYSIS	5	0	5	1

1	POWER SYSTEM ANALYSIS	5	0	5	1	1	POWER SYSTEM ANALYSIS	5	0	5	1	1	POWER SYSTEM ANALYSIS	5	0	5	1
2	POWER SYSTEM ANALYSIS	5	0	5	1	2	POWER SYSTEM ANALYSIS	5	0	5	1	2	POWER SYSTEM ANALYSIS	5	0	5	1
3	POWER SYSTEM ANALYSIS	5	0	5	1	3	POWER SYSTEM ANALYSIS	5	0	5	1	3	POWER SYSTEM ANALYSIS	5	0	5	1
4	POWER SYSTEM ANALYSIS	5	0	5	1	4	POWER SYSTEM ANALYSIS	5	0	5	1	4	POWER SYSTEM ANALYSIS	5	0	5	1
5	POWER SYSTEM ANALYSIS	5	0	5	1	5	POWER SYSTEM ANALYSIS	5	0	5	1	5	POWER SYSTEM ANALYSIS	5	0	5	1
6	POWER SYSTEM ANALYSIS	5	0	5	1	6	POWER SYSTEM ANALYSIS	5	0	5	1	6	POWER SYSTEM ANALYSIS	5	0	5	1
7	POWER SYSTEM ANALYSIS	5	0	5	1	7	POWER SYSTEM ANALYSIS	5	0	5	1	7	POWER SYSTEM ANALYSIS	5	0	5	1
8	POWER SYSTEM ANALYSIS	5	0	5	1	8	POWER SYSTEM ANALYSIS	5	0	5	1	8	POWER SYSTEM ANALYSIS	5	0	5	1
9	POWER SYSTEM ANALYSIS	5	0	5	1	9	POWER SYSTEM ANALYSIS	5	0	5	1	9	POWER SYSTEM ANALYSIS	5	0	5	1
10	POWER SYSTEM ANALYSIS	5	0	5	1	10	POWER SYSTEM ANALYSIS	5	0	5	1	10	POWER SYSTEM ANALYSIS	5	0	5	1
11	POWER SYSTEM ANALYSIS	5	0	5	1	11	POWER SYSTEM ANALYSIS	5	0	5	1	11	POWER SYSTEM ANALYSIS	5	0	5	1
12	POWER SYSTEM ANALYSIS	5	0	5	1	12	POWER SYSTEM ANALYSIS	5	0	5	1	12	POWER SYSTEM ANALYSIS	5	0	5	1

Maximum of Credits registered for: 192
Applicable Credits Secured by Year: 188
Percentage of Credits Secured: 97.92%
Date of issue: December 30, 2021
Signature of Controller of Examinations: *K. Venkateswara Rao*
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