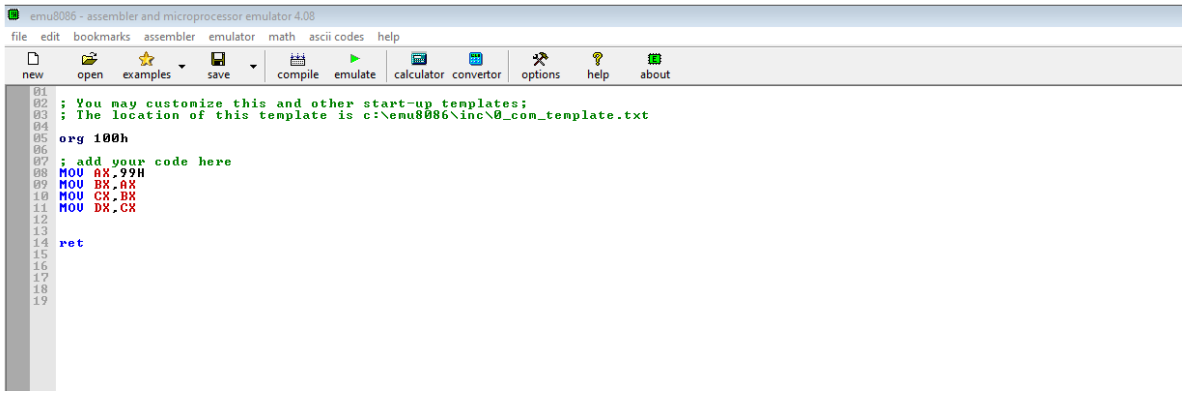
Mnl Lab Manual: 2

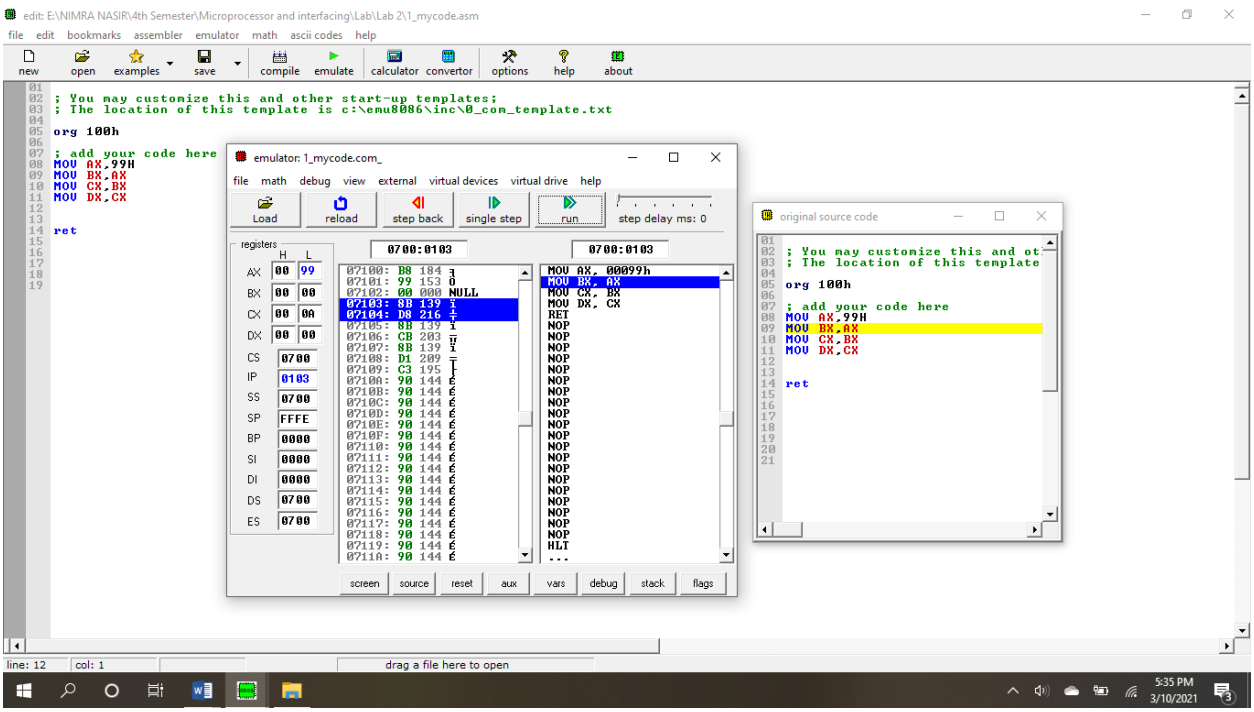
TASK: 1

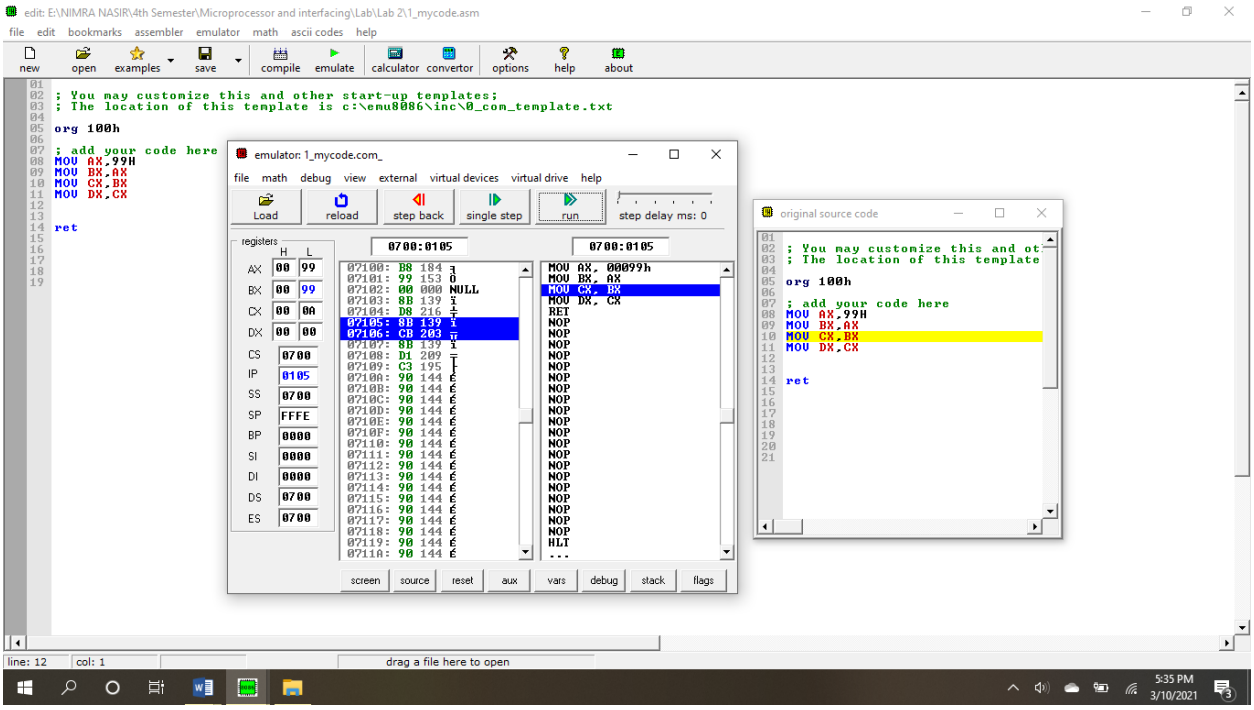
Write and assemble a program to load register AX with value 99H. Then from register AX move it to BX, CX, and DX. Use the simulator to single-step the program and examine the registers.

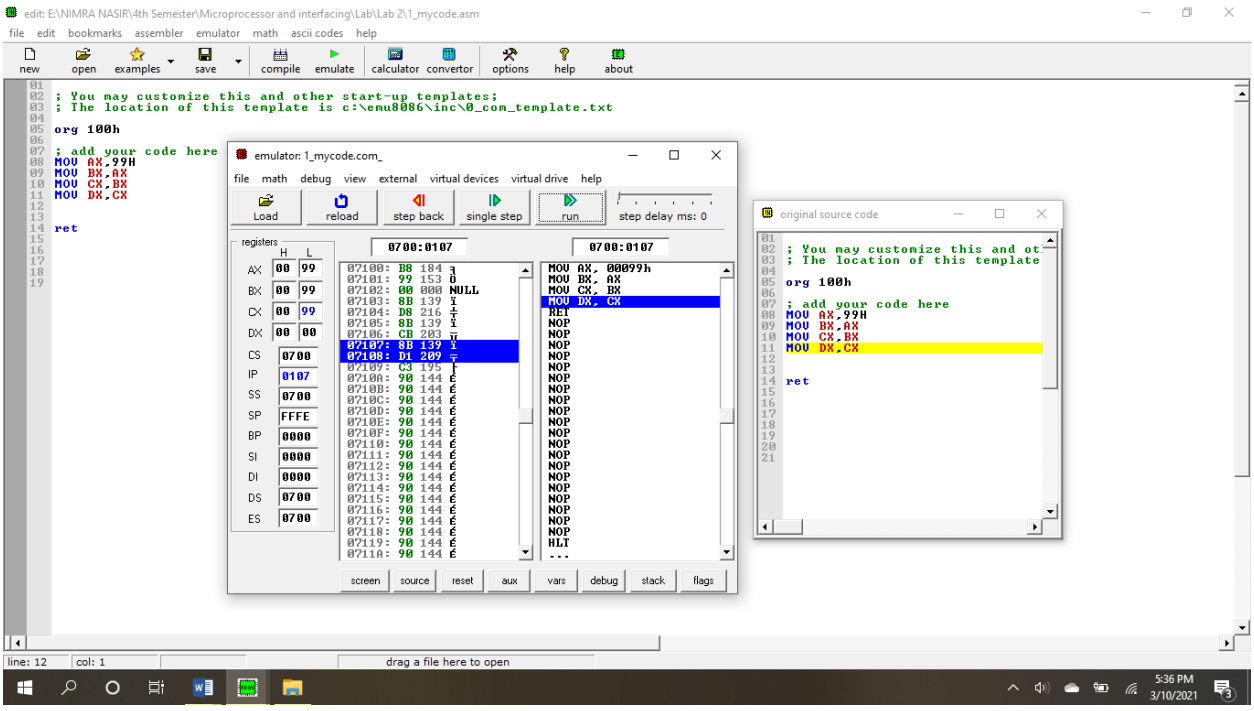
SOURCE CODE:



Output:




edit: E:\NIMRA NASIR\4th Semester\Microprocessor and interfacing\Lab\Lab 2\1_mycode.asm
file edit bookmarks assembler emulator math ascii codes help
new open examples save compile emulate calculator convertor options help about
; You may customize this and other start-up templates;
; The location of this template is c:\emu8086\inc\0_com_template.txt
org 100h
; add your code here
MOV AX,99H
MOV BX,AX
MOV CX,BX
MOV DX,CX
ret
emulator: 1_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 99
BX 00 99
CX 00 0A
DX 00 00
CS 0700
IP 0105
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
0700:0105
0700:0105
07100: B8 184
07101: 99 153
07102: 00 000 NULL
07103: 8B 139
07104: D8 216
07105: 8B 139
07106: CB 203
07107: 8B 139
07108: D1 209
07109: C3 195
0710A: 90 144 É
0710B: 90 144 É
0710C: 90 144 É
0710D: 90 144 É
0710E: 90 144 É
0710F: 90 144 É
07110: 90 144 É
07111: 90 144 É
07112: 90 144 É
07113: 90 144 É
07114: 90 144 É
07115: 90 144 É
07116: 90 144 É
07117: 90 144 É
07118: 90 144 É
07119: 90 144 É
0711A: 90 144 É
MOV AX, 00099h
MOV BX, AX
MOV CX, BX
MOV DX, CX
RET
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
HLT
...
screen source reset aux vars debug stack flags
original source code
; You may customize this and ot
; The location of this template
org 100h
; add your code here
MOV AX,99H
MOV BX,AX
MOV CX,BX
MOV DX,CX
ret
line: 12 col: 1
drag a file here to open
5:35 PM
3/10/2021


edit: E:\NIMRA NASIR\4th Semester\Microprocessor and interfacing\Lab\Lab 2\1_mycode.asm
file edit bookmarks assembler emulator math ascii codes help
new open examples save compile emulate calculator convertor options help about
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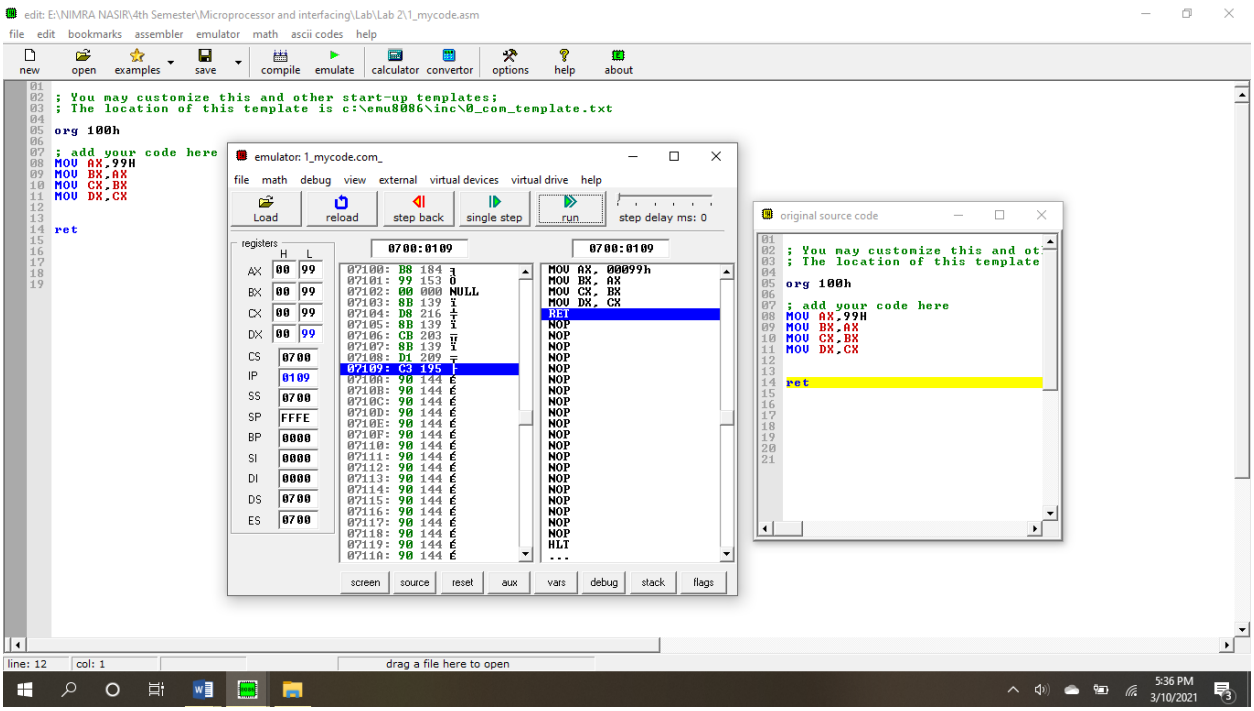
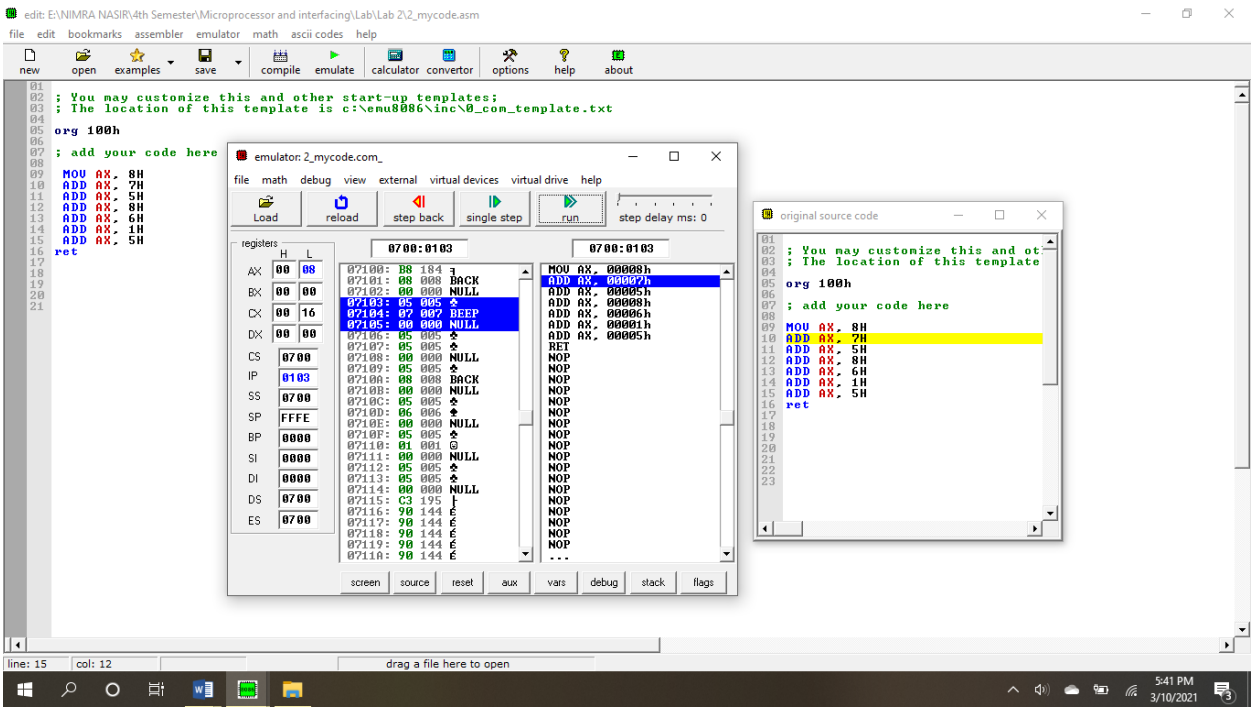
; The location of this template is c:\emu8086\inc\0_com_template.txt
org 100h
; add your code here
MOV AX,99H
MOV BX,AX
MOV CX,BX
MOV DX,CX
ret
emulator: 1_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 99
BX 00 99
CX 00 99
DX 00 00
CS 0700
IP 0107
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
0700:0107
0700:0107
07100: B8 184
07101: 99 153
07102: 00 000 NULL
07103: 8B 139
07104: D8 216
07105: 8B 139
07106: CB 203
07107: 8B 139
07108: D1 209
07109: C3 195
0710A: 90 144 É
0710B: 90 144 É
0710C: 90 144 É
0710D: 90 144 É
0710E: 90 144 É
0710F: 90 144 É
07110: 90 144 É
07111: 90 144 É
07112: 90 144 É
07113: 90 144 É
07114: 90 144 É
07115: 90 144 É
07116: 90 144 É
07117: 90 144 É
07118: 90 144 É
07119: 90 144 É
0711A: 90 144 É
MOV AX, 00099h
MOV BX, AX
MOV CX, BX
MOV DX, CX
RET
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
HLT
...
screen source reset aux vars debug stack flags
original source code
; You may customize this and ot
; The location of this template
org 100h
; add your code here
MOV AX,99H
MOV BX,AX
MOV CX,BX
MOV DX,CX
ret
line: 12 col: 1
drag a file here to open
5:36 PM
3/10/2021



TASK: 2

Write and assemble a program to add all the single digits of your ID number and save the result in Accumulator. Pick 7 random numbers (all single digit) if you do not want to use your ID number. Then use the simulator to single-step the program and examine the registers.

Output:


edit: E:\NIMRA NASIR\4th Semester\Microprocessor and interfacing\Lab\Lab 2\2_mycode.asm
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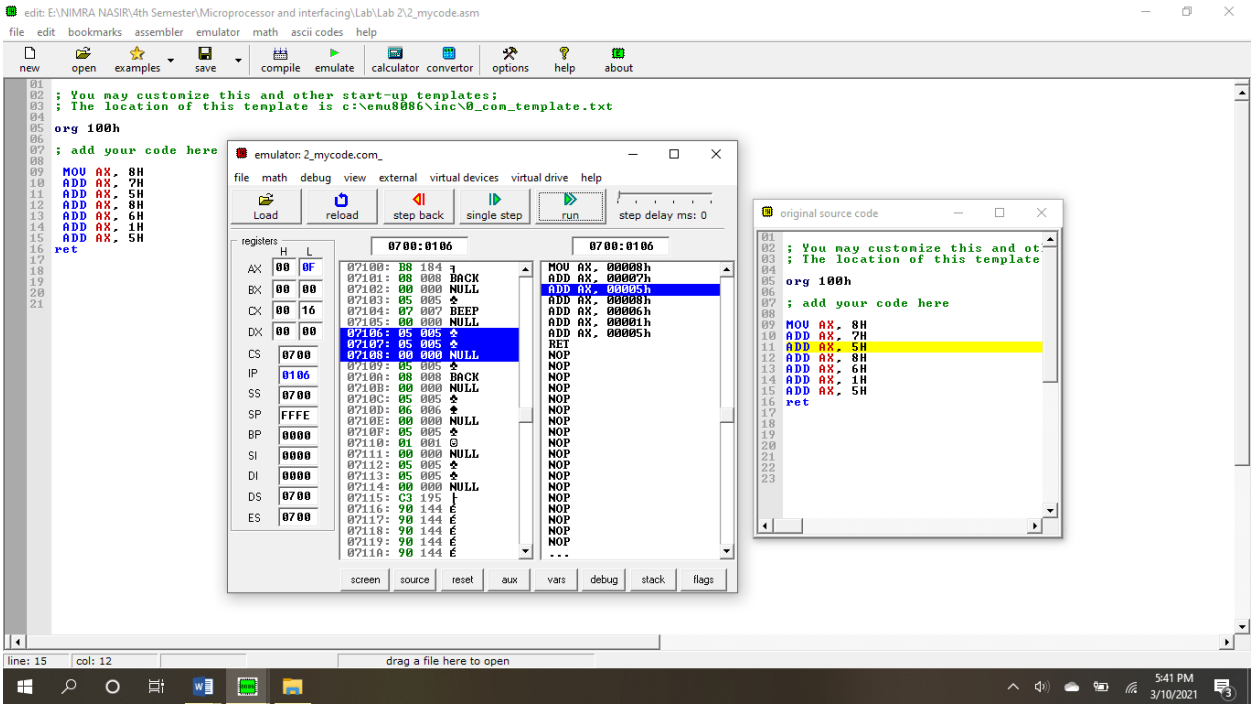
new open examples save compile emulate calculator convertor options help about
; You may customize this and other start-up templates;
; The location of this template is c:\emu8086\inc\0_com_template.txt
org 100h
; add your code here
MOV AX, 8H
ADD AX, 7H
ADD AX, 5H
ADD AX, 8H
ADD AX, 6H
ADD AX, 1H
ADD AX, 5H
ret
emulator: 2_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 08
BX 00 00
CX 00 16
DX 00 00
CS 0700
IP 0103
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
0700:0103
0700:0103
screen source reset aux vars debug stack flags
original source code
line: 15 col: 12
drag a file here to open
5:41 PM
3/10/2021


edit: E:\NIMRA NASIR\4th Semester\Microprocessor and interfacing\Lab\Lab 2\2_mycode.asm
file edit bookmarks assembler emulator math ascii codes help
new open examples save compile emulate calculator convertor options help about
; You may customize this and other start-up templates;
; The location of this template is c:\emu8086\inc\0_com_template.txt
org 100h
; add your code here
MOV AX, 8H
ADD AX, 7H
ADD AX, 5H
ADD AX, 8H
ADD AX, 6H
ADD AX, 1H
ADD AX, 5H
ret
emulator: 2_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 0F
BX 00 00
CX 00 16
DX 00 00
CS 0700
IP 0106
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
0700:0106
0700:0106
screen source reset aux vars debug stack flags
original source code
line: 15 col: 12
drag a file here to open
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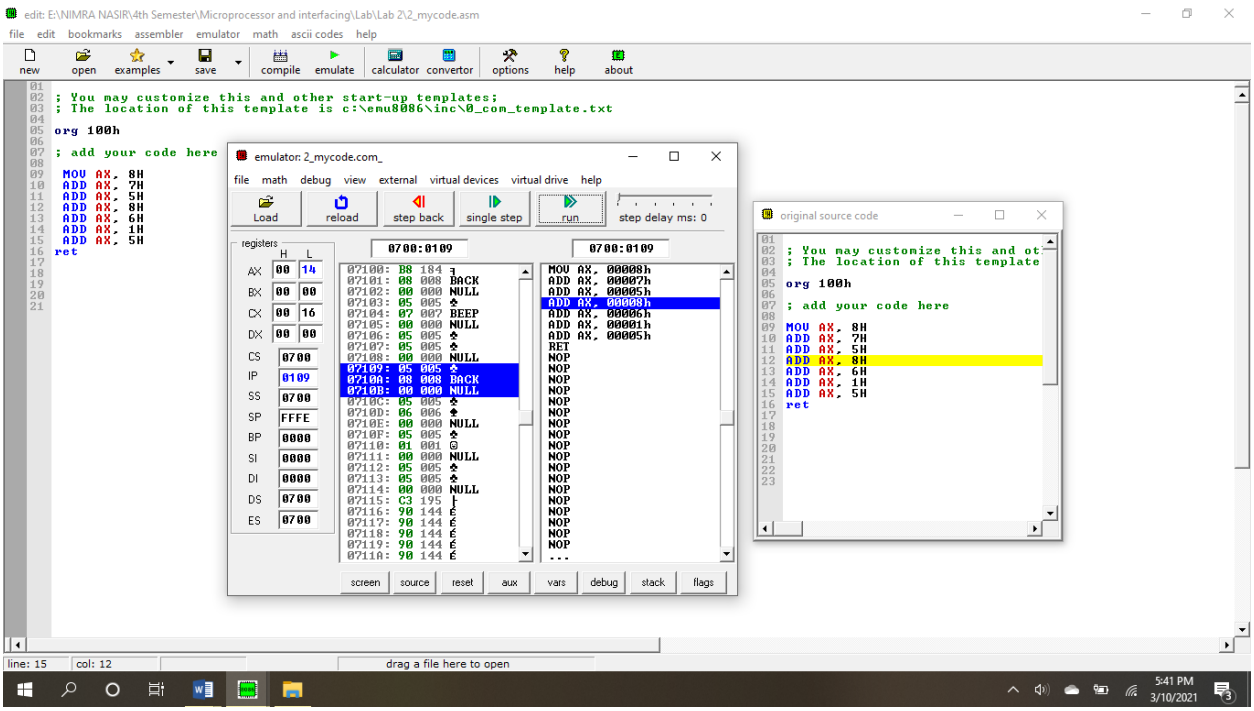
3/10/2021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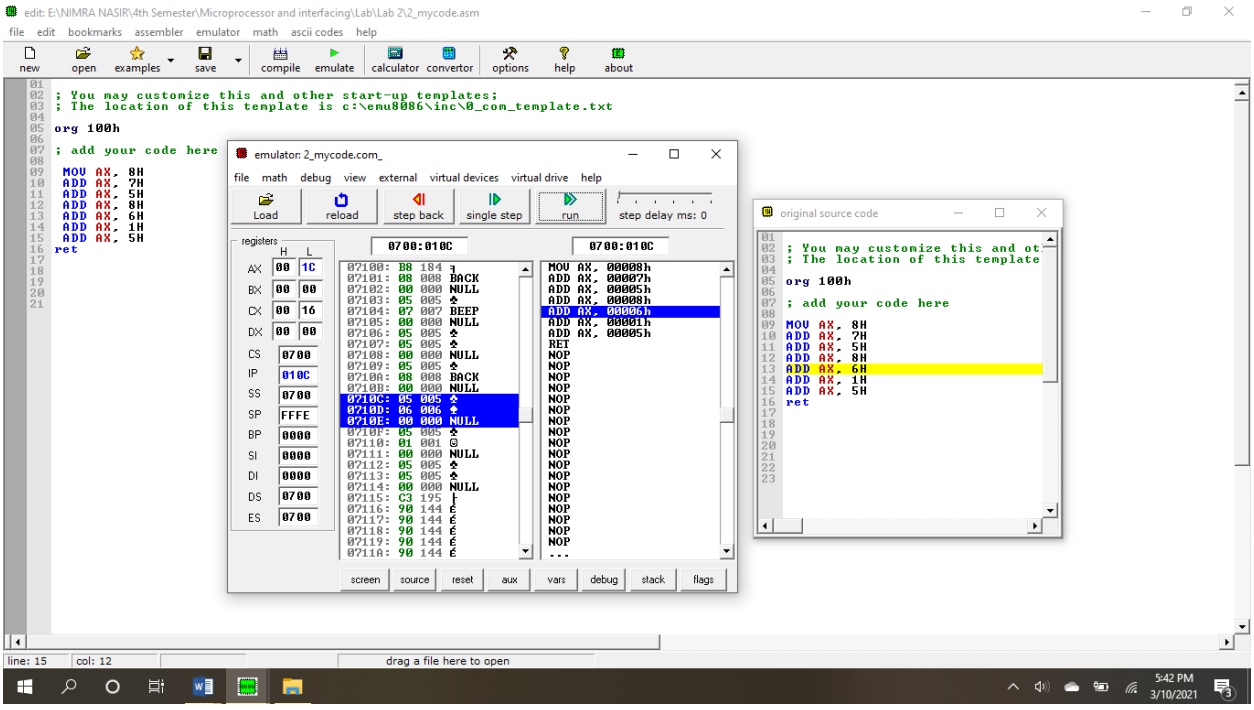
new open examples save compile emulate calculator convertor options help about
; You may customize this and other start-up templates;
; The location of this template is c:\emu8086\inc\0_com_template.txt
org 100h
; add your code here
MOV AX, 8H
ADD AX, 7H
ADD AX, 5H
ADD AX, 8H
ADD AX, 6H
ADD AX, 1H
ADD AX, 5H
ret
emulator: 2_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 14
BX 00 00
CX 00 16
DX 00 00
CS 0700
IP 0109
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
0700:0109
0700:0109
screen source reset aux vars debug stack flags
original source code
line: 15 col: 12
drag a file here to open
5:41 PM
3/10/2021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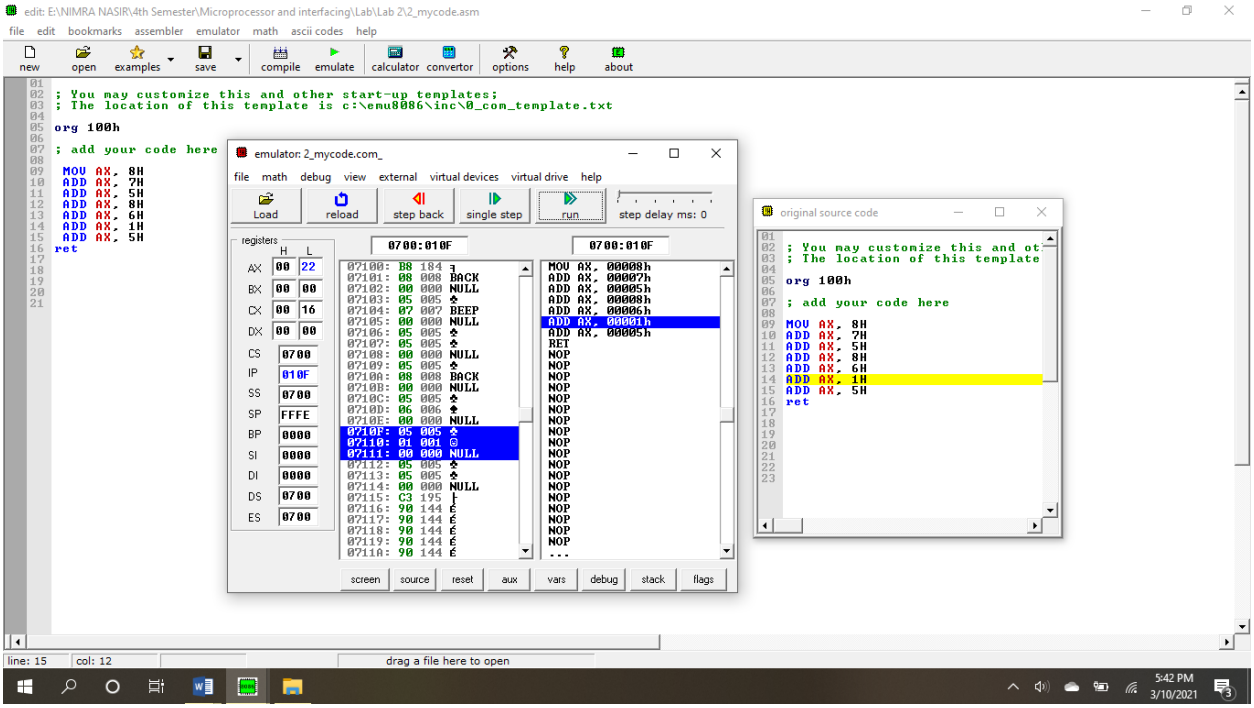
new open examples save compile emulate calculator convertor options help about
; You may customize this and other start-up templates;
; The location of this template is c:\emu8086\inc\0_com_template.txt
org 100h
; add your code here
MOV AX, 8H
ADD AX, 7H
ADD AX, 5H
ADD AX, 8H
ADD AX, 6H
ADD AX, 1H
ADD AX, 5H
ret
emulator: 2_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 1C
BX 00 00
CX 00 16
DX 00 00
CS 0700
IP 010C
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
0700:010C
0700:010C
screen source reset aux vars debug stack flags
original source code
line: 15 col: 12
drag a file here to open
5:42 PM
3/10/2021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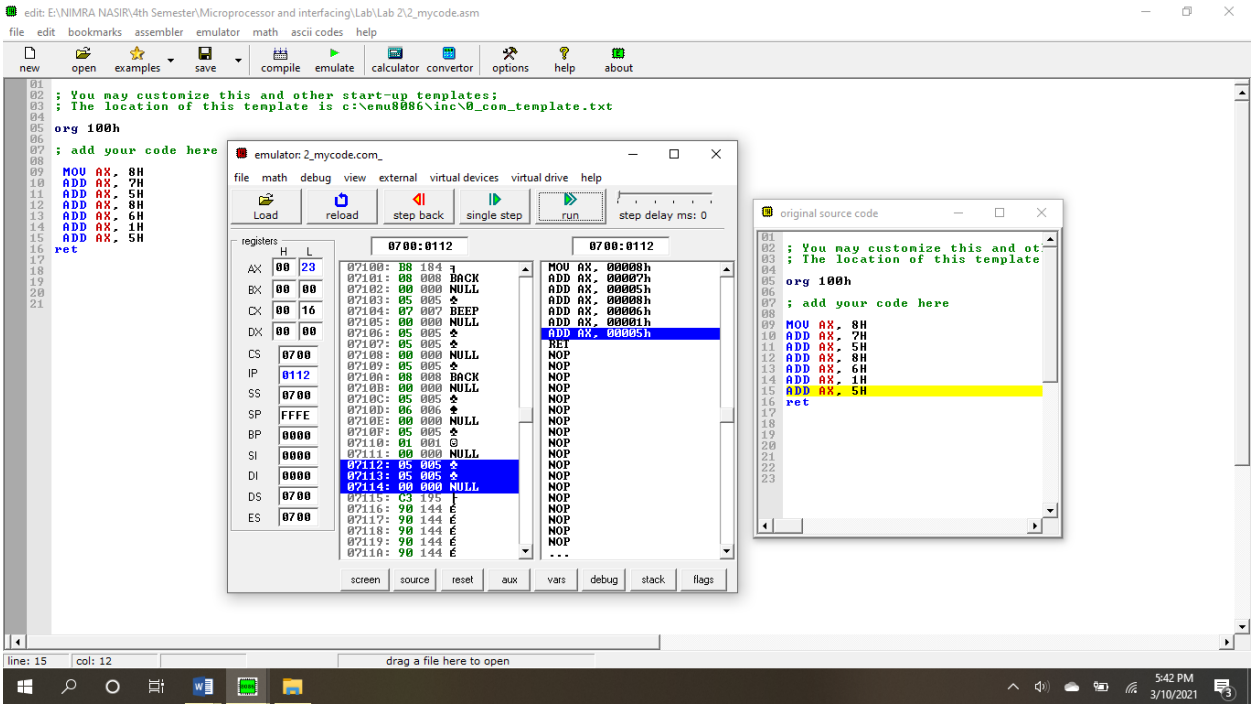
file edit bookmarks assembler emulator math ascii codes help
new open examples save compile emulate calculator convertor options help about
; You may customize this and other start-up templates;
; The location of this template is c:\emu8086\inc\0_com_template.txt
org 100h
; add your code here
MOV AX, 8H
ADD AX, 7H
ADD AX, 5H
ADD AX, 8H
ADD AX, 6H
ADD AX, 1H
ADD AX, 5H
ret
emulator: 2_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 22
BX 00 00
CX 00 16
DX 00 00
CS 0700
IP 010F
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
0700:010F
0700:010F
screen source reset aux vars debug stack flags
original source code
line: 15 col: 12
drag a file here to open
5:42 PM
3/10/2021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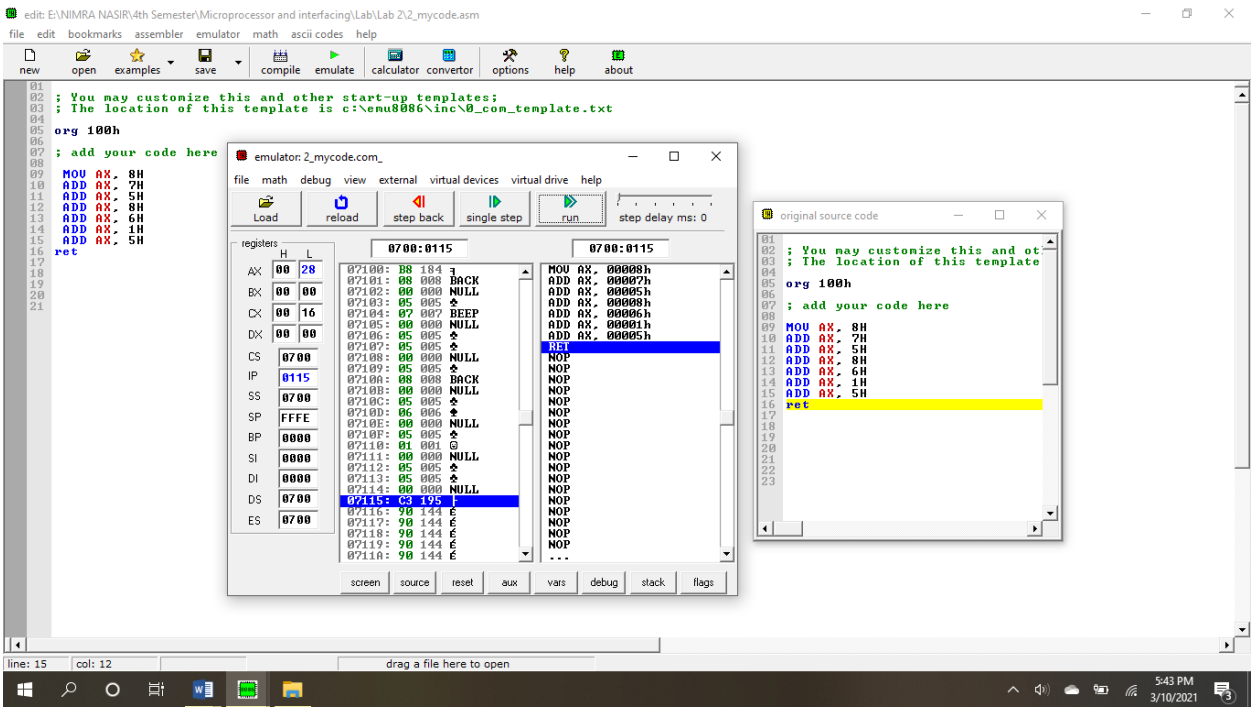
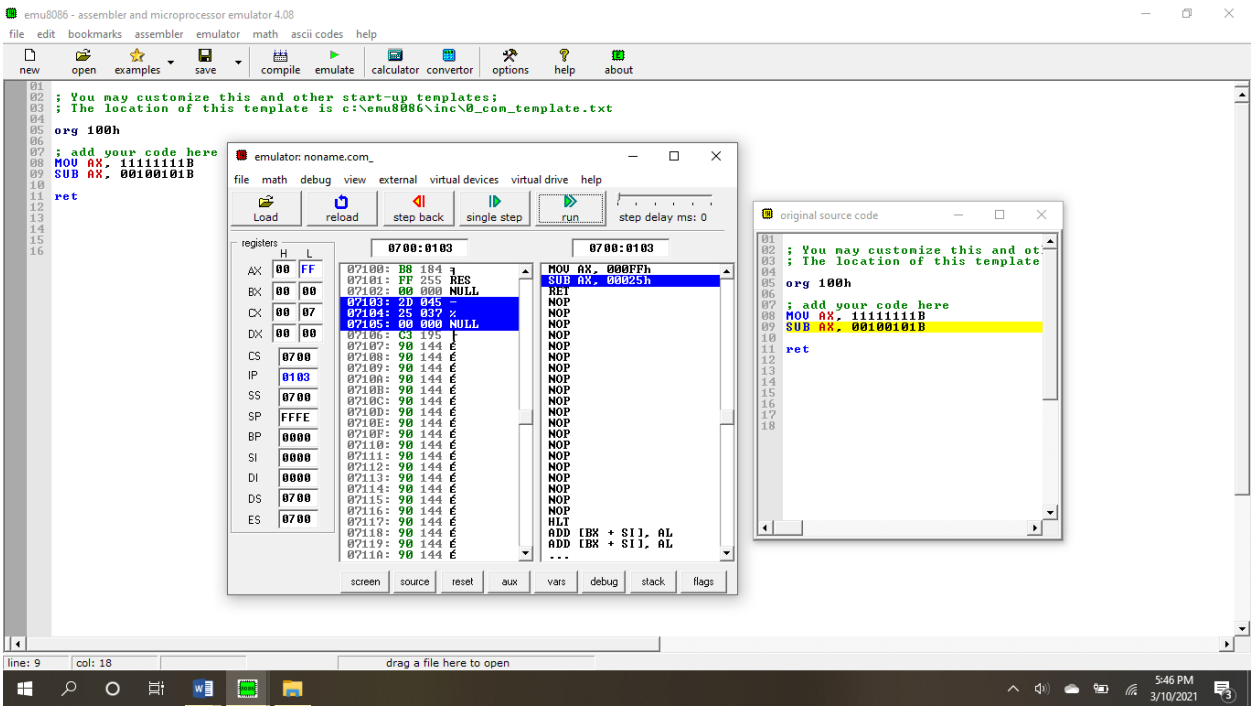
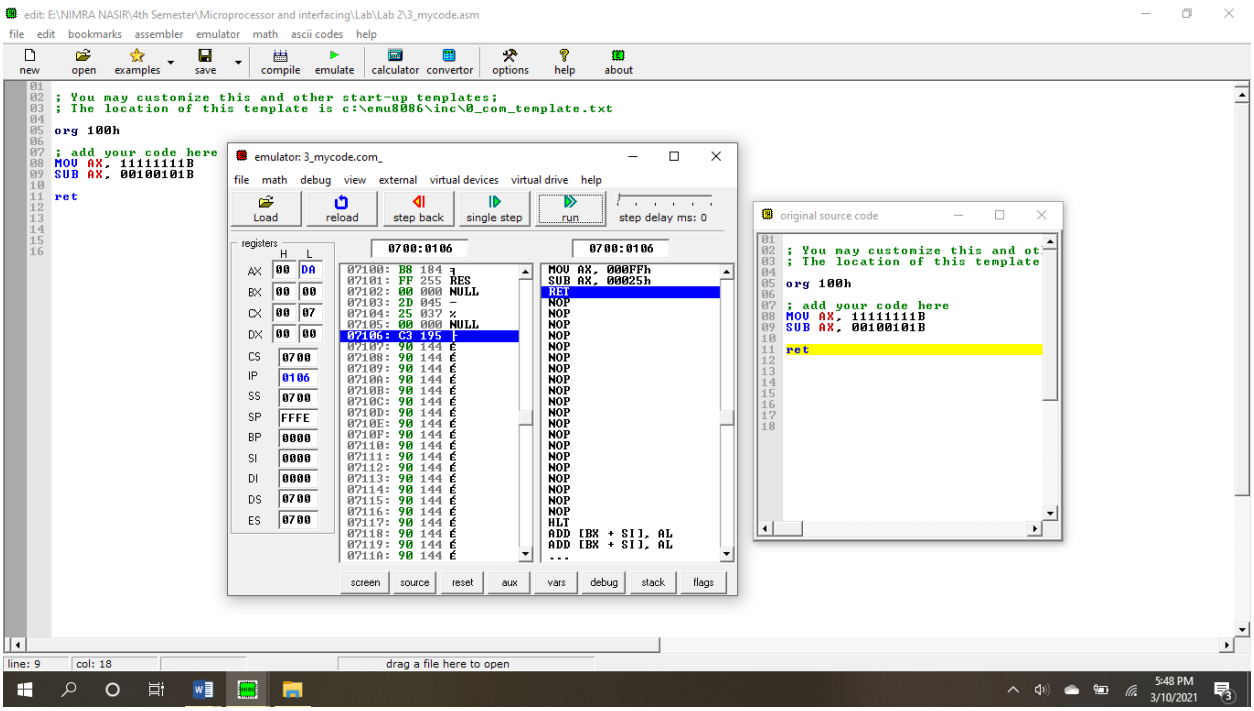
file edit bookmarks assembler emulator math ascii codes help
new open examples save compile emulate calculator convertor options help about
; You may customize this and other start-up templates;
; The location of this template is c:\emu8086\inc\0_com_template.txt
org 100h
; add your code here
MOV AX, 8H
ADD AX, 7H
ADD AX, 5H
ADD AX, 8H
ADD AX, 6H
ADD AX, 1H
ADD AX, 5H
ret
emulator: 2_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 23
BX 00 00
CX 00 16
DX 00 00
CS 0700
IP 0112
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
0700:0112
0700:0112
screen source reset aux vars debug stack flags
original source code
line: 15 col: 12
drag a file here to open
5:42 PM
3/10/2021



TASK: 3

Subtraction of two 8 bit numbers and place the result in accumulator register

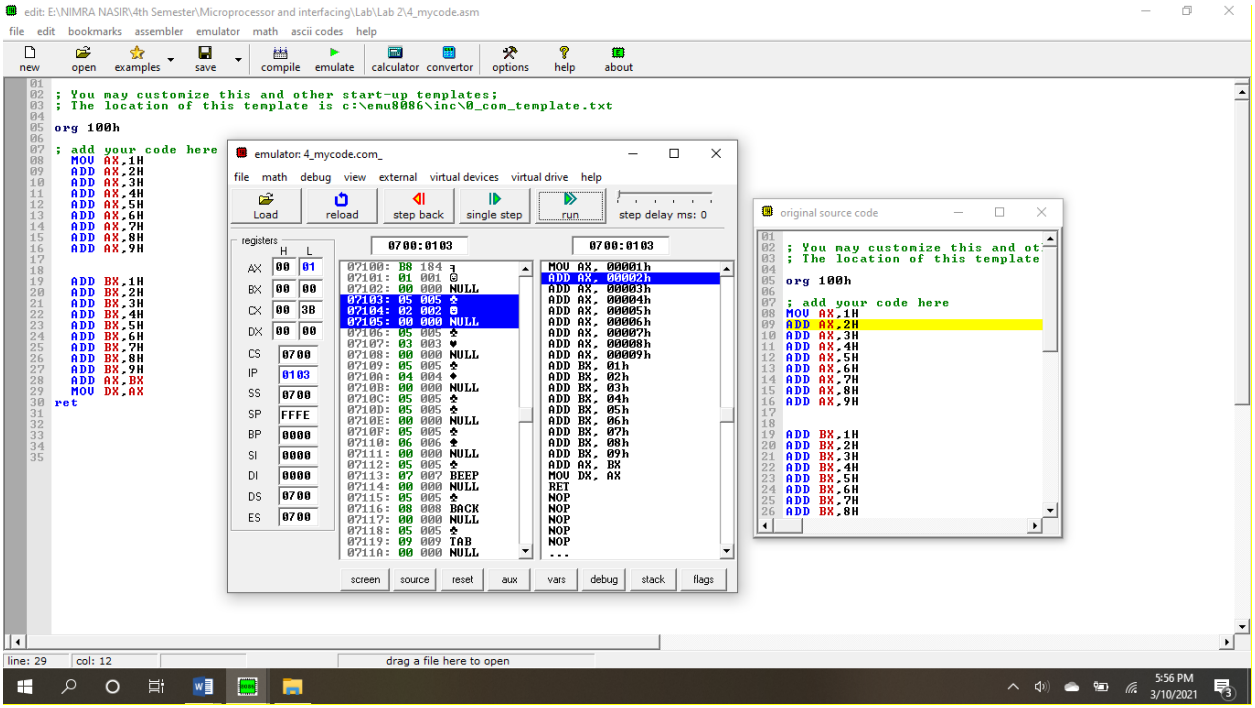


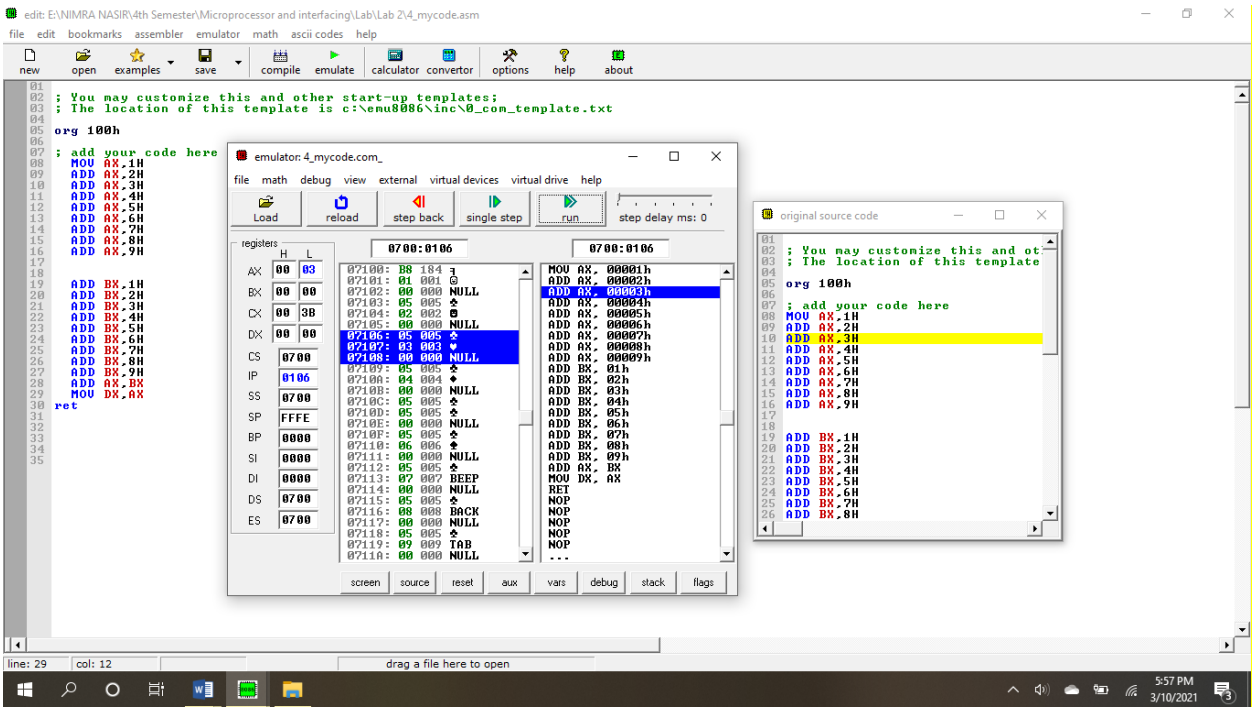


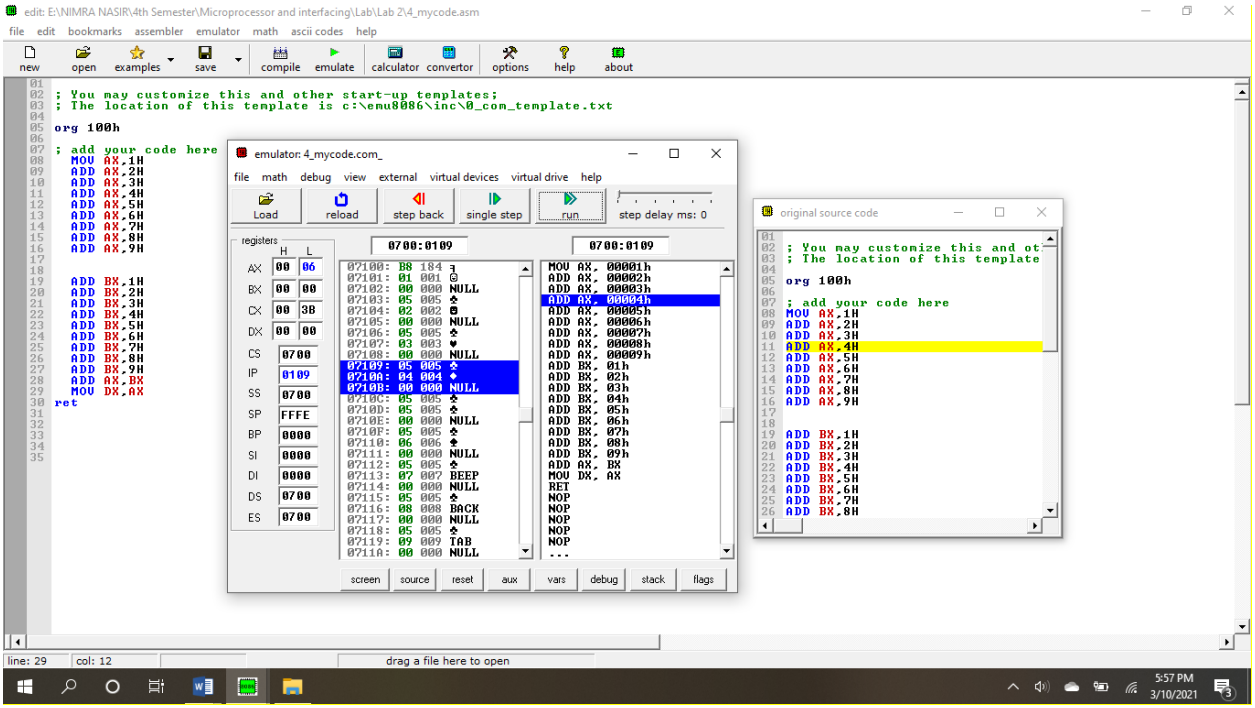
TASK: 4

Add sum of series of first 9 numbers and save it into one of the register. Again take the sum of first 9 numbers and save it to another register. The contents of both the register must be added finally and save the result in the third register.

Output:


edit: E:\NIMRA NASIR\4th Semester\Microprocessor and interfacing\Lab\Lab 2\4_mycode.asm
file edit bookmarks assembler emulator math ascii codes help
new open examples save compile emulate calculator convertor options help about
; You may customize this and other start-up templates;
; The location of this template is c:\emu8086\inc\0_com_template.txt
org 100h
; add your code here
MOV AX,1H
ADD AX,2H
ADD AX,3H
ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
emulator: 4_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 01
BX 00 00
CX 00 3B
DX 00 00
CS 0700
IP 0103
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
0700:0103
0700:0103
screen source reset aux vars debug stack flags
original source code
line: 29 col: 12
drag a file here to open
5:56 PM
3/10/2021


edit: E:\NIMRA NASIR\4th Semester\Microprocessor and interfacing\Lab\Lab 2\4_mycode.asm
file edit bookmarks assembler emulator math ascii codes help
new open examples save compile emulate calculator convertor options help about
; You may customize this and other start-up templates;
; The location of this template is c:\emu8086\inc\0_com_template.txt
org 100h
; add your code here
MOV AX,1H
ADD AX,2H
ADD AX,3H
ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
emulator: 4_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 03
BX 00 00
CX 00 3B
DX 00 00
CS 0700
IP 0106
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
0700:0106
0700:0106
screen source reset aux vars debug stack flags
original source code
line: 29 col: 12
drag a file here to open
5:57 PM
3/10/2021


edit: E:\NIMRA NASIR\4th Semester\Microprocessor and interfacing\Lab\Lab 2\4_mycode.asm
file edit bookmarks assembler emulator math ascii codes help
new open examples save compile emulate calculator convertor options help about
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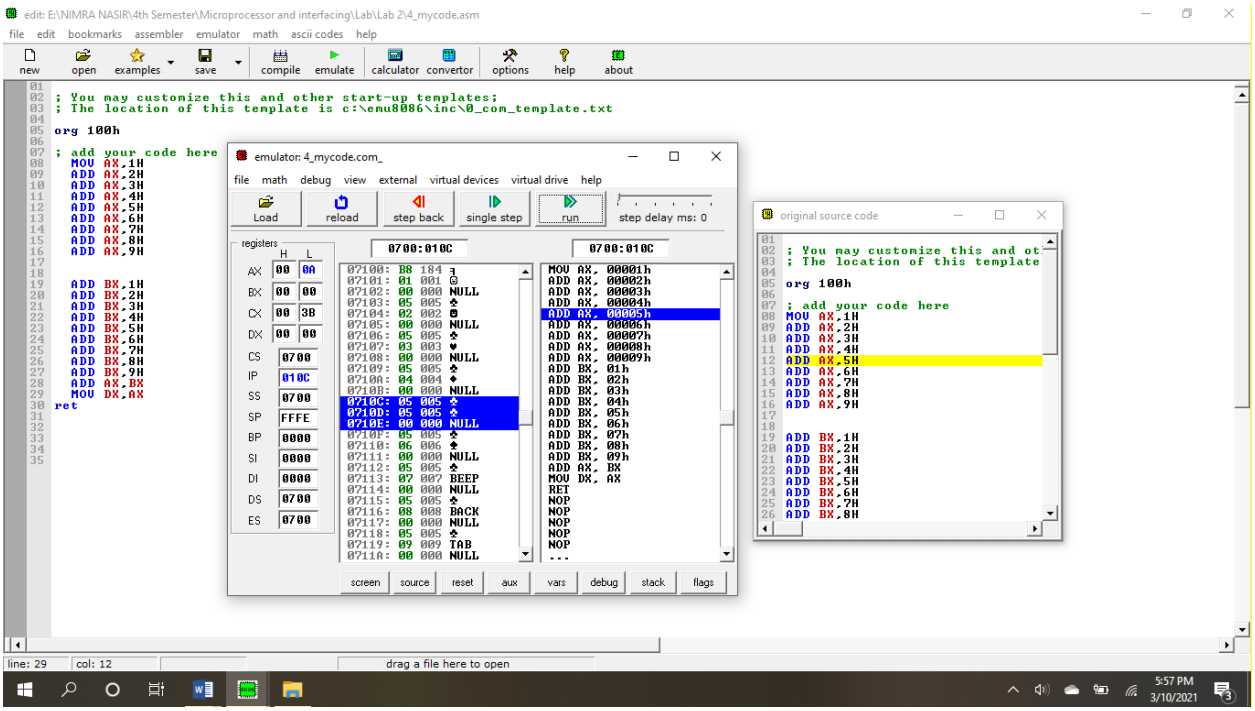
; The location of this template is c:\emu8086\inc\0_com_template.txt
org 100h
; add your code here
MOV AX,1H
ADD AX,2H
ADD AX,3H
ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
emulator: 4_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 06
BX 00 00
CX 00 3B
DX 00 00
CS 0700
IP 0109
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
0700:0109
0700:0109
screen source reset aux vars debug stack flags
original source code
line: 29 col: 12
drag a file here to open
5:57 PM
3/10/2021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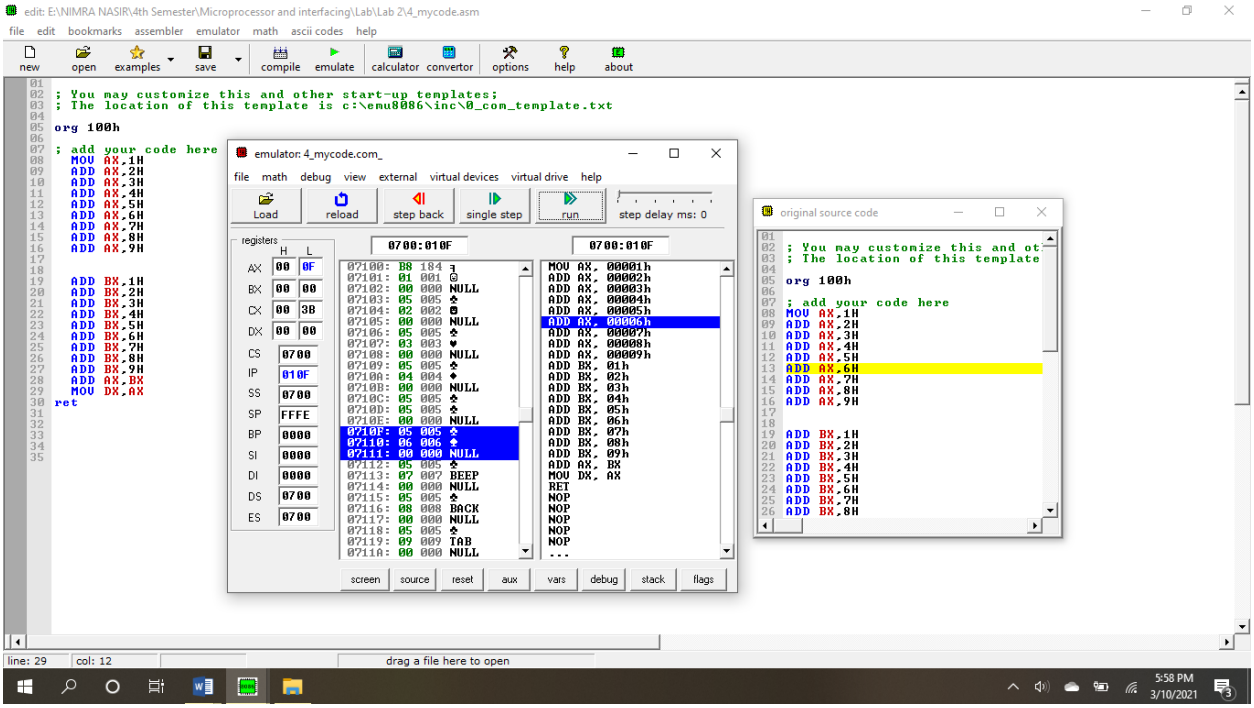
; The location of this template is c:\emu8086\inc\0_com_template.txt
org 100h
; add your code here
MOV AX,1H
ADD AX,2H
ADD AX,3H
ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
emulator: 4_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 0A
BX 00 00
CX 00 3B
DX 00 00
CS 0700
IP 010C
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
0700:010C
0700:010C
screen source reset aux vars debug stack flags
original source code
line: 29 col: 12
drag a file here to open
5:57 PM
3/10/2021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file edit bookmarks assembler emulator math ascii codes help
new open examples save compile emulate calculator convertor options help about
; You may customize this and other start-up templates;
; The location of this template is c:\emu8086\inc\0_com_template.txt
org 100h
; add your code here
MOV AX,1H
ADD AX,2H
ADD AX,3H
ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
emulator: 4_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 0F
BX 00 00
CX 00 3B
DX 00 00
CS 0700
IP 010F
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
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0700:010F
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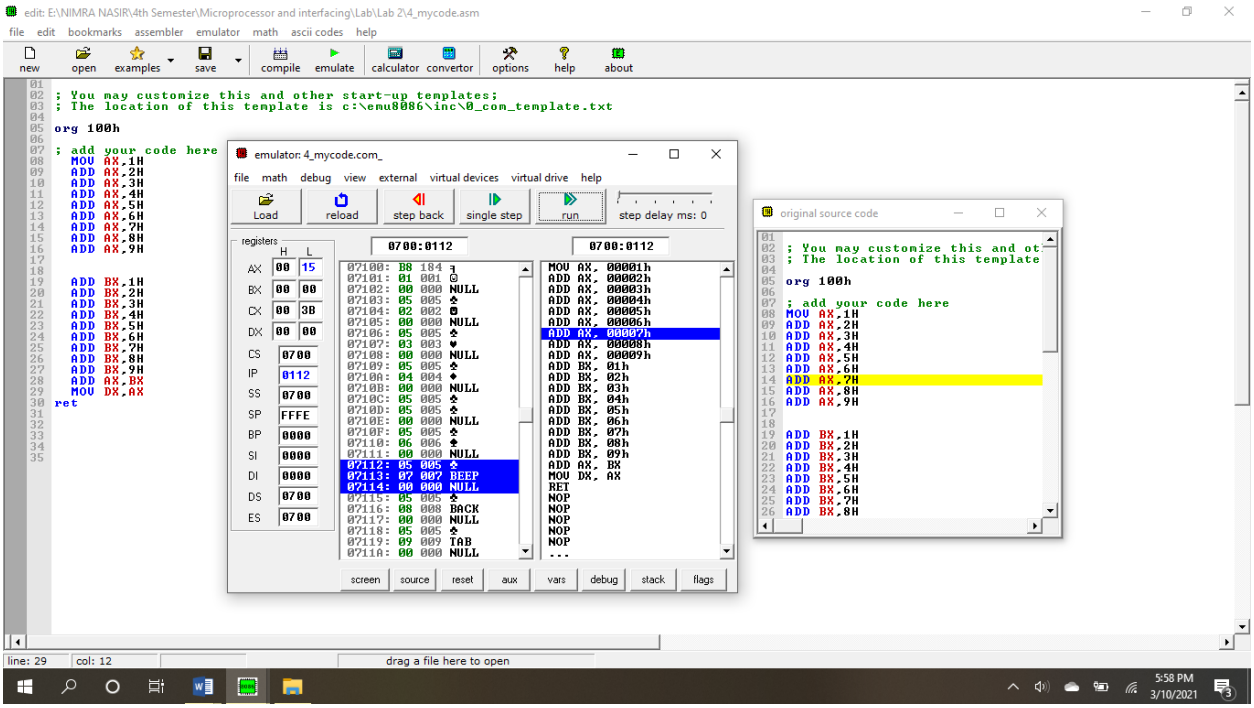
3/10/2021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ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
emulator: 4_mycode.com_
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CS 0700
IP 0112
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DI 0000
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ES 0700
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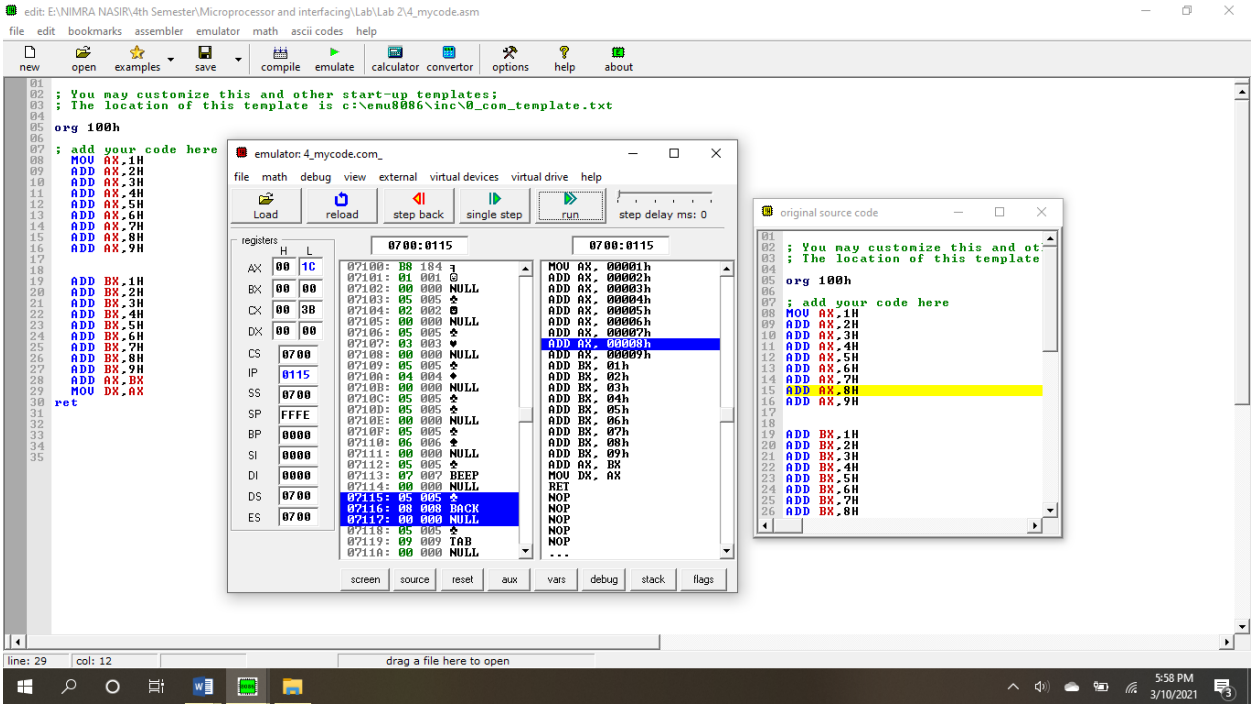
3/10/2021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ADD AX,3H
ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
emulator: 4_mycode.com_
file math debug view external virtual devices virtual drive help
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registers
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BX 00 00
CX 00 3B
DX 00 00
CS 0700
IP 0115
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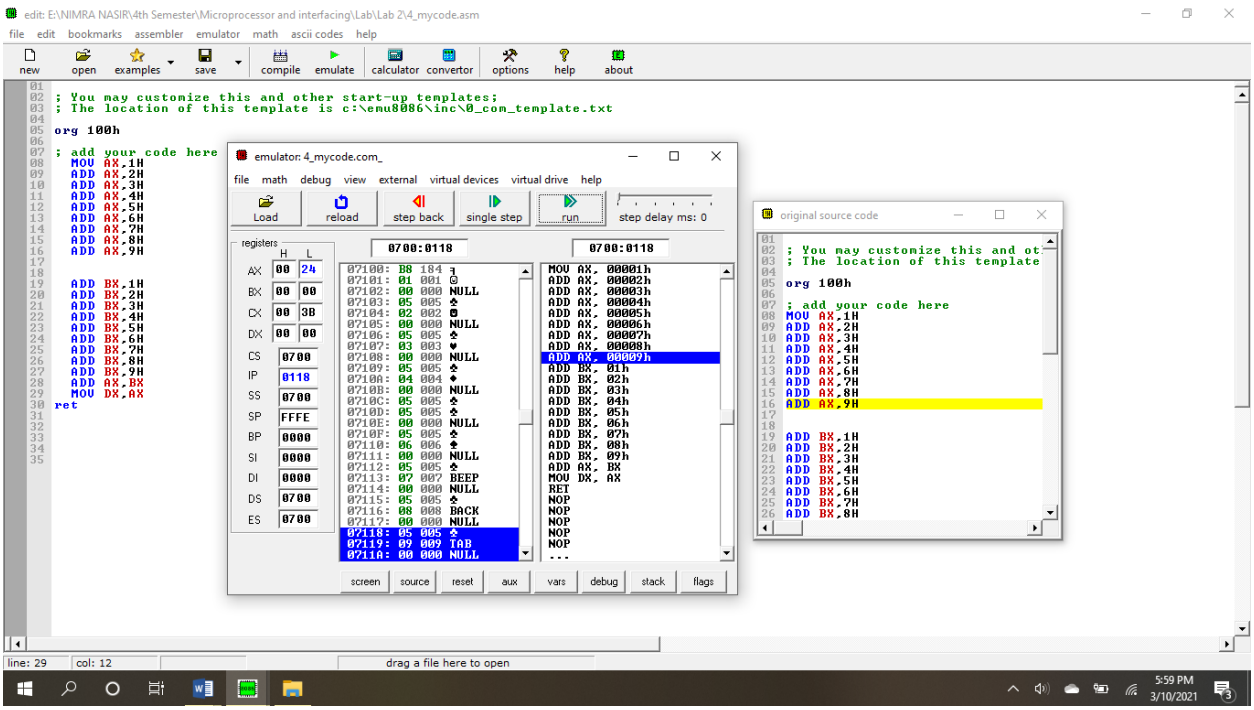
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ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
emulator: 4_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 24
BX 00 00
CX 00 3B
DX 00 00
CS 0700
IP 0118
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
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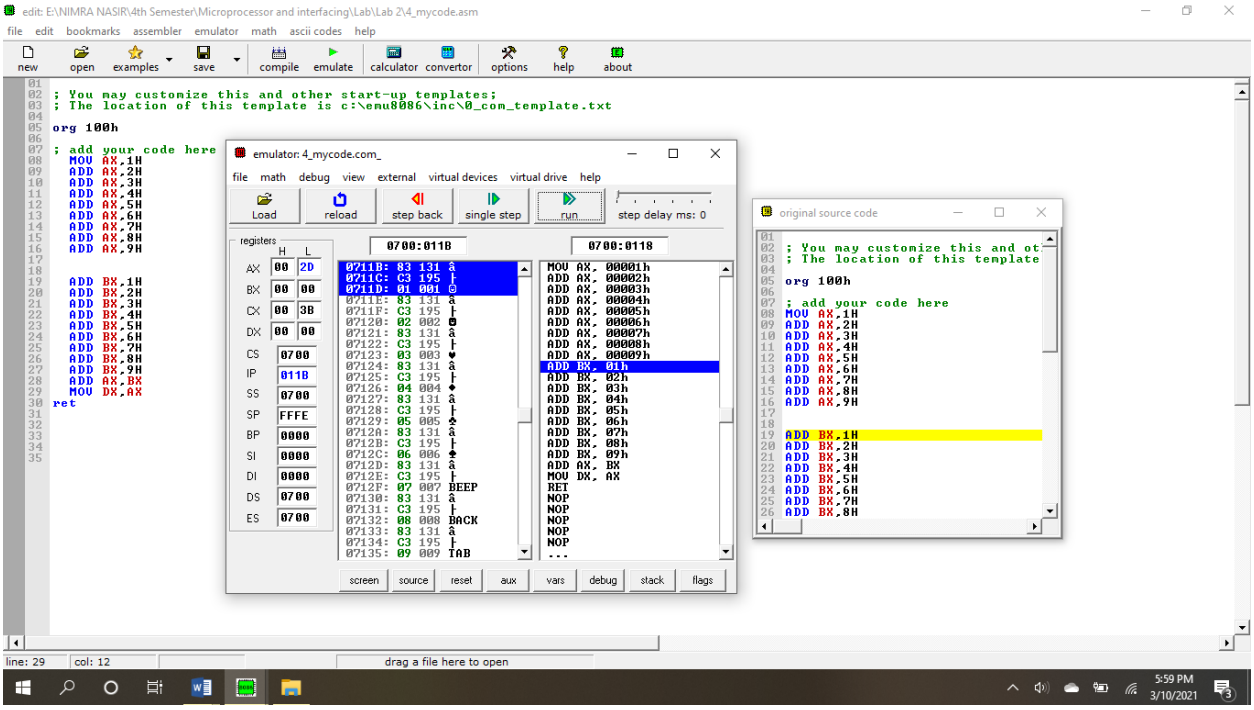
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ADD AX,3H
ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
emulator: 4_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 2D
BX 00 00
CX 00 3B
DX 00 00
CS 0700
IP 011B
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
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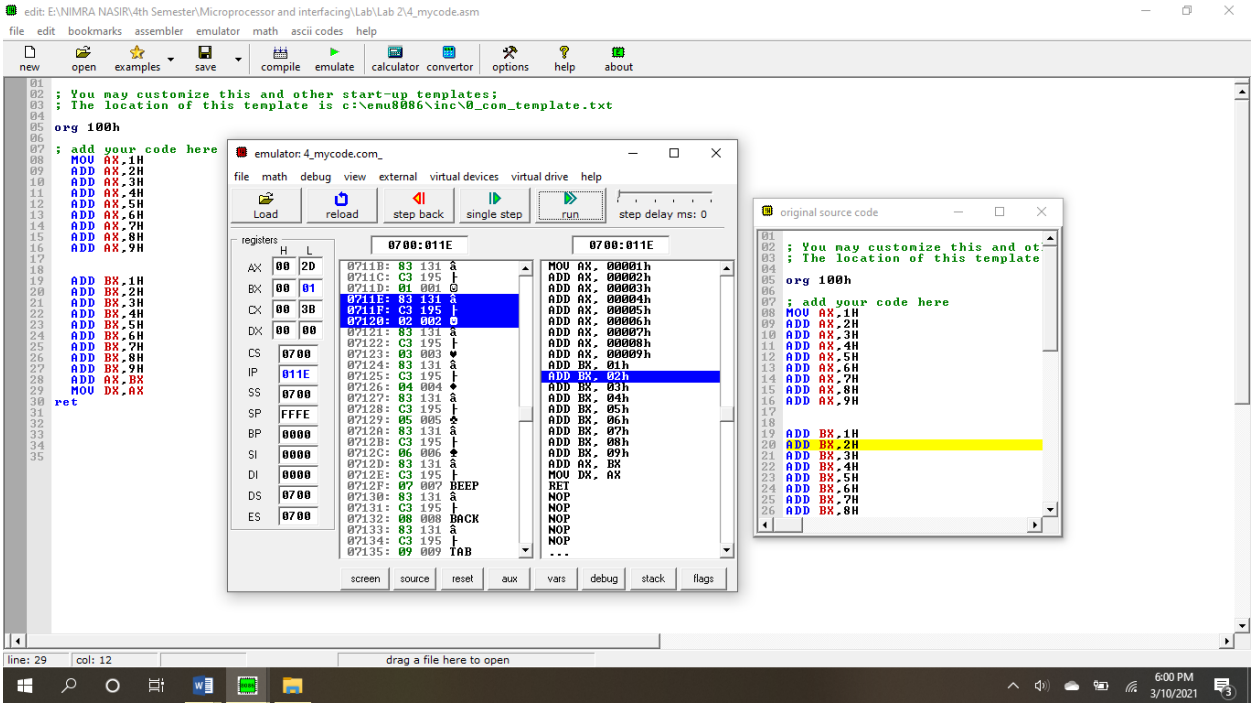
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MOV AX,1H
ADD AX,2H
ADD AX,3H
ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
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file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
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CX 00 3B
DX 00 00
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IP 011E
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BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
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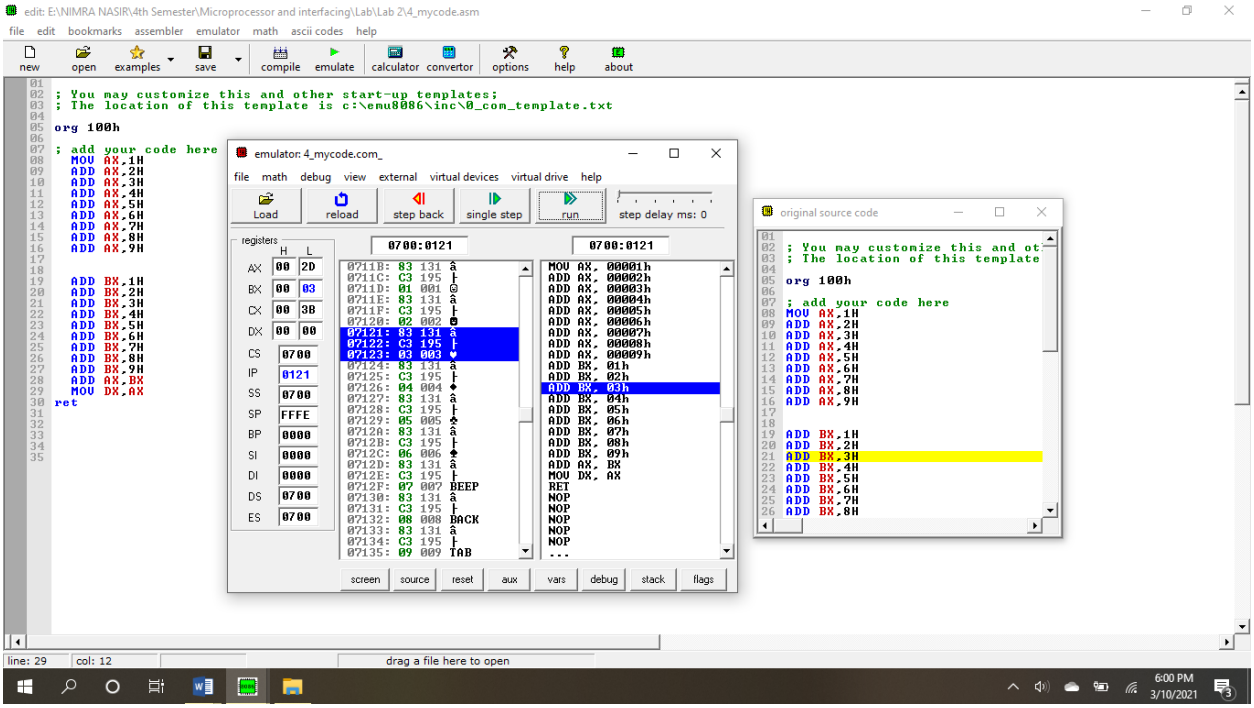
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MOV AX,1H
ADD AX,2H
ADD AX,3H
ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
emulator: 4_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 2D
BX 00 03
CX 00 3B
DX 00 00
CS 0700
IP 0121
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
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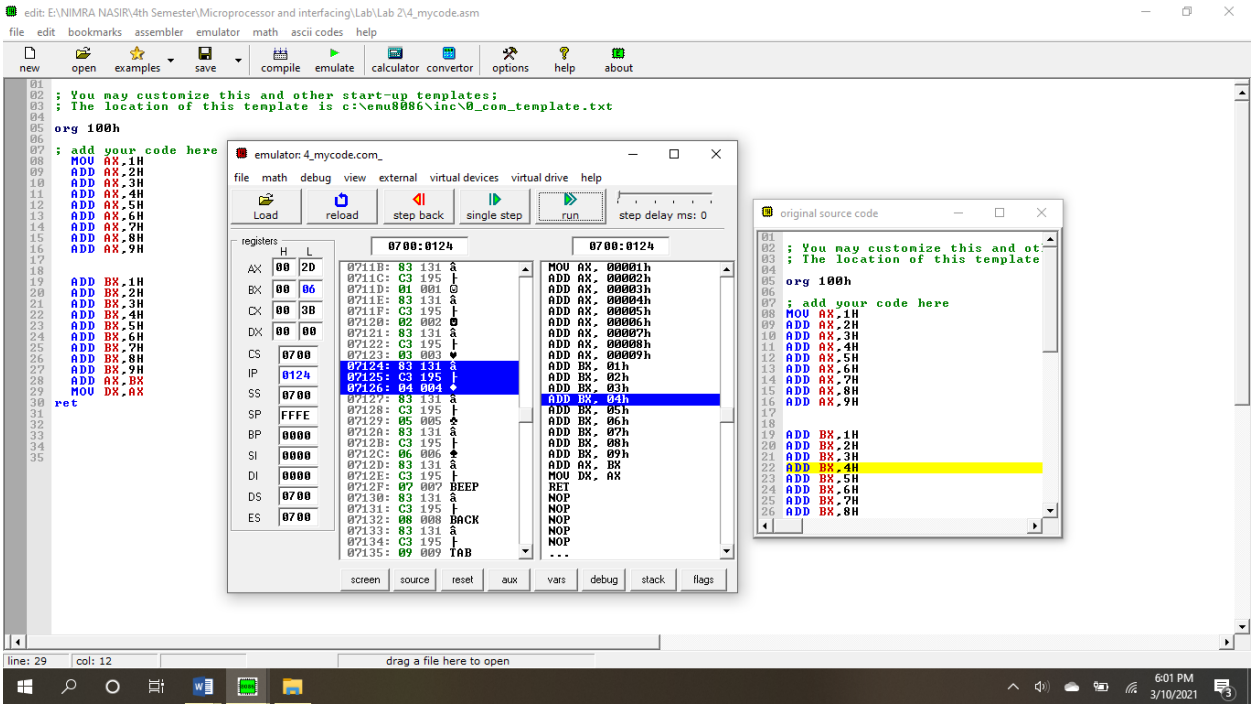
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MOV AX,1H
ADD AX,2H
ADD AX,3H
ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
emulator: 4_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 2D
BX 00 06
CX 00 3B
DX 00 00
CS 0700
IP 0124
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
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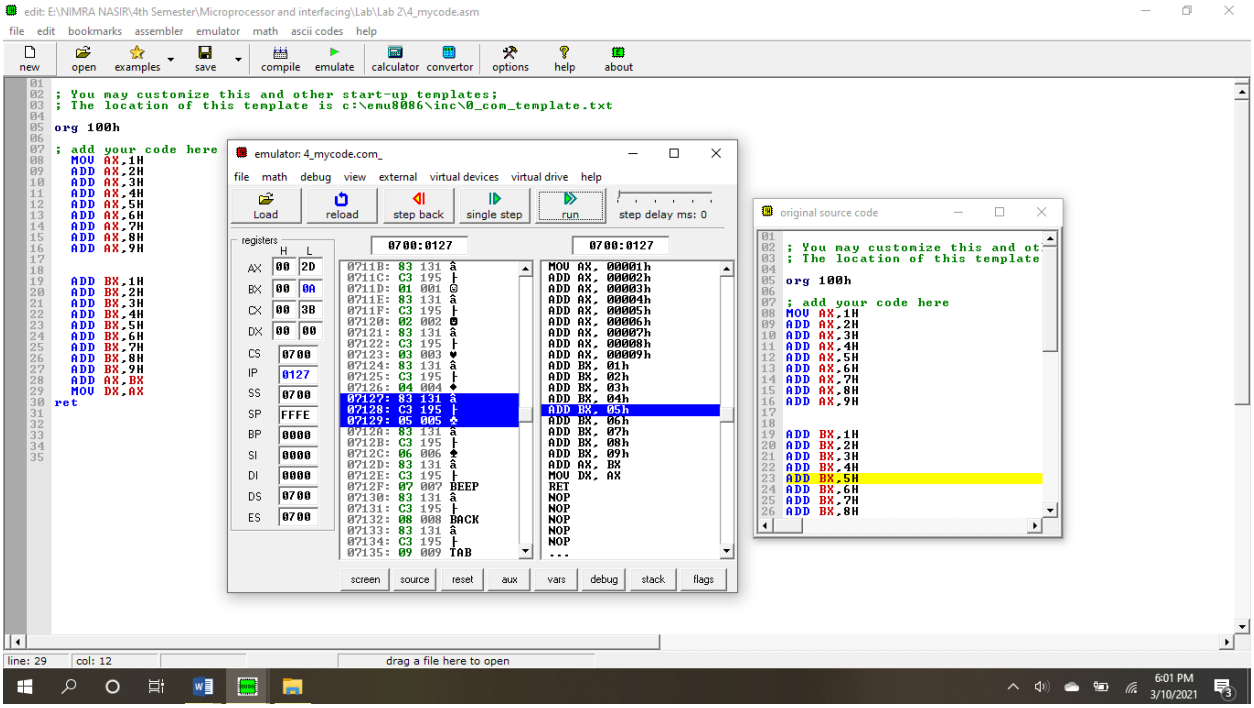
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MOV AX,1H
ADD AX,2H
ADD AX,3H
ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
emulator: 4_mycode.com_
file math debug view external virtual devices virtual drive help
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registers
AX 00 2D
BX 00 0A
CX 00 3B
DX 00 00
CS 0700
IP 0127
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
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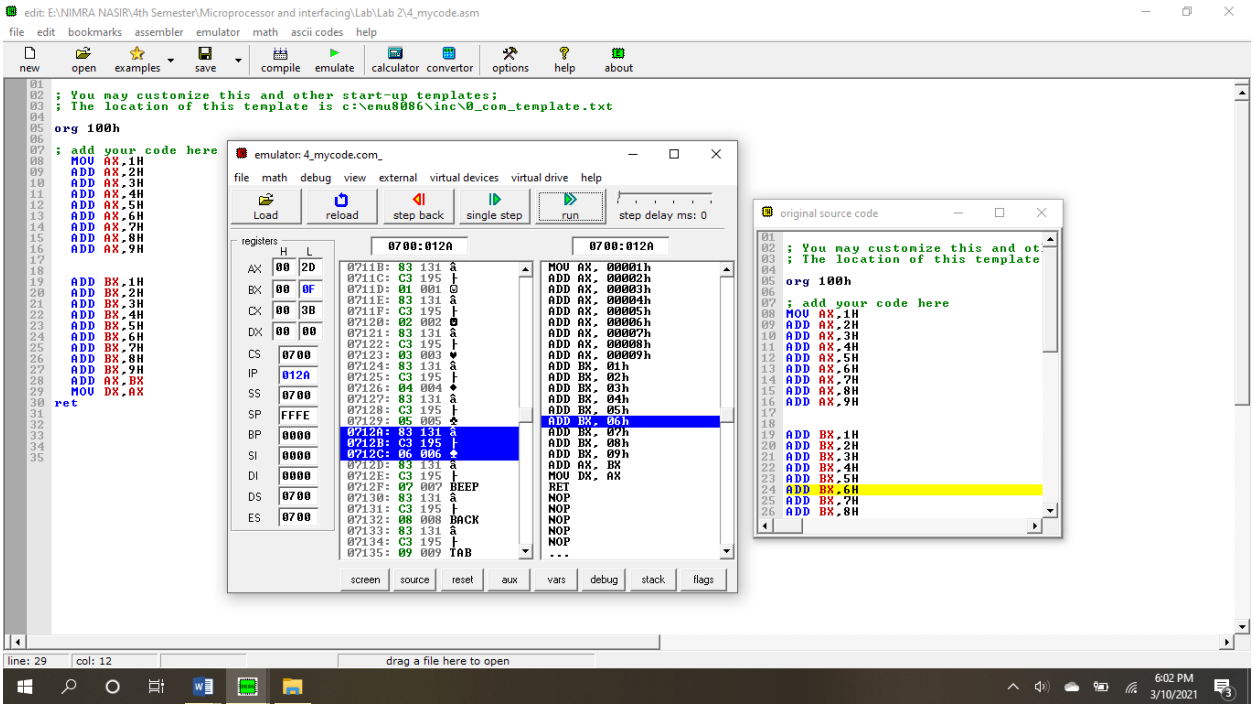
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ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
emulator: 4_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
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BX 00 0F
CX 00 3B
DX 00 00
CS 0700
IP 012A
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
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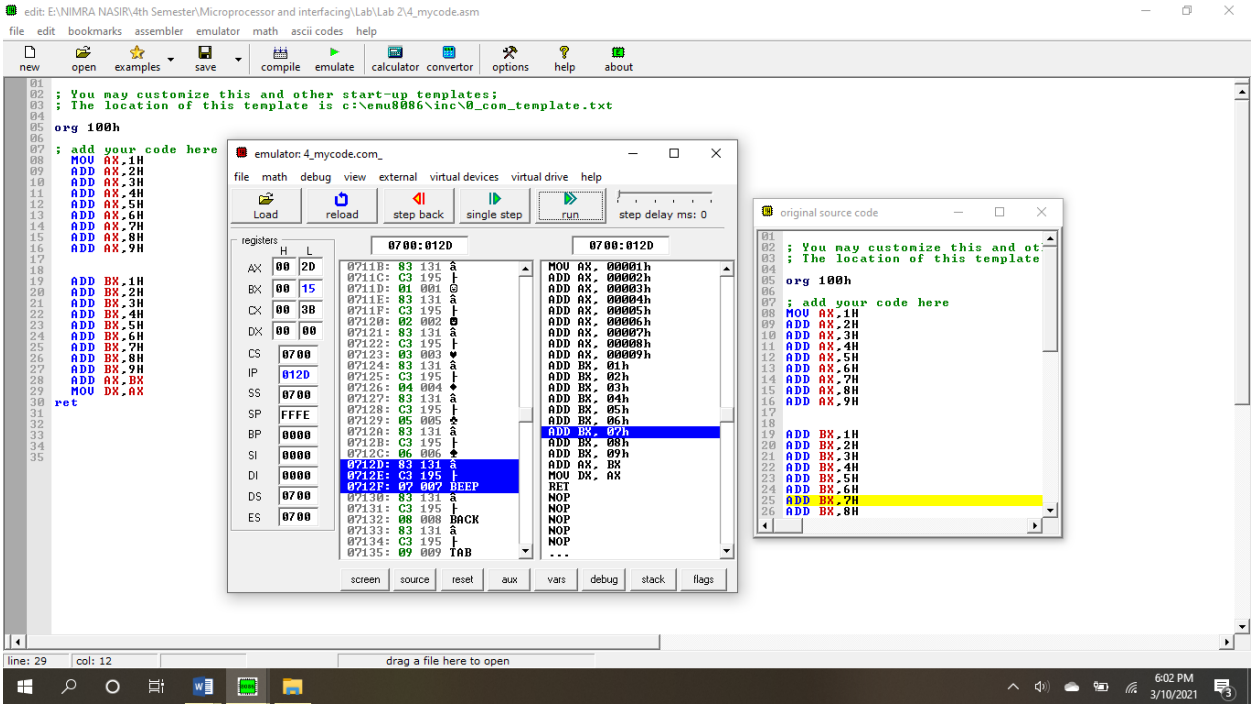
3/10/2021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ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
emulator: 4_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
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BX 00 15
CX 00 3B
DX 00 00
CS 0700
IP 012D
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
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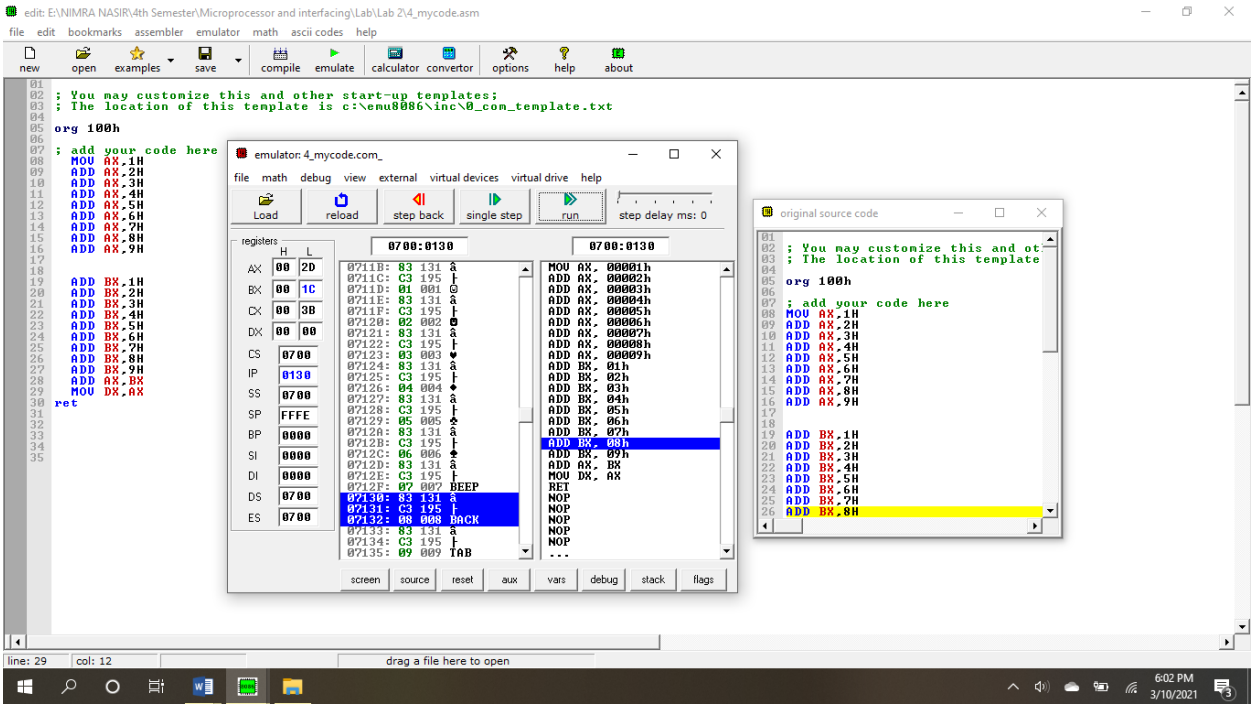
3/10/2021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ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
emulator: 4_mycode.com_
file math debug view external virtual devices virtual drive help
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registers
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BX 00 1C
CX 00 3B
DX 00 00
CS 0700
IP 0130
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
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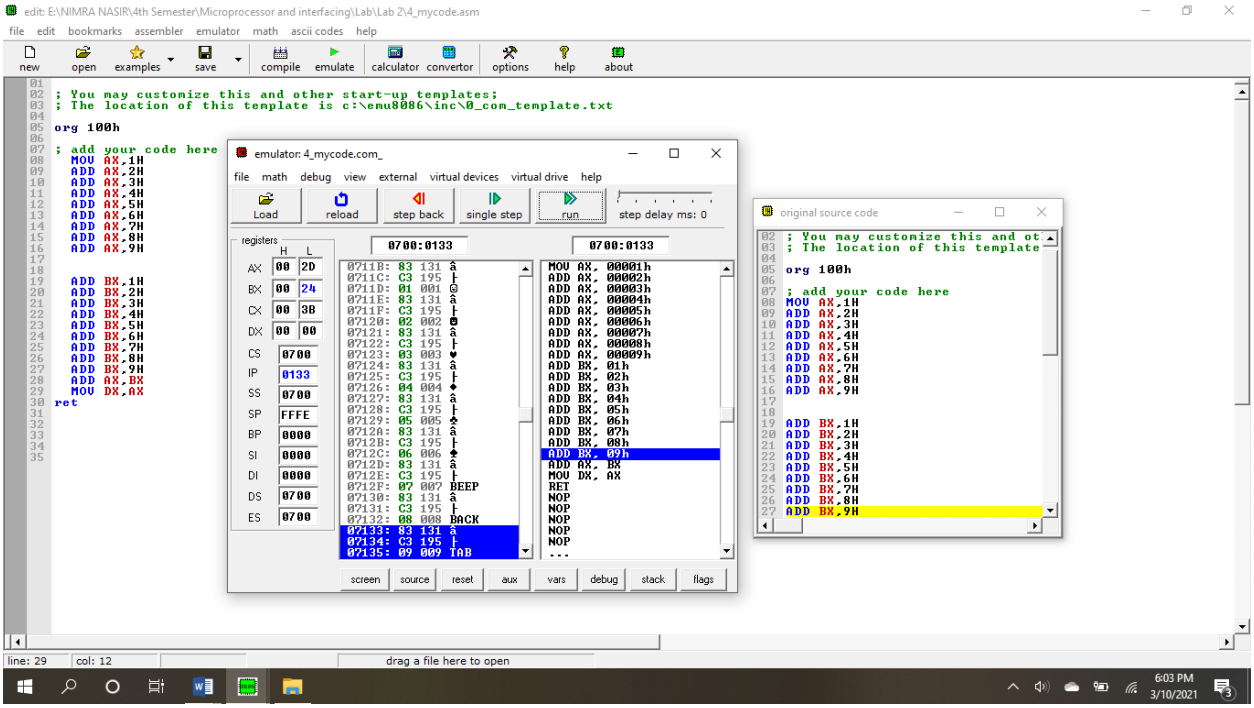
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ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
emulator: 4_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 2D
BX 00 24
CX 00 3B
DX 00 00
CS 0700
IP 0133
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
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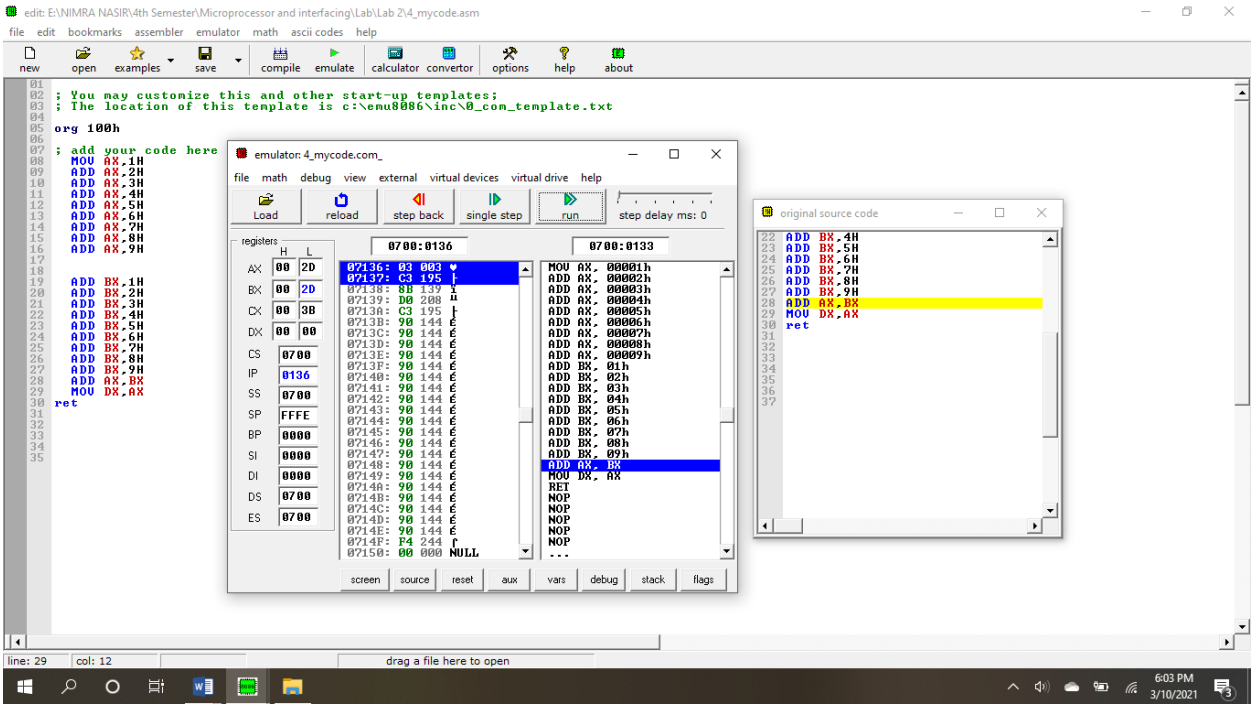
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ADD AX,3H
ADD AX,4H
ADD AX,5H
ADD AX,6H
ADD AX,7H
ADD AX,8H
ADD AX,9H
ADD BX,1H
ADD BX,2H
ADD BX,3H
ADD BX,4H
ADD BX,5H
ADD BX,6H
ADD BX,7H
ADD BX,8H
ADD BX,9H
ADD AX,BX
MOV DX,AX
ret
emulator: 4_mycode.com_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
AX 00 2D
BX 00 2D
CX 00 3B
DX 00 00
CS 0700
IP 0136
SS 0700
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0700
ES 0700
0700:0136
0700:0133
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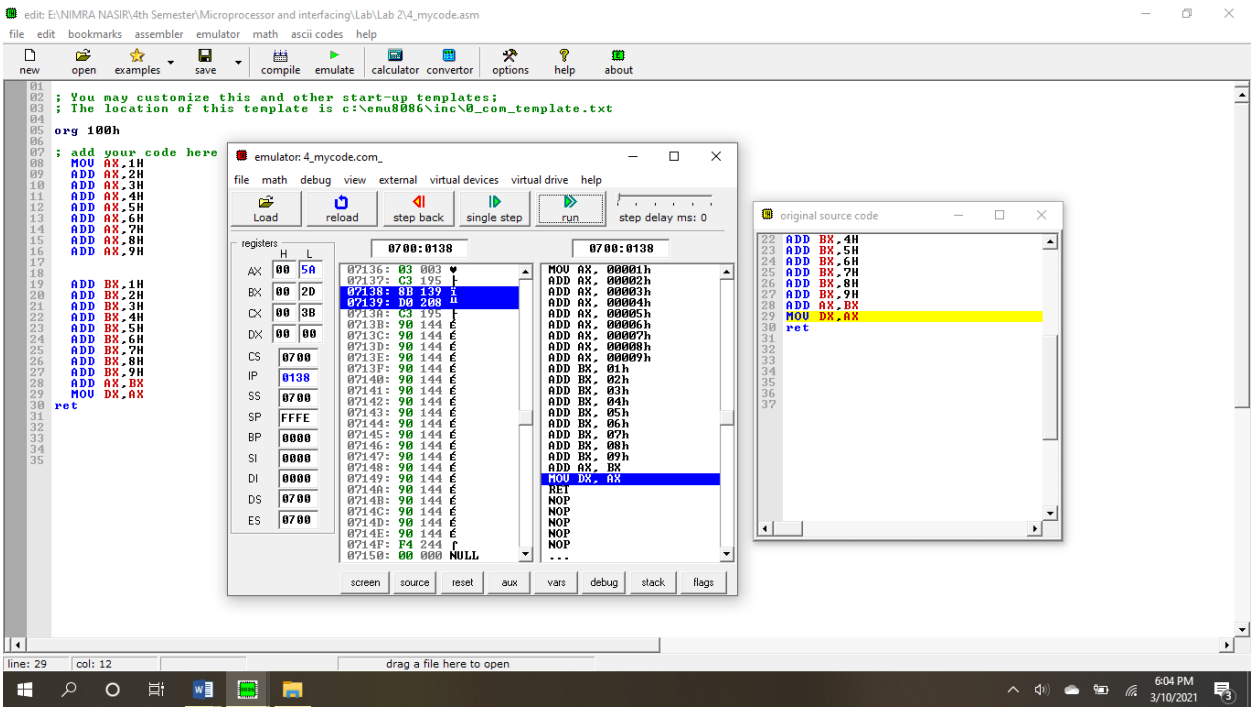
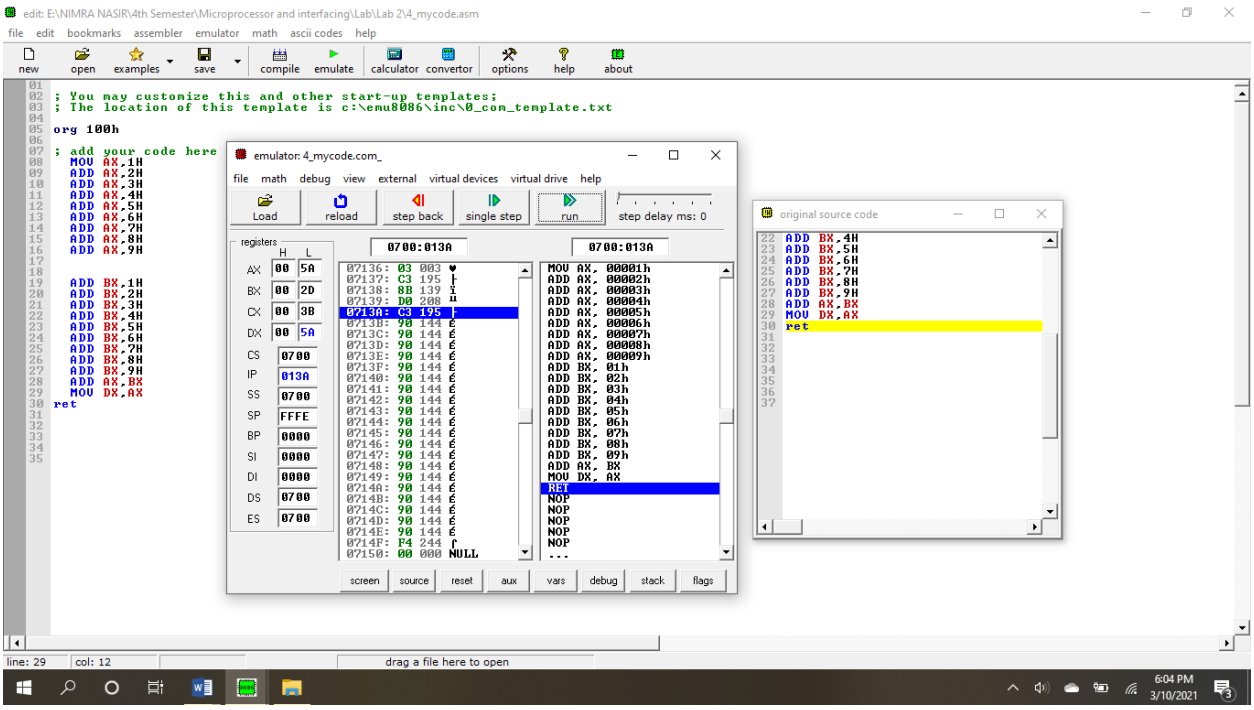
6:03 PM
3/10/2021

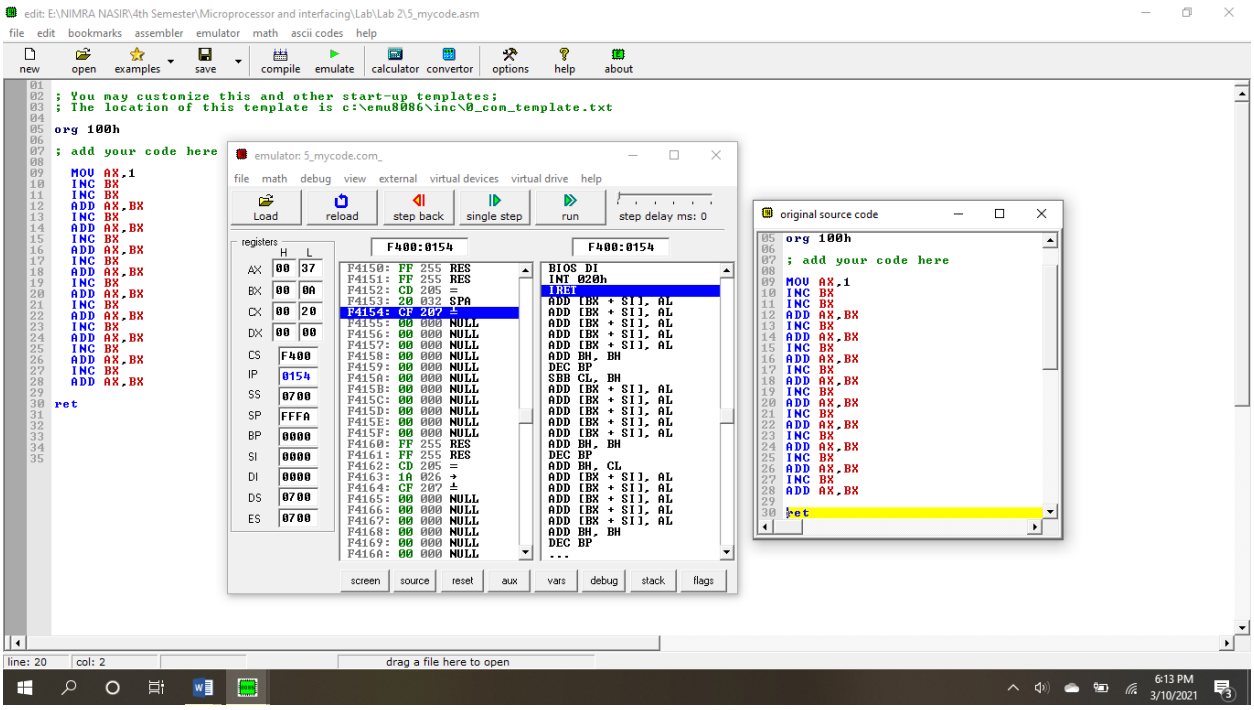




TASK: 5

Addition of first ten natural numbers by using INC and ADD instruction.

Output:



TASK: 6

X= (A+B) – (C+D)

Implement the following equation and place the result in accumulator register.