



U6  
XC6SLX25-FT256

# Amiga Interface

D5\_3V3 P11 IO\_L13N\_D10\_2  
/DS3\_3V3 N9 IO\_L14P\_D11\_2  
/CCS\_3V3 M9 IO\_L29P\_GCLK3\_2  
SENSE\_Z3\_3V3 P9 IO\_L14N\_D12\_2  
EZM\_3V3 T8 IO\_L30N\_GCLK0\_USERCLK\_2  
DOE\_3V3 N8 IO\_L29N\_GCLK2\_2  
/FCS\_3V3 P8 IO\_L30P\_GCLK1\_D13\_2  
/DS1\_3V3 P7 IO\_L31P\_GCLK3T\_D14\_2

D1\_3V3 L14 IO\_L47P\_FWE\_B\_M1DQ0\_1  
DIR\_1 L13 IO\_L53N\_VREF\_1  
D9\_3V3 M14 IO\_L74N\_DOUT\_BUSY\_1  
D2\_3V3 M13 IO\_L74P\_AWAKE\_1  
D8\_3V3 N14 IO\_L45P\_A1\_M1LDQS\_1  
D3\_3V3 M12 IO\_L2P\_CMPCLK\_2  
D7\_3V3 N12 IO\_L12P\_D1\_MISO2\_2  
D4\_3V3 P12 IO\_L12N\_D2\_MISO3\_2  
D6\_3V3 M11 IO\_L2N\_CMPMOSI\_2

A23\_3V3 F12 IO\_L30P\_A21\_M1RESET\_1  
DIR\_2 F14 IO\_L32N\_A16\_M1A9\_1  
D15\_3V3 G12 IO\_L38P\_A5\_M1CLK\_1  
D14\_3V3 G14 IO\_L36P\_A9\_M1BA0\_1  
D13\_3V3 H14 IO\_L39N\_M1ODT\_1  
D12\_3V3 J14 IO\_L43P\_GCLK5\_M1DQ4\_1  
D11\_3V3 J13 IO\_L41P\_GCLK9\_IRDY1\_M1RASN\_1  
D10\_3V3 J12 IO\_L40N\_GCLK10\_M1A6\_1  
D0\_3V3 K14 IO\_L41N\_GCLK8\_M1CASN\_1

A15\_3V3 F9 IO\_L40P\_0  
DIR\_3 F10 IO\_L64P\_SCP5\_0  
A16\_3V3 D11 IO\_L66P\_SCP1\_0  
A17\_3V3 E11 IO\_L64N\_SCP4\_0  
A18\_3V3 D14 IO\_L31P\_A19\_M1CKE\_1  
A19\_3V3 D12 IO\_L66N\_SCP0\_0  
A20\_3V3 E12 IO\_L1N\_A24\_VREF\_1  
A22\_3V3 E13 IO\_L1P\_A25\_1  
A21\_3V3 F13 IO\_L32P\_A17\_M1A8\_1

A8\_3V3 D8 IO\_L38P\_0  
A9\_3V3 E8 IO\_L36N\_GCLK14\_0  
A10\_3V3 D9 IO\_L40N\_0  
A11\_3V3 A10 IO\_L35N\_GCLK16\_0  
A12\_3V3 B10 IO\_L35P\_GCLK17\_0  
A13\_3V3 C10 IO\_L37N\_GCLK12\_0  
A14\_3V3 E10 IO\_L37P\_GCLK13\_0

FC0\_3V3 C11 IO\_L39P\_0  
FC1\_3V3 A11 IO\_L39N\_0  
FC2\_3V3 B12 IO\_L62P\_0  
/BERR\_3V3 A12 IO\_L62N\_VREF\_0  
/DS0\_3V3 C13 IO\_L63P\_SCP7\_0  
/RESET\_3V3 A13 IO\_L63N\_SCP6\_0  
READ\_3V3 A14 IO\_L65N\_SCP2\_0  
/DS2\_3V3 B14 IO\_L65P\_SCP3\_0

A5\_3V3 A4 IO\_L1N\_VREF\_0  
A6\_3V3 D5 IO\_L3P\_0  
A4\_3V3 C5 IO\_L3N\_0  
A3\_3V3 E6 IO\_L5N\_0  
A2\_3V3 C6 IO\_L7N\_0  
A7\_3V3 D6 IO\_L7P\_0  
A1\_3V3 B8 IO\_L33P\_0  
DIR\_4 A8 IO\_L33N\_0

/SLAVEN\_3V3 C3 IO\_L48P\_M3BA0\_3  
/CFGOUT\_3V3 B2 IO\_L52P\_M3A8\_3  
/CINH\_3V3 A2 IO\_L52N\_M3A9\_3  
XRDY\_3V3 B3 IO\_L83P\_3  
/DTACK\_3V3 A3 IO\_L83N\_VREF\_3  
/CFGIN\_3V3 G5 IO\_L51N\_M3A4\_3

# GPIOs

PORTD7 L5 IO\_L45N\_M3ODT\_3  
PORTD6 L4 IO\_L45P\_M3A3\_3  
PORTD5 L3 IO\_L36P\_M3DQ8\_3  
PORTD4 L1 IO\_L36N\_M3DQ9\_3  
PORTA4 D3 IO\_L49P\_M3A7\_3  
PORTA3 C2 IO\_L48N\_M3BA1\_3  
PORTB0 L12 IO\_L53P\_1  
PORTA1 K12 IO\_L42P\_GCLK7\_M1UDM\_1  
PORTB1 M10 IO\_L16N\_VREF\_2  
PORTB4 L10 IO\_L16P\_2  
PORTA0 J11 IO\_L40P\_GCLK11\_M1A5\_1  
PORTA2 K11 IO\_L42N\_GCLK6\_TRDY1\_M1LDM\_1  
PORTB2 E3 IO\_L54N\_M3A11\_3  
PORTB3 E2 IO\_L49N\_M3A2\_3  
PORTB5 E1 IO\_L46P\_M3CLK\_3  
PORTC3 E4 IO\_L54P\_M3RESET\_3  
PORTC4 F4 IO\_L53P\_M3CKE\_3  
PORTC5 F5 IO\_L55N\_M3A14\_3  
PORTD3 G3 IO\_L40P\_M3DQ6\_3  
PORTD2 F3 IO\_L53N\_M3A12\_3  
PORTE7 G1 IO\_L40N\_M3DQ7\_3  
PORTD0 H3 IO\_L44N\_GCLK20\_M3A6\_3  
PORTC2 H1 IO\_L39N\_M3LDQSN\_3  
PORTC1 H2 IO\_L39P\_M3LDQS\_3  
PORTC0 J1 IO\_L38N\_M3DQ3\_3

# Not Used

H13 IO\_L39P\_M1A3\_1  
H11 IO\_L38N\_A4\_M1CLKN\_1  
G11 IO\_L30N\_A20\_M1A11\_1  
F7 IO\_L5P\_0  
F1 IO\_L41N\_GCLK26\_M3DQ5\_3  
L11 CMPCS\_B\_2  
F2 IO\_L41P\_GCLK27\_M3DQ4\_3  
K2 IO\_L37P\_M3DQ0\_3  
J3 IO\_L38P\_M3DQ2\_3  
L8 IO\_L62P\_D5\_2  
L7 IO\_L62N\_D6\_2  
J6 IO\_L43P\_GCLK23\_M3RASN\_3  
K5 IO\_L47P\_M3A0\_3  
M5 IO\_L2P\_3  
M4 IO\_L1P\_3  
K6 IO\_L47N\_M3A1\_3  
P6 IO\_L47P\_2  
N4 IO\_L2N\_3  
K1 IO\_L37N\_M3DQ1\_3

# POWER & GROUND

|         |        |         |          |         |          |
|---------|--------|---------|----------|---------|----------|
| GND A1  | GND@19 | 3V3 D2  | VCCO_3@3 | 1V2 G7  | VCCINT@5 |
| GND T1  | GND@22 | 3V3 J2  | VCCO_3@4 | 1V2 J7  | VCCINT@1 |
| GND G2  | GND@18 | 3V3 N2  | VCCO_3@5 | 1V2 H8  | VCCINT@4 |
| GND L2  | GND@20 | 3V3 B4  | VCCO_0@2 | 1V2 K8  | VCCINT@2 |
| GND P3  | GND@24 | 3V3 G4  | VCCO_3@2 | 1V2 G9  | VCCINT@3 |
| GND D4  | GND@12 | 3V3 K4  | VCCO_3@1 | 1V2 J9  | VCCINT@8 |
| GND J5  | GND@16 | 3V3 R4  | VCCO_2@3 | 1V2 H10 | VCCINT@6 |
| GND R6  | GND@13 | 3V3 E5  | VCCAUX@1 | 1V2 K10 | VCCINT@7 |
| GND H7  | GND@5  | 3V3 H6  | VCCAUX@7 |         |          |
| GND B7  | GND@14 | 3V3 L6  | VCCAUX@8 |         |          |
| GND H7  | GND@3  | 3V3 D7  | VCCO_0@5 |         |          |
| GND K7  | GND@15 | 3V3 F8  | VCCO_2@1 |         |          |
| GND G8  | GND@9  | 3V3 R8  | VCCAUX@5 |         |          |
| GND J8  | GND@25 | 3V3 B9  | VCCO_2@4 |         |          |
| GND M8  | GND@11 | 3V3 G9  | VCCO_0@1 |         |          |
| GND E9  | GND@23 | 3V3 L9  | VCCAUX@4 |         |          |
| GND H9  | GND@17 | 3V3 D10 | VCCO_0@3 |         |          |
| GND K9  | GND@6  | 3V3 G10 | VCCAUX@6 |         |          |
| GND R10 | GND@8  | 3V3 J10 | VCCAUX@3 |         |          |
| GND B11 | GND@7  | 3V3 N10 | VCCO_2@2 |         |          |
| GND H12 | GND@26 | 3V3 F11 | VCCAUX@2 |         |          |
| GND D13 | GND@21 | 3V3 B13 | VCCO_0@4 |         |          |
| GND N13 | GND@1  | 3V3 G13 | VCCO_1@2 |         |          |
| GND G15 | GND@2  | 3V3 K13 | VCCO_1@1 |         |          |
| GND L15 | GND@10 | 3V3 R13 | VCCO_1@6 |         |          |
| GND A16 | GND@4  | 3V3 D15 | VCCO_1@5 |         |          |
| GND T16 |        | 3V3 J15 | VCCO_1@5 |         |          |
|         |        | 3V3 N15 | VCCO_1@3 |         |          |

# CONFIG

REBOOT T2 PROGRAM\_B\_2  
FPGA-INIT# R3 IO\_L65P\_INIT\_B\_2  
FPGA-DONE P13 DONE\_2  
3V3 T11 IO\_L1N\_M0\_CMPMISO\_2  
GND N11 IO\_L13P\_M1\_2  
GND C4 IO\_L1P\_HSWAPEN\_0  
GND P14 SUSPEND

# JTAG

TCK C14 TCK  
TMS A15 TMS  
TDI C12 TDI  
TDO E14 TDO

# FLASH

FLASH-CS# T3 IO\_L65N\_CSO\_B\_2  
FPGA-CCLK R11 IO\_L1P\_CCLK\_2  
FPGA-MOSI T10 IO\_L3N\_MOSI\_CSI\_B\_MISO0\_2  
FPGA-MISO P10 IO\_L3P\_D0\_DI\_N\_MISO\_MISO1\_2

# micro SD Card

\_SD\_DAT0 C1 IO\_L50P\_M3WE\_3  
\_SD\_DAT1 B1 IO\_L50N\_M3BA2\_3  
\_SD\_DAT2 H4 IO\_L44P\_GCLK2T\_M3A5\_3  
\_SD\_CD/DAT3 H5 IO\_L43N\_GCLK22\_IRDY2\_M3CASN\_3  
\_SD\_CMD G6 IO\_L51P\_M3A10\_3  
\_SD\_CLK F6 IO\_L55P\_M3A13\_3

\_FDID\_SCI E7 IO\_L36P\_GCLK15\_0  
\_FDID\_SDA C8 IO\_L38N\_VREF\_0

# SDRAM

IO\_L35P\_A11\_M1A7\_1 F15 UDQM  
IO\_L36N\_A8\_M1BA1\_1 G16 SDRAM-CLK  
IO\_L37N\_A6\_M1A1\_1 H16 CKE  
IO\_L37P\_A7\_M1A0\_1 H15 A12  
IO\_L43N\_GCLK4\_M1DQ5\_1 J16 A11  
IO\_L44N\_A2\_M1DQ7\_1 K16 A9  
IO\_L44P\_A3\_M1DQ6\_1 K15 A8  
IO\_L47N\_LDC\_M1DQ1\_1 L16 A7  
IO\_L46N\_FOE\_B\_M1DQ3\_1 M16 A6  
IO\_L46P\_FCS\_B\_M1DQ2\_1 M15 A5  
IO\_L45N\_A0\_M1LDQSN\_1 N16 A4  
IO\_L48N\_M1DQ9\_1 P16 A3  
IO\_L48P\_HDC\_M1DQ8\_1 P15 A2  
IO\_L49N\_M1DQ11\_1 R16 A1  
IO\_L50N\_M1UDQSN\_1 T15 A0  
IO\_L49P\_M1DQ10\_1 R15 A10  
IO\_L51P\_M1DQ12\_1 T14 BS1  
IO\_L50P\_M1UDQS\_1 R14 BS0  
IO\_L32N\_M3DQ15\_3 R2 RAS#  
IO\_L32P\_M3DQ14\_3 T4 CAS#  
IO\_L63N\_2 R5 WE#  
IO\_L48P\_D7\_2 T5 LDQM  
IO\_L48N\_RDWR\_B\_VREF\_2 T5 D15  
IO\_L33P\_A15\_M1A10\_1 C15 D14  
IO\_L33N\_A14\_M1A4\_1 C16 D13  
IO\_L29P\_A23\_M1A13\_1 B15 D12  
IO\_L29N\_A22\_M1A14\_1 B16 D11  
IO\_L31N\_A18\_M1A12\_1 D16 D10  
IO\_L34N\_A12\_M1BA2\_1 E16 D9  
IO\_L34P\_A13\_M1WE\_1 E15 D8  
IO\_L35N\_A10\_M1A2\_1 F16 D7  
IO\_L47N\_2 T6 D6  
IO\_L32P\_GCLK29\_2 R7 D5  
IO\_L32N\_GCLK28\_2 T7 D4  
IO\_L23P\_2 R9 D3  
IO\_L23N\_2 T9 D2  
IO\_L52P\_M1DQ14\_1 R12 D1  
IO\_L52N\_M1DQ15\_1 T12 D0  
IO\_L51N\_M1DQ13\_1 T13 D0

# FTDI

FTDI\_D0 M7 IO\_L31N\_GCLK30\_D15\_2  
FTDI\_D1 N6 IO\_L64N\_D9\_2  
FTDI\_D2 M6 IO\_L64P\_D8\_2  
FTDI\_D3 P5 IO\_L49N\_D4\_2  
FTDI\_D4 N5 IO\_L49P\_D3\_2  
FTDI\_D5 P4 IO\_L63P\_2  
FTDI\_D6 P2 IO\_L33P\_M3DQ12\_3  
FTDI\_D7 P1 IO\_L33N\_M3DQ13\_3  
FTDI\_RXF N3 IO\_L34P\_M3UDQS\_3  
FTDI\_TXF N1 IO\_L34N\_M3UDQSN\_3  
FTDI\_SIWUA M3 IO\_L1N\_VREF\_3  
FTDI\_WR M2 IO\_L35P\_M3DQ10\_3  
FTDI\_RD M1 IO\_L35N\_M3DQ11\_3

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