clc

SNR\_arry=0:3:18;

erro\_arry1=zeros(1,7);

for times=1:10

erro\_arry=ones(1,7);

number1=1;

for SNR=0:3:18

%Generation of bit pattern

sig1=round(rand(1,63));

sig2=round(rand(1,63));

sig3=round(rand(1,63));

sig4=round(rand(1,63));

signal1=[];

signal2=[];

signal3=[];

signal4=[];

for k=1:63

if sig1(1,k)==0

sig=- ones(1,120);

else

sig=ones(1,120);

end

signal1=[signal1 sig];

end

subplot(4,1,1);

plot (signal1);

end