

野火i.MX RT10xx核心板

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Title 野火i.MX RT10xx核心板

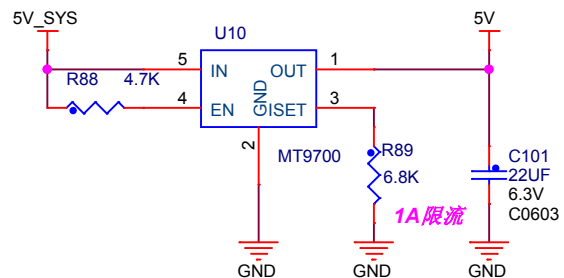
Size A4	Document Number 目录	Rev V1.0
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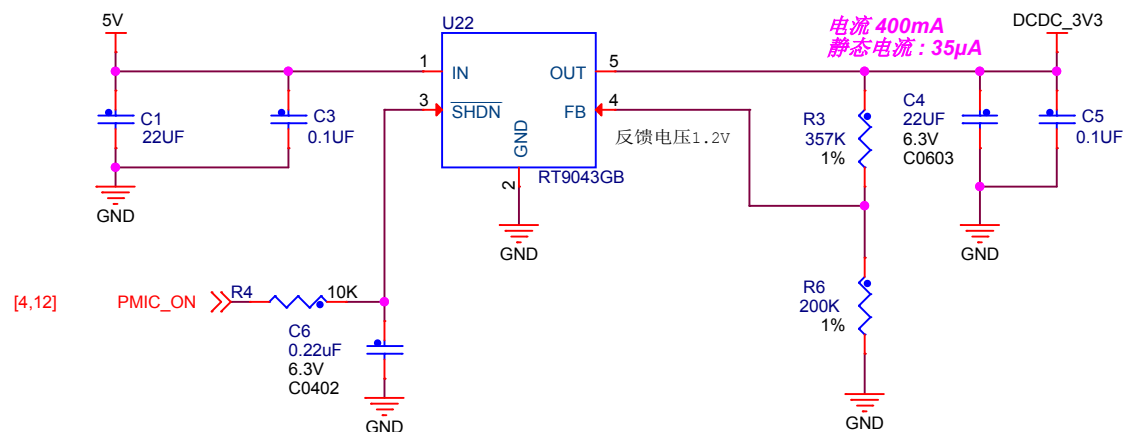
历史版本

版本号	日期	设计	描述
V0.1	2018-06-14	cancore	初始版本

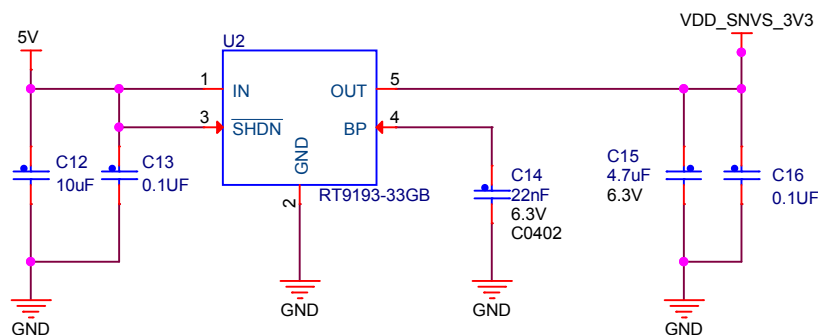
5V供电过流保护



3.3V



3V3 LDO for SNVS



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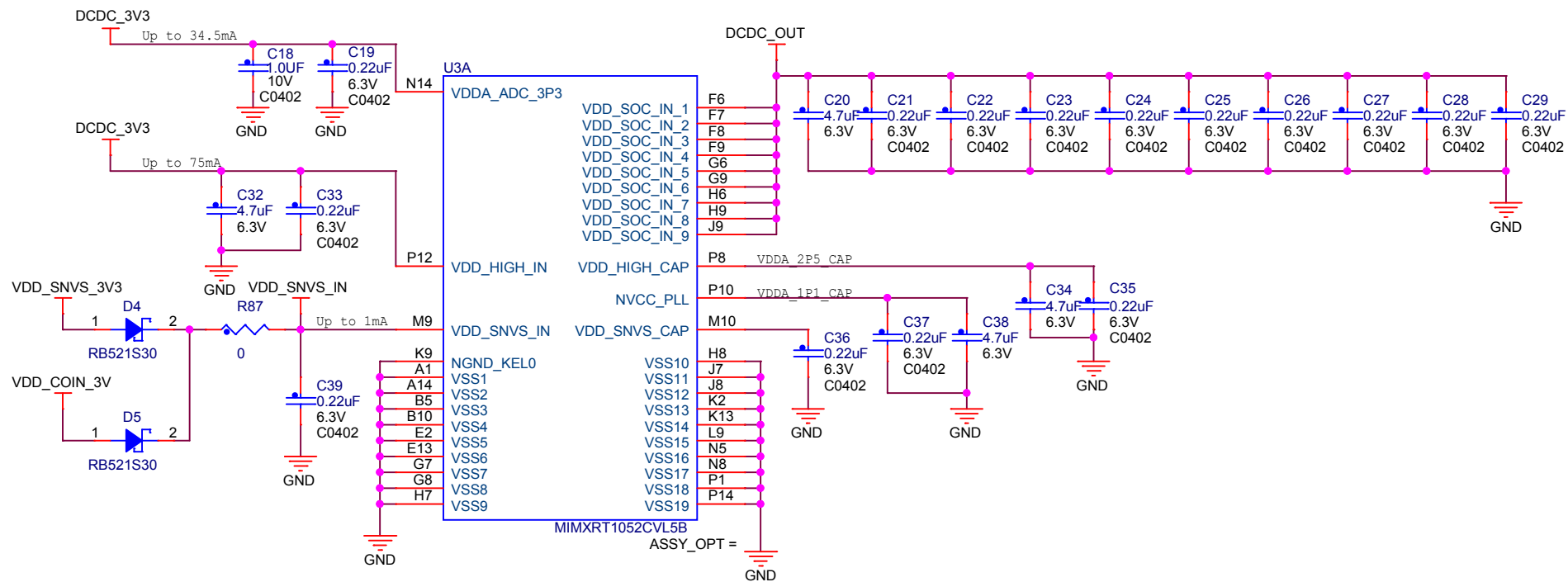
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Size A4	Document Number 主电源	Rev V1.0
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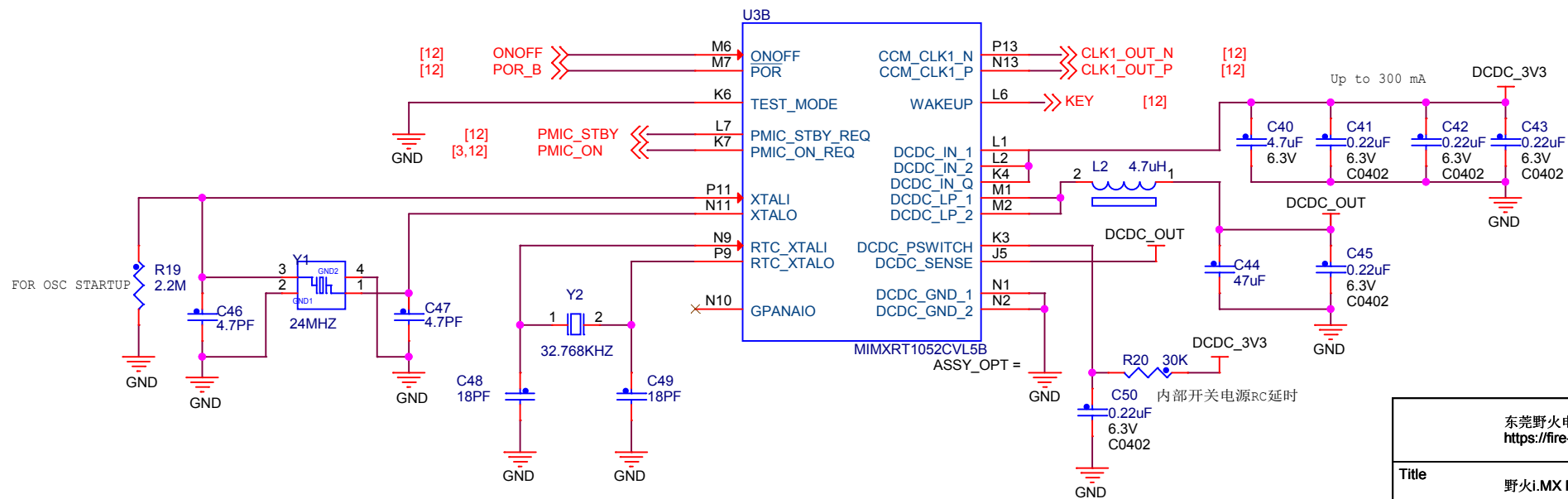
D

C



B

A



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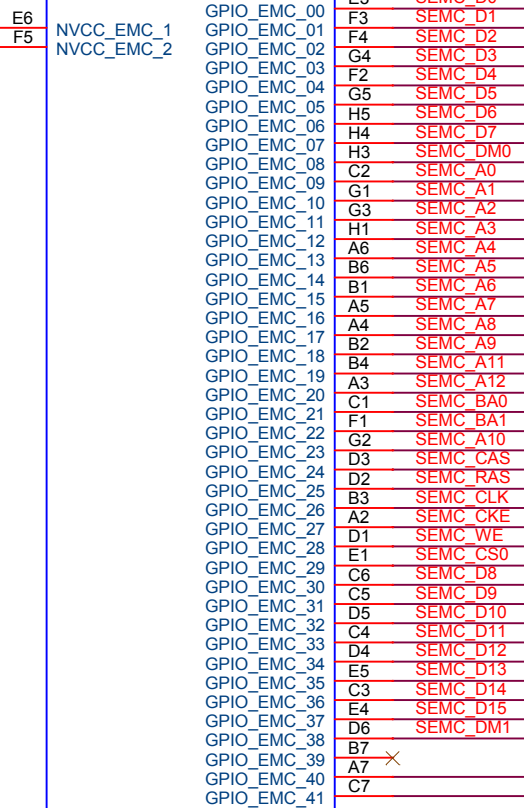
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A4	MCU电源	V1.0

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NVCC_SEMC_3V3

U3D

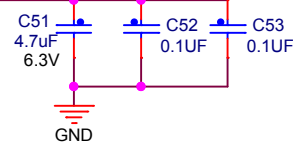


MIMXRT1052CVL5B

ASSY_OPT =

DCDC_3V3

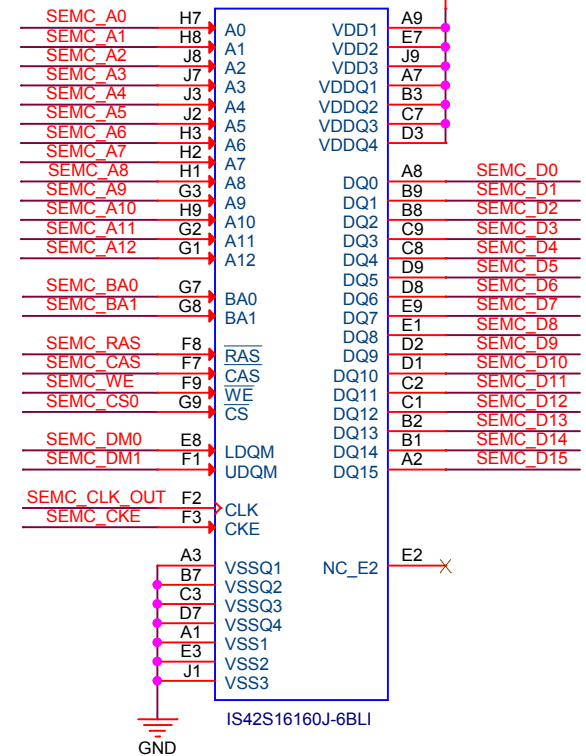
NVCC_SEMC_3V3



Layout:
1. 等长走线, 误差±50mil
2. 阻抗50ohm

SDRAM_3V3

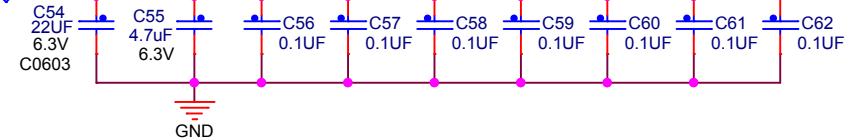
U20



DCDC_3V3

SDRAM_3V3

MAX 150 mA @3.3V



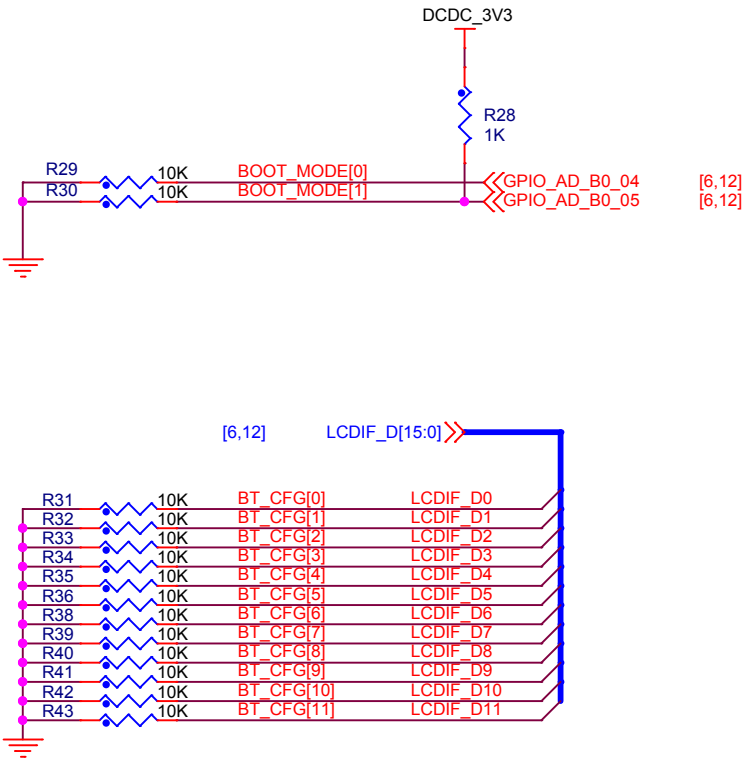
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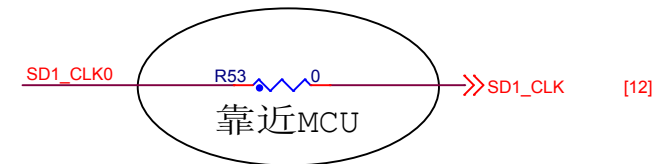
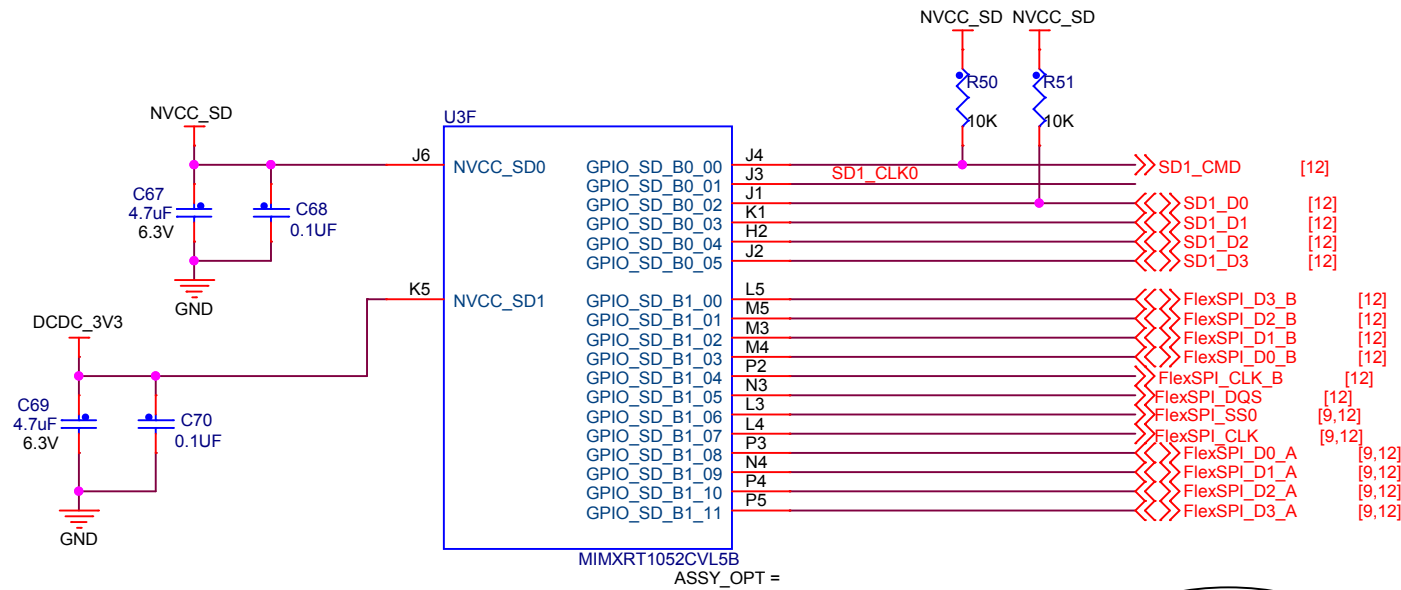
Size	Document Number	Rev
A4	SDRAM	V1.0

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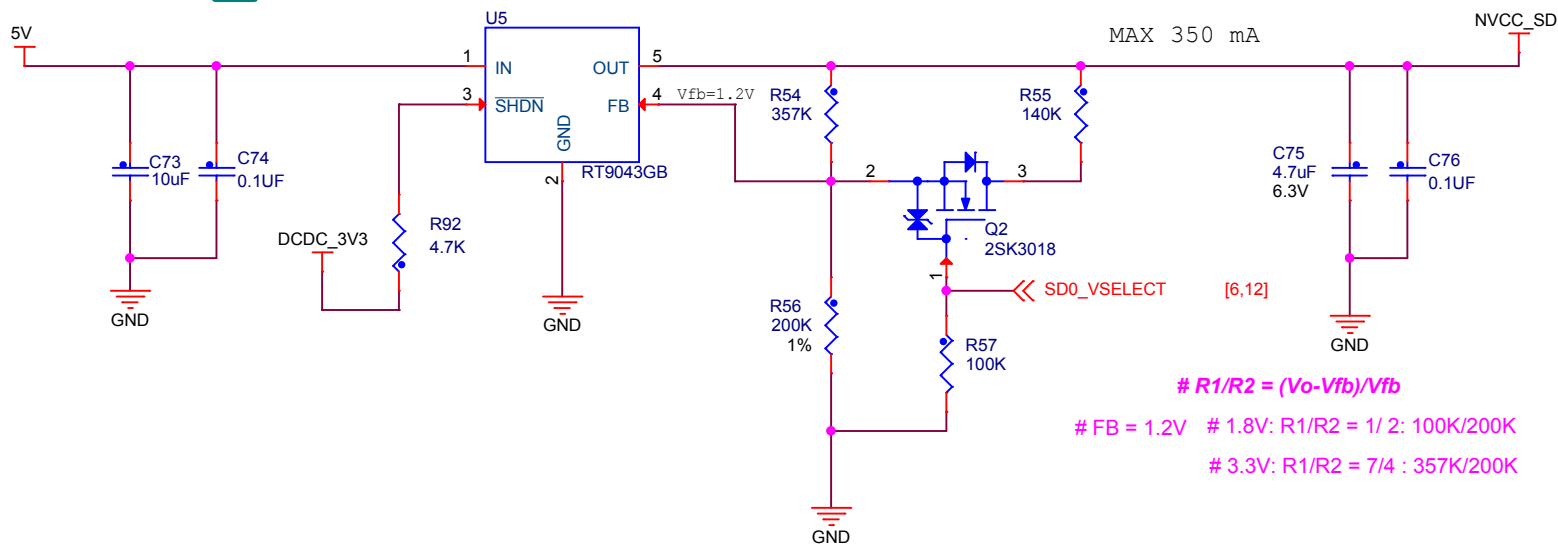
启动设置



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NVCC_SD <SD3.0>



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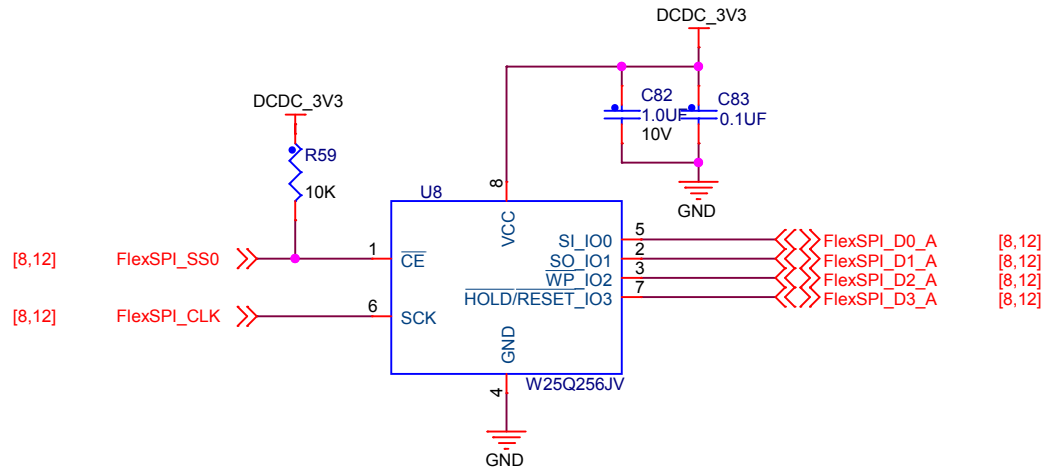
Title 野火i.MX RT1052核心板

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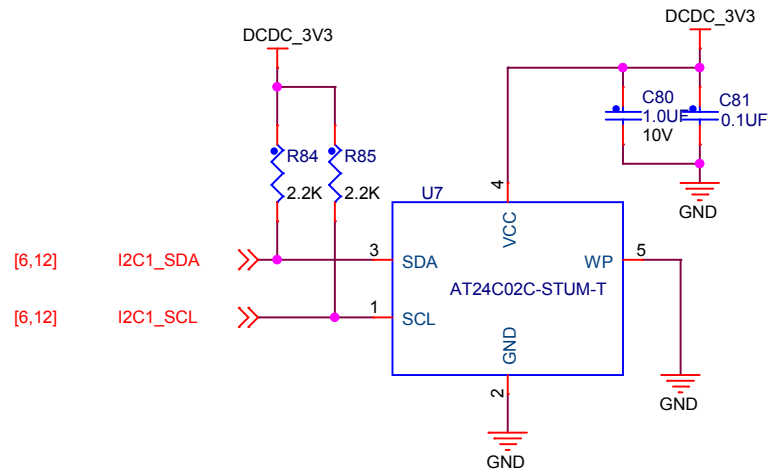
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QSPI Flash

Layout:
1等长走线



EEPROM

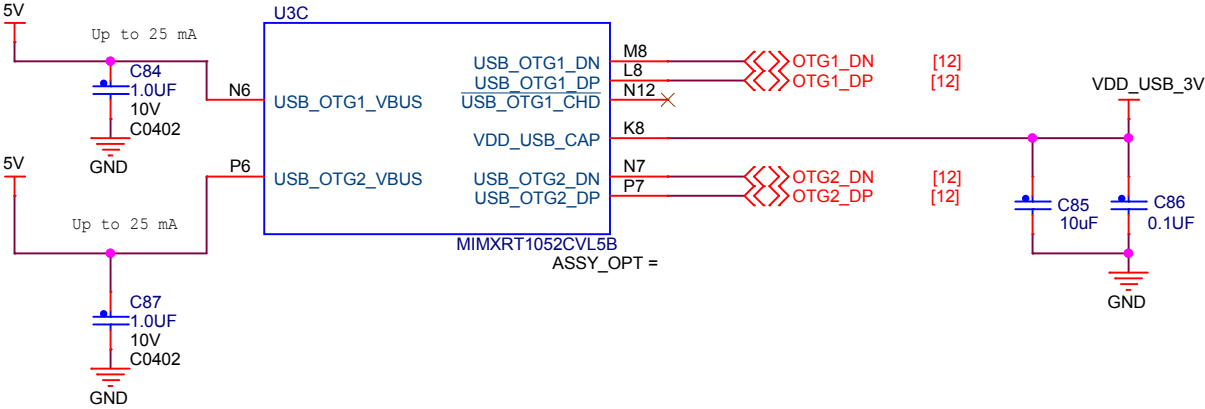


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A4	E2PROM	V1.0
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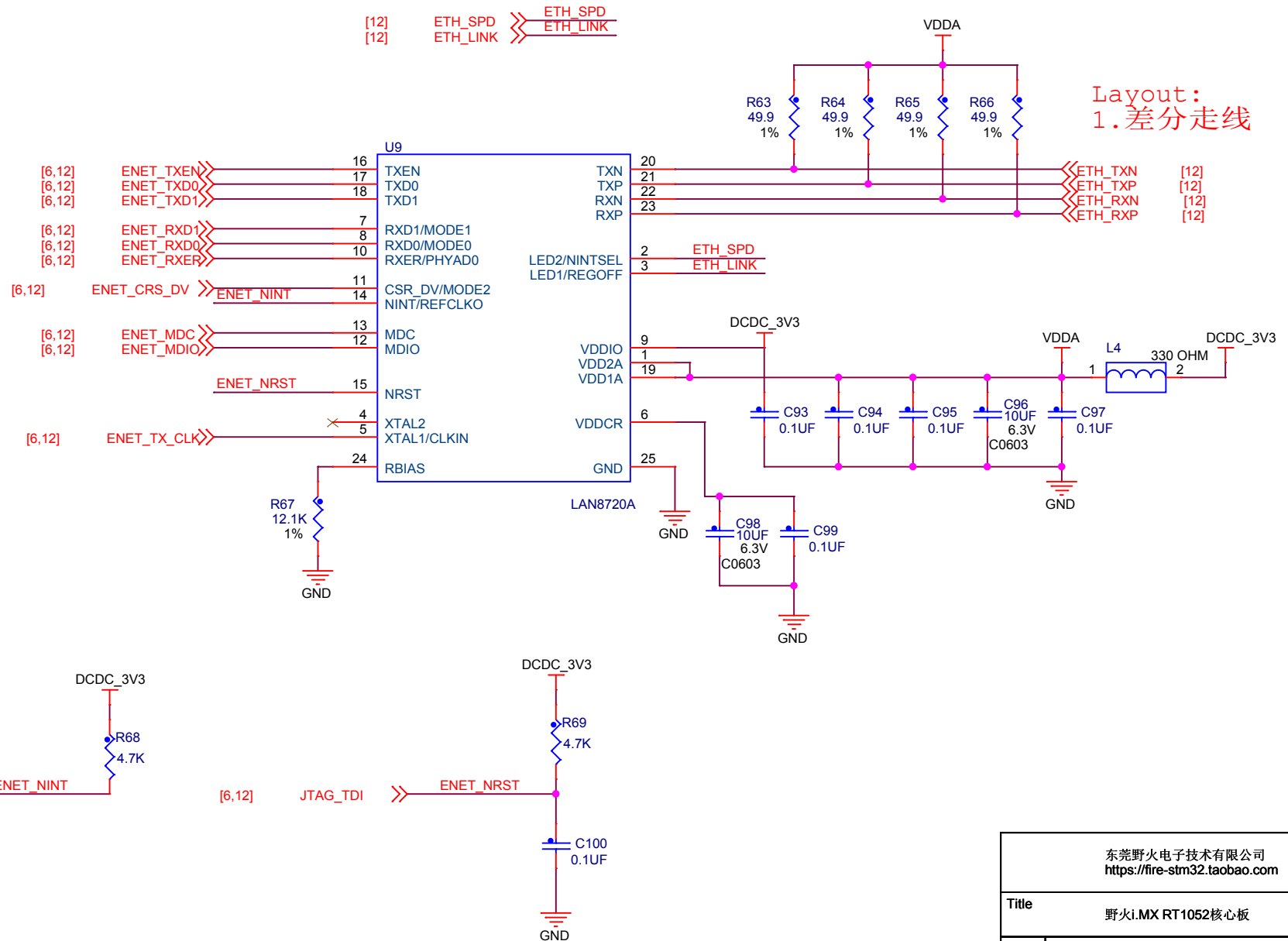
USB OTG

Layout:
1. 差分走线
2. 阻抗90ohm

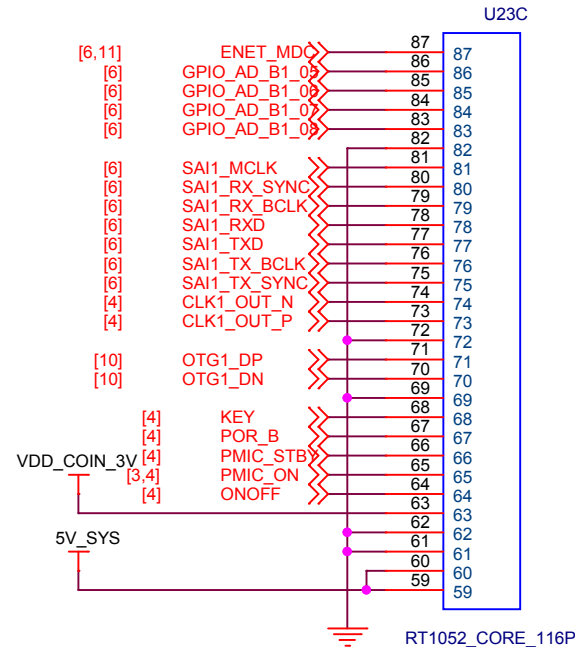
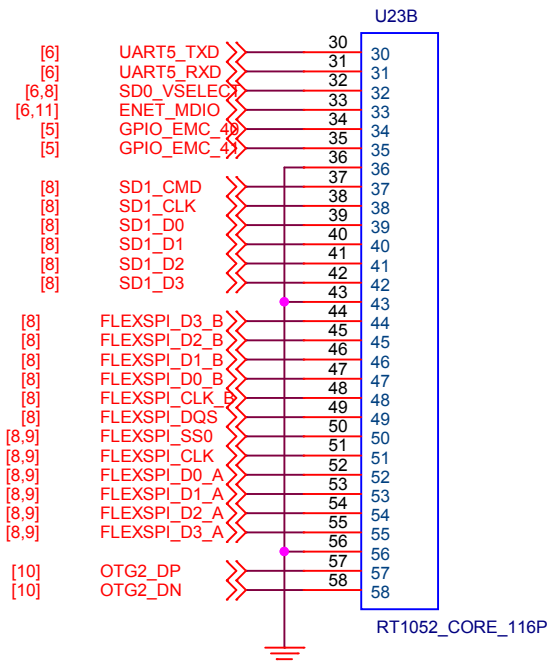
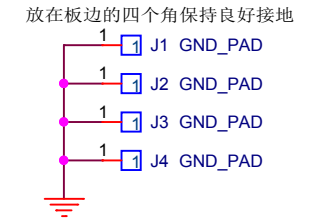
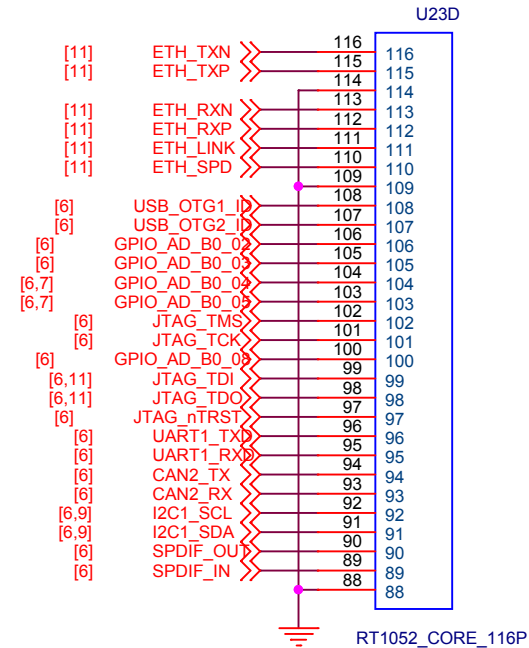
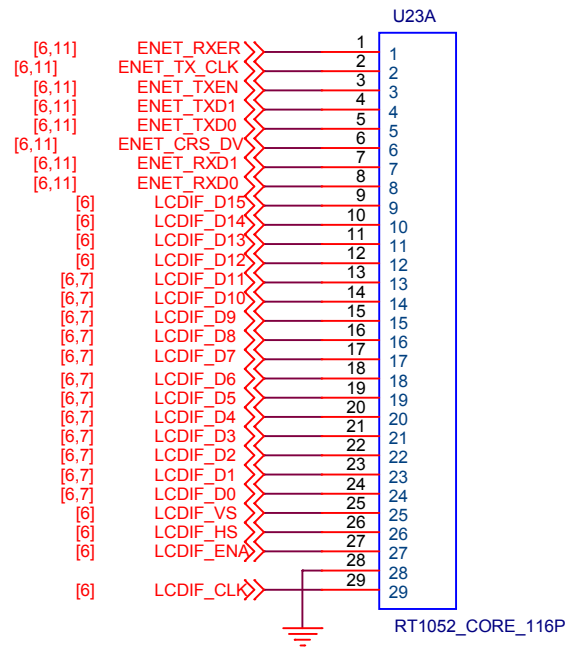


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Layout:
1. 等长走线



Layout:
1. 差分走线



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