

## Jesse Ardonne

### PRESENT ADDRESS

Southlake Avenue  
Baton Rouge, LA 70804

### PERMANENT ADDRESS

Smith Drive  
Walker , LA 70785

### JOB OBJECTIVE

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To apply my experience in a high-tech growth-oriented organization and utilize strong organizational, communication, and interpersonal relation skills in a challenging and creative environment.

### EDUCATION

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*Master of Science*, Electrical Engineering **GPA 3.909** August 2012 - December 2015

Louisiana State University Baton Rouge, LA  
Computer Engineering Concentration  
PhD Progress: Passed PhD qualifying exam in Fall 2013. Will have completed all coursework by December 2015.

*Bachelor of Science*, Engineering Technology **GPA 3.817** August 2008 - May 2012  
Southeastern Louisiana University Hammond, LA  
Dual Concentration in Computer Engineering Technology and Mechanical Engineering Technology

### INDUSTRY EXPERIENCE

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**Engineering Intern** Panama City, FL  
Naval Surface Warfare Center PCD May 2015 - July 2015  
The project consisted of autonomously launching and landing a quadcopter on an unmanned surface vehicle. An IRIS quadcopter, equipped with a Pixhawk controller, was outfitted with a Raspberry Pi backseat driver for autonomous control. Python, MAVlink and OpenCV were utilized to interface with the vehicle and provide autonomous control and image processing. Through image processing, an IR light source was identified and used to guide the quadcopter to the landing platform.

**Engineering Intern** Morgan City, LA  
Oceaneering December 2011 - August 2012  
During this internship, my primary objective was maintaining, troubleshooting, and debugging underwater ROV hardware units returning from offshore. Working with two other engineering undergraduates from different engineering backgrounds, additional tasks included creating models in SolidWorks, providing offshore technical support, and wiring equipment modules for deployment.

### ACADEMIC EXPERIENCE

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**Graduate Teaching Assistant** Baton Rouge, LA  
Louisiana State University August 2015 - Present  
Teaching assistant for EE 4755 - Digital Design Using HDLs and EE 3752 - Microprocessor Systems.

**Graduate Research Assistant** Baton Rouge, LA  
Louisiana State University August 2012 - March 2015  
Research in Computer Architecture, Hardware Engineering, Cache Management, Transactional Memory, and Memory Hierarchy which included GPU cache bypassing and warp throttling, hardware implementation of transactional memory on GPUs, heterogeneous shared cache resource contention algorithms, and microarchitectural comparisons between Intel Xeon Phi and Nvidia GPU accelerators.. Held the Economic Development Assistantship.

**Undergraduate Research Assistant**

Southeastern Louisiana University

Hammond, LA

February 2010 - June 2010

Assisted in faculty research projects by testing incoming hardware peripherals via programming and hyperterminal. Presented research on internet-based teleoperations of a mobile robot (Pioneer 3AT).