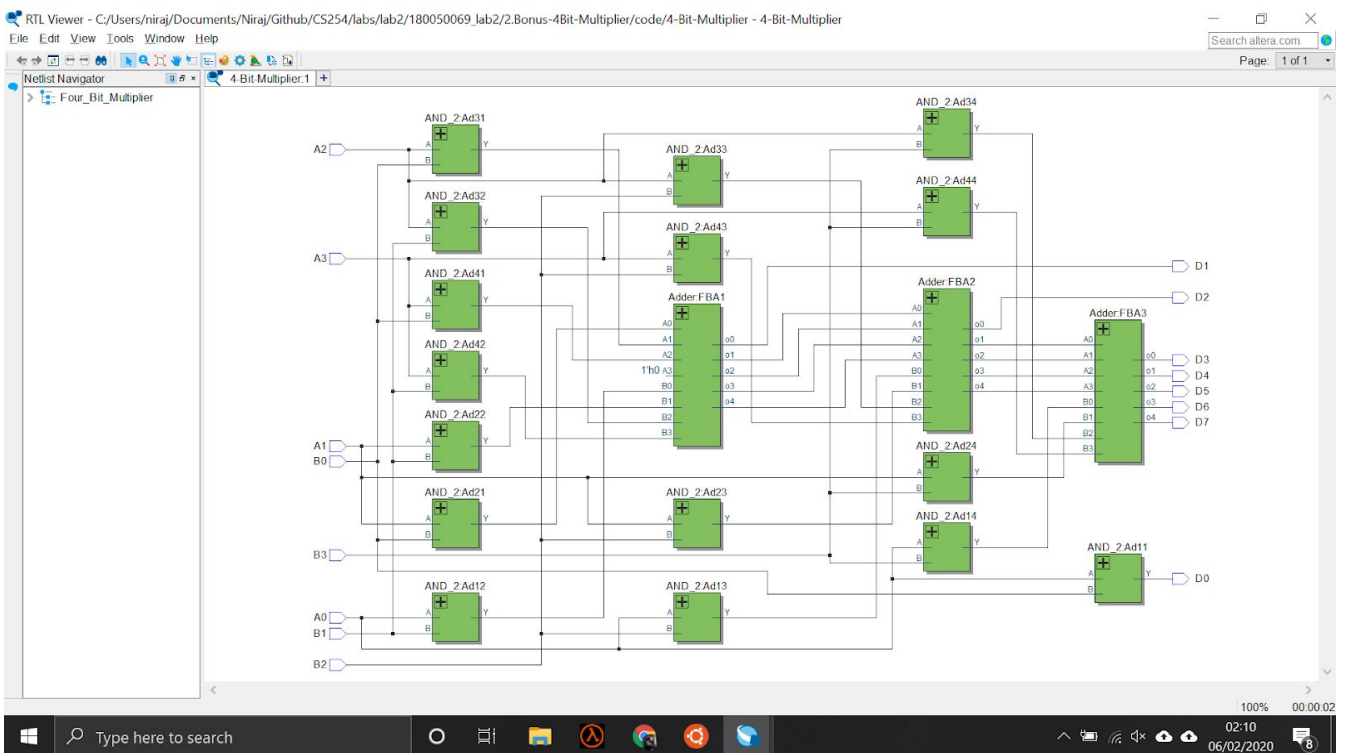# Lab2 Report

Niraj Mahajan
180050069

Arithmetic Circuit

1. Implementation details
   a. In this question I have calculated the necessary 'conjunctions' of the input signals using AND gates
   b. I have used Full Adders to construct my very own Four Bit adder.
   c. This four bit adder is used to combine the conjunctions into our required result.
2. Diagram

3. Simulation: