Lab5 Report
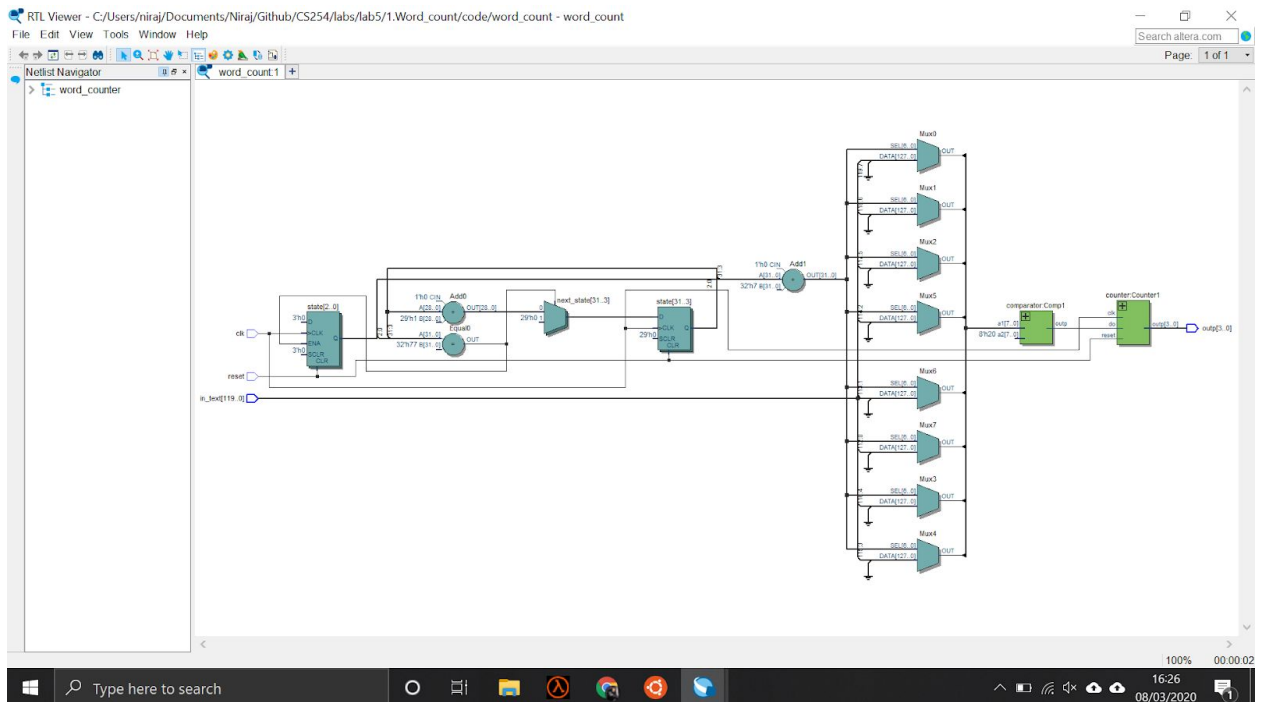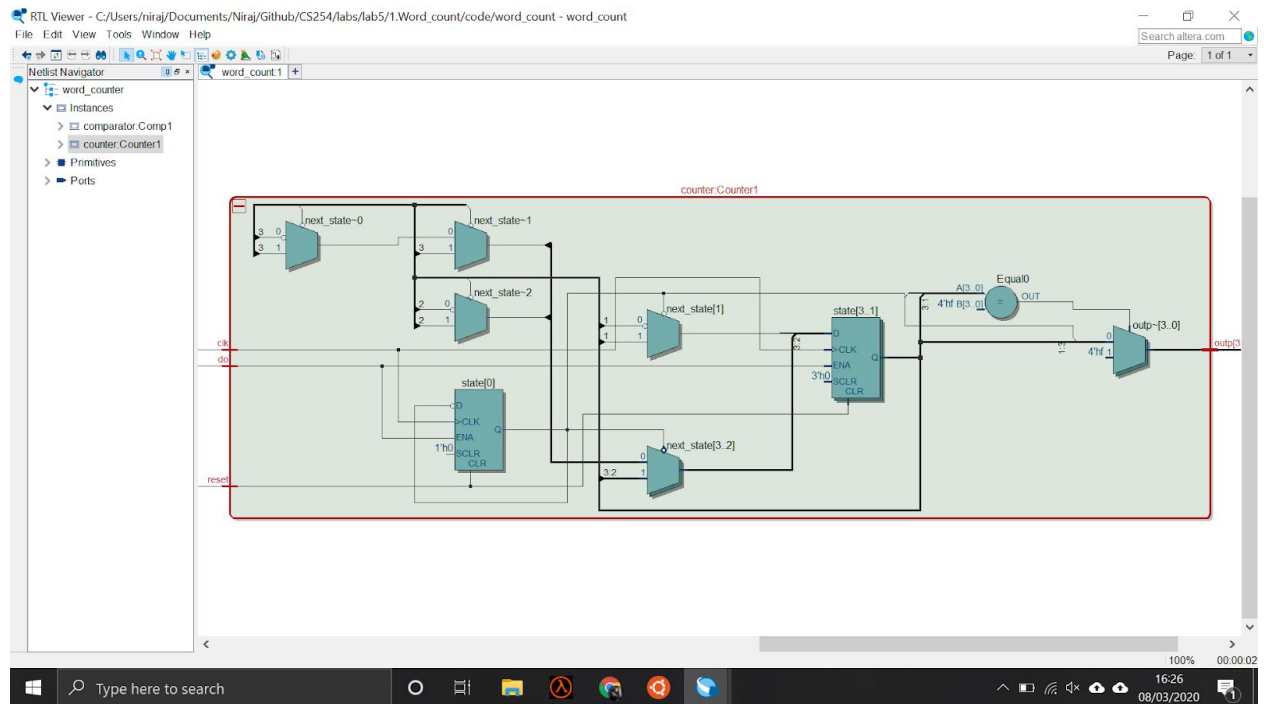
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Word Count

1. Implementation details
   a. Gates used as taught in class.
   b. If - else loop and and, xor, not & or gates used.
   c. FSM used
2. Diagram

3. Components (FSM state diagrams):
    a. Counter

    b. Comparator