

$$V_r = \frac{V_m}{2fRC}$$

$$V_{r} = \frac{12\sqrt{2} - 1.4}{2 + RV_{r}} = \frac{12\sqrt{2} - 1.4}{2 \times 60 \times 100 \times 1} = 1.2975 \times 10^{3} F$$

$$C = \frac{V_{m}}{2 + RV_{r}} = \frac{12\sqrt{2} - 1.4}{2 \times 60 \times 100 \times 1} = 1.297 \text{ MF}$$
(a)

$$= |.2975 \times 10^{5} F$$

$$c = |297 HF$$

$$\frac{DC}{(aurege)}$$
 at output = $(V_m - \frac{1}{2}V_m) = \frac{12V_2 - 1.4 - \frac{1}{2}x}{2 \cdot 15.07 \cdot 1.4}$

DC (amage value) with E'

Max. convent through R' =
$$\frac{12\sqrt{2}-1.4}{R} = \frac{12\sqrt{2}-1.4}{100-2} = 0.155A$$
 (c)

$$PIV = V: (mox) - Vy = (12V2 - 0.7)V = [16.27V]$$
 (d)

2

In the previous page, At is time duration during which diode conducts.

$$\Delta t = \frac{1}{2\pi f} \sqrt{\frac{2 \text{ Nr}}{\text{Nrm}}}; \text{ (fan details please See Neama's book)}.$$

$$= \frac{1}{2\pi \times 60} \sqrt{\frac{2 \times 1}{12\sqrt{2} - 1.4}}$$

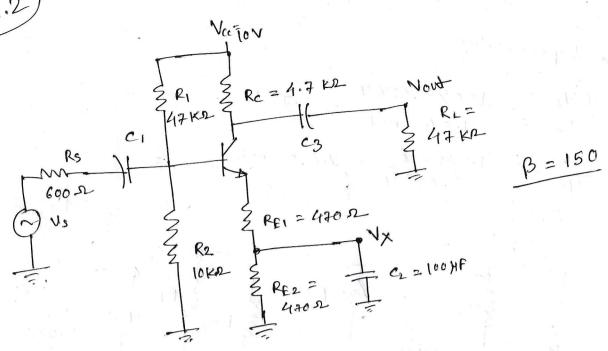
At = 951 HS on 0.951 ms an 9.51 ×10 Sec.

(e)

DC equivalent circuit.

(a)





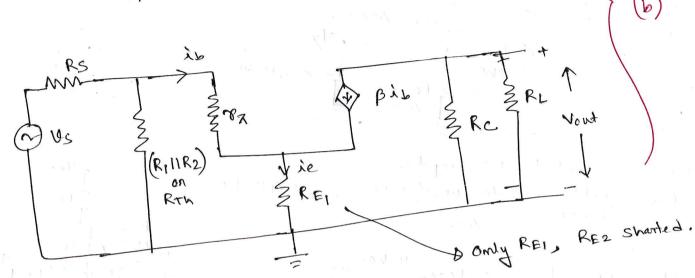
Therenin equivalent at the import,

$$f_c = \beta + \beta = 180 \times 6.75 \text{ M}$$
 $V_c = V_{ce} - I_c R_c = 10 - 1.048 \text{ mA} \times 4.7 \text{ kg}$
 $V_c = 5.074 \text{ V}$.

Ac amalysis small signal parameters

$$r_{Z}^{2} = \frac{\beta V_{T}}{I_{C}} = \frac{150 \times 0.026 \text{ V}}{1.048 \text{ mA}} = 3.72 \text{ kp}.$$

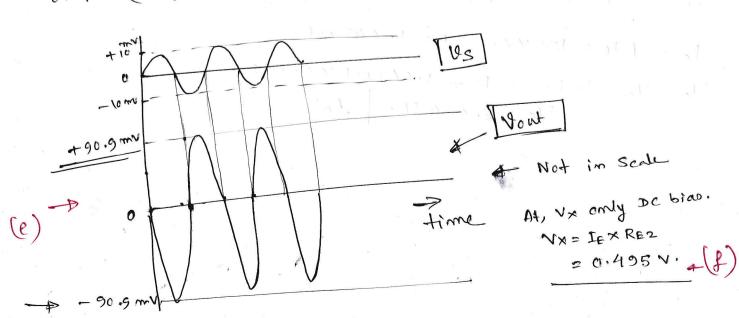
$$g_{m} = \frac{f_{c}}{V_{T}} = \frac{1.048 \text{ mA}}{0.026 \text{ N}} = 40.3 \text{ mS}.$$



(d) If C2 is reduced to 10 mF, then fan AC analysis C2 count be considered an Short, we have to consider the impedence offered by C2, hence effective RE incress.

Imput (Vs) = 10 mV (pests)

Imput (Vs) = 10 mV (pests) output (Vout) = -10 mV x 9.09 ==90.9 mV; C3 blocks The De part.





DC equivalent cft!

$$V_{G} = V_{DD} \times \frac{RG2}{RG1 + RG2}$$

$$= 15 \times \frac{5}{10 + 5}$$

$$V_{S} = (V_{A} - 2)V = (5 - 2) = 3V$$
.

again,
$$V_s = I_D R_S$$

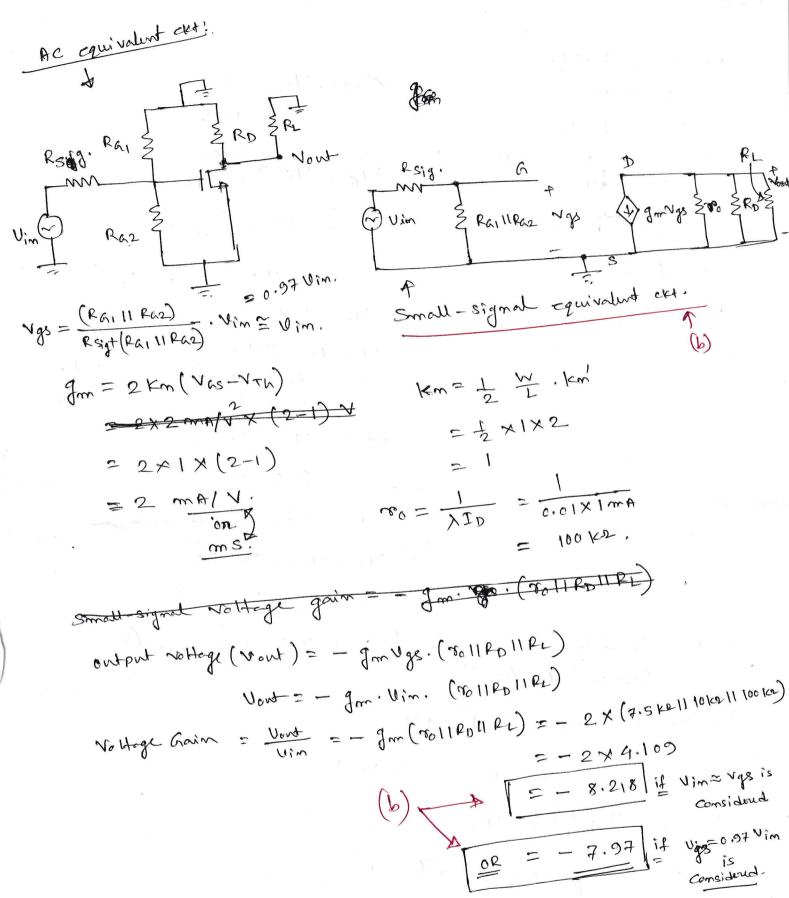
 $3 = I_D \times 3 K_D$

Se,
$$V_{DS} = V_{D} - V_{S}$$

= $(7.5 - 3) \times 24.5 \times$

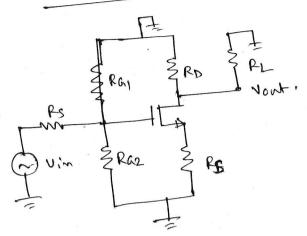
VDS > VDSat, so mosfet is in saturation.

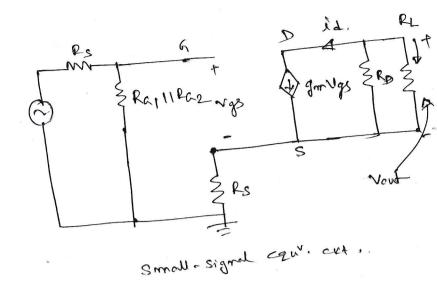
$$I_D = \frac{1}{2} \left(\frac{W}{L} \right) k_m \left(v_{LS} - V_{Th} \right)^2$$



It cs is removed.

then equ' Ac. clet.





Considering, $\lambda = 0$, $v_0 \Rightarrow \omega$.

Volteg Gain =
$$-\frac{RD}{RS} = -\frac{7.5}{3} = -\frac{2.5}{4}$$
 (c)

If C1 is reduced drastically, then for the signed fore. in C1 con't be considered as short-cxt. Impedence of C1 then should be considered and Ugs & Vin, So, Small-signal gover reduces.