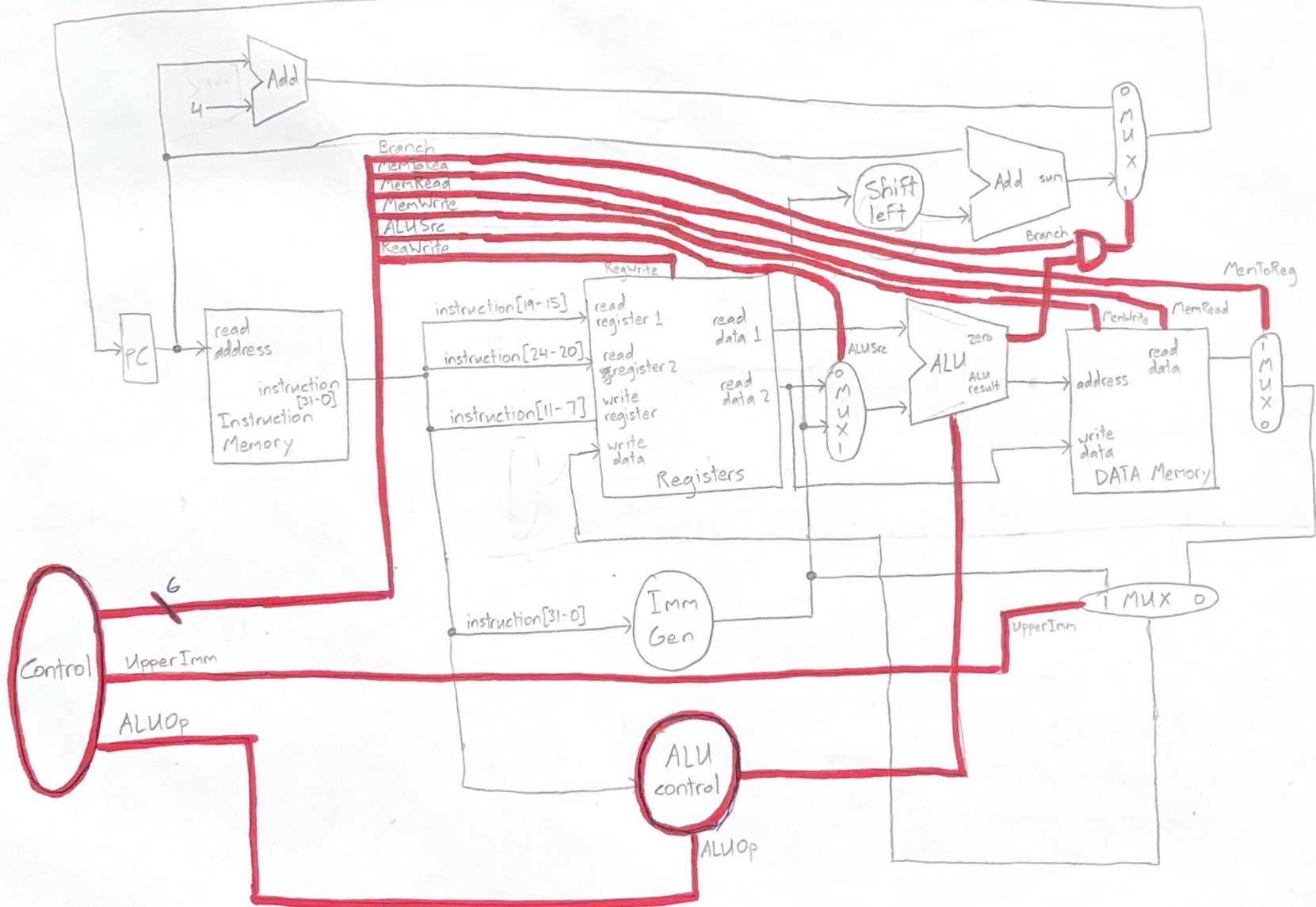


DATAPATH + Controller



Instruction	Opcode	RegWrite	AluSrc	Branch	MemRe	MemWr	MemtoReg	UpperImm	ALUOp
R-type	0110011	1	0	0	0	0	0	0	-
I-type	0010011	1	1	0	0	0	0	0	-
Load	0000011	1	1	0	1	0	1	0	-
Store	0100011	0	1	0	0	1	0	0	-
BEQ	1100011	0	0	1	0	0	0	0	12
LUI	0110111	1	1	0	0	0	0	1	14
JAL	1101111	1	0	1	0	0	0	0	13

-Instruction	ALUOp	Func3	Func7
ADD	0	0x0	0x0
XOR	4	0x4	
SRAI	5	0x5	0x20
ORI	6	0x6	
LB	8	0x0	
LW	9	0x2	
SB	10	0x0	
SW	11	0x2	
BEQ	12		
JAL	13		
LUI	14		

opcode is enough for these 3

