

Design of a single-ended mirrored cascode OTA
Use NMOS input transistors.
[pp. 265-266]

Design specifications:

- Nominal supply voltage = 1.8 V
- DC open loop gain > 50 dB
- Load capacitance = 200 fF
- Unity gain frequency > 500 MHz
- Minimize the power consumption

Minimum features to be evaluated/simulated:

- Bode diagrams
- PSRR and CMRR (apply a small mismatch at the differential pair before simulating)
- Slew-rate (positive and negative)

Provide a layout stick diagram of the designed OTA.