

Maharshi Dayanand University, Rohtak



DMC No. 17106
Roll No. 588430
Regn. No. 03-BMES-120

RESULT-cum-DETAILED MARKS CARD

BACHELOR OF ENGINEERING
(COMPUTER SC.& ENGG.)

5TH SEMESTER EXAMINATION DEC, 2005

Name : NISHANT CHAWLA

Father's Name : Shri SUBHASH CHAWLA

DETAILS OF MARKS

Sr. No.	Subject	Marks Obtained	Minimum Pass Marks	Maximum Marks
1.	D06 MULTIMEDIA TECHNOLOGIES IT-204-E	048 039 087	040 060	100T 050S 150
2.	D53 MULTIMEDIA TECHNOLOGIES LAB IT-208-E	020 021 041	010 020	025P 025S 050
3.	E04 PRINCIPLES OF OPERATING SYS CSE-301-E	062 039 101	040 060	100T 050S 150
4.	E05 COMPUTER GRAPHICS CSE-303-E	040 038 078	040 060	100T 050S 150
5.	E06 ANALYSIS & DESIGN OF ALGORITHMS CSE-305-E	048 038 086	040 060	100T 050S 150
6.	E07 WEB DEVELOPMENT CSE-307-E	064 038 102	040 060	100T 050S 150
7.	E08 MICROPROCESSOR & INTERFACING EE-309-E	051 037 088	040 060	100T 050S 150
8.	E33 COMPUTER GRAPHICS LAB CSE-309-E	019 020 039	010 020	025P 025S 050
9.	E34 WEB DEV. & CORE JAVA LAB. CSE-311-E	021 017 038	010 020	025P 025S 050
10.	E35 ASSESSMENT OF MINI-PROJECT CSE-313-E	✓030 030	020	S 050 050
11.	E36 MICROPROCESSOR & INTERFAC. LAB EE-329-E	013 022 035	010 020	025P 025S 050
12.				
13.				
14.				
15.				
Grand Total		0725		1150

RESULT : PASSED AND OBTAINED SEVEN HUNDRED TWENTY FIVE MARKS

DATED : MAR 31, 2006
ROHTAK

Checked by 1. [Signature] 2. [Signature]
(Full Signature) (Clerk) (Assistant)

Date of issue _____

CONTROLLER OF EXAMINATIONS