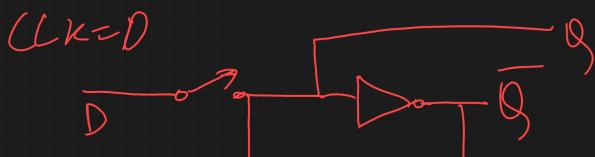
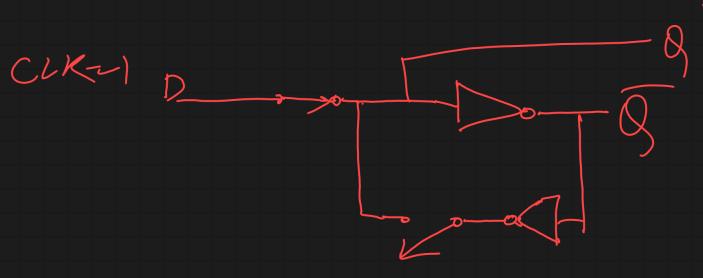
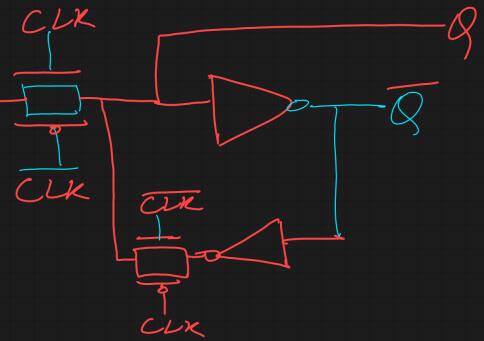
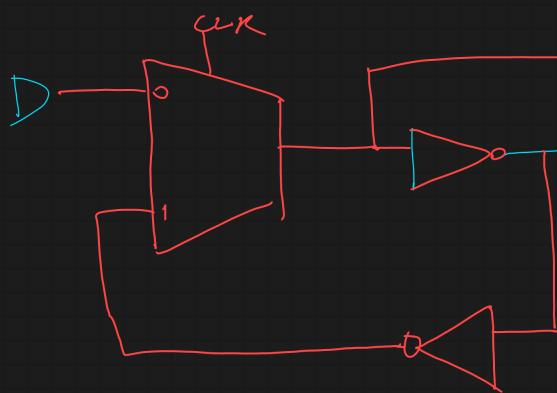
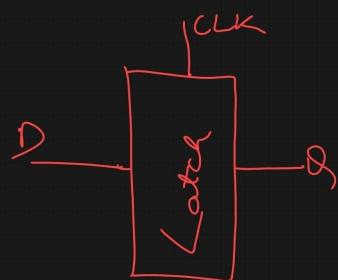


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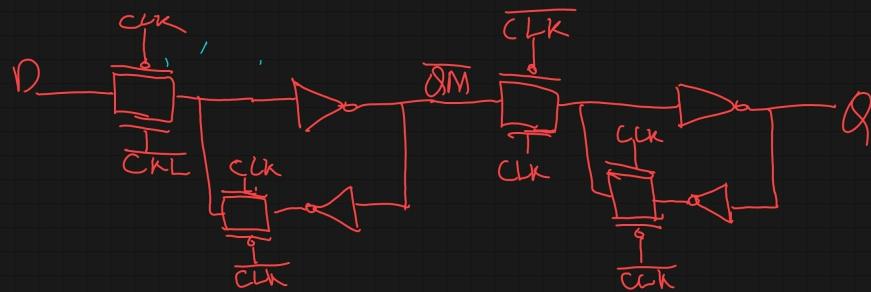
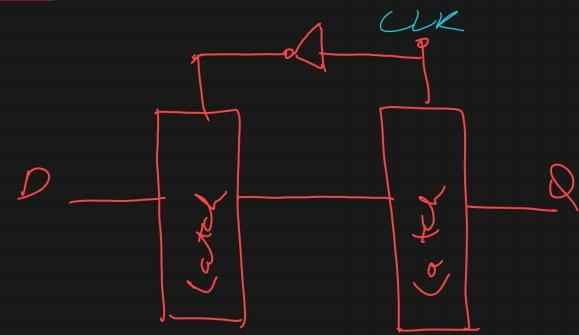


-1-

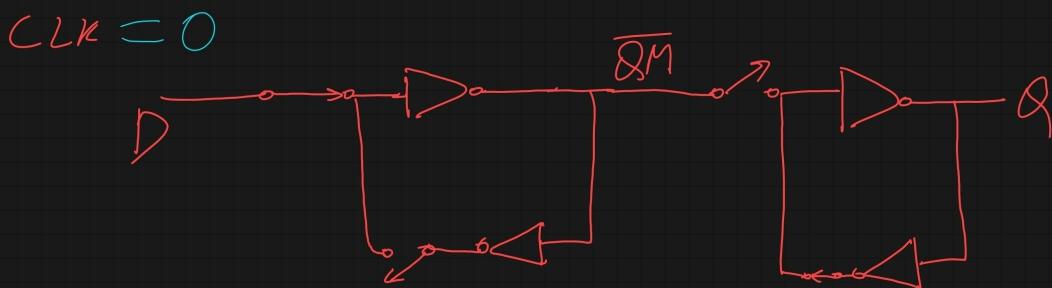
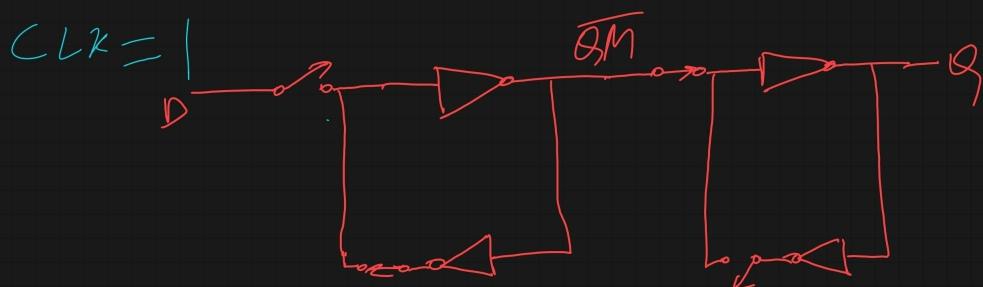


-2-

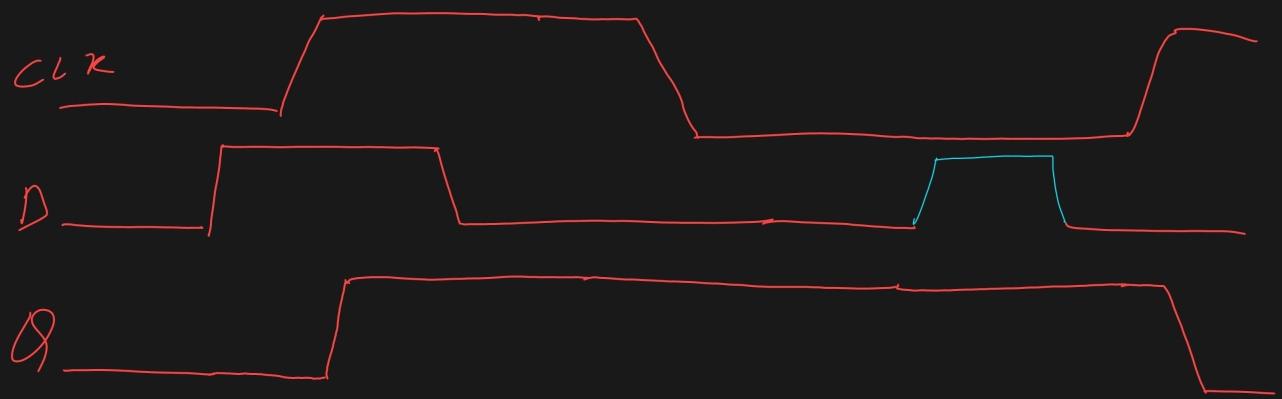
## Flip-Flop :-



-3-

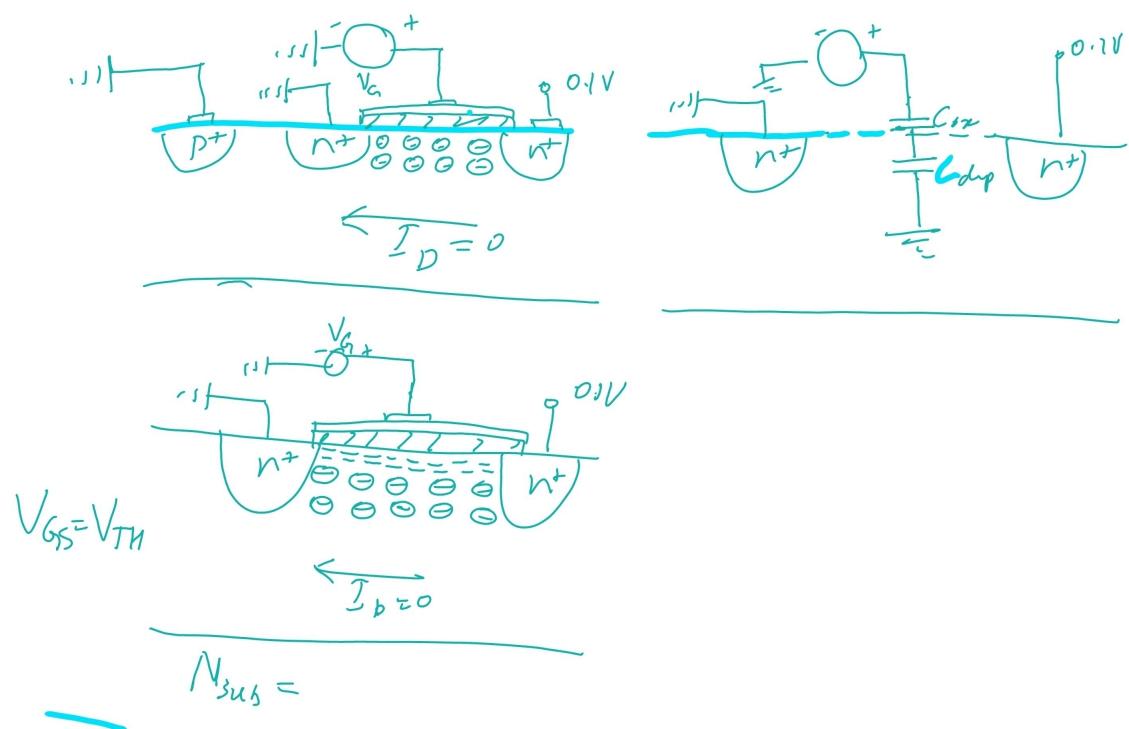


-4-



-5-

20/08/25



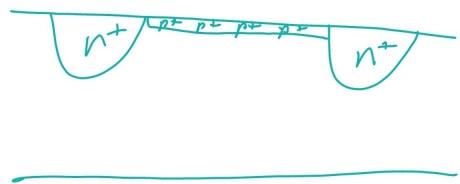
-6-

$$V_{TH} = \phi_{MS} + 2\phi_F + \frac{\phi_{dep}}{C_{ox}}$$

where,  $\phi_F = (kT/q) \ln (N_{sub}/n_i)$

$$\phi_{dep} = \sqrt{4q\epsilon_{si} |\phi_F| N_{sub}}$$

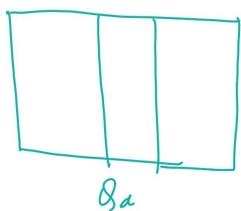
$$C_{ox} = 6.9 fF/\mu m^2 \quad \text{for } t_{ox} = 50 \text{ \AA}.$$



-7-

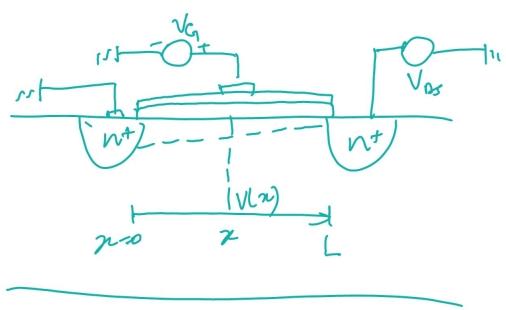
$$I_d = \phi_d V$$

where,  $\phi_d$  (charge density per unit area)



$$\phi_d = W C_{ox} (V_{GS} - V_{TH})$$

-8-



$$Q_d = W C_{ox} (V_{GS} - V(x) - V_{TH})$$

$$I_d = -W C_{ox} (V_{GS} - V(x) - V_{TH}) V$$

$$I_d = W C_{ox} (V_{GS} - V(x) - V_{TH}) \mu_n \frac{dV(x)}{dx}$$

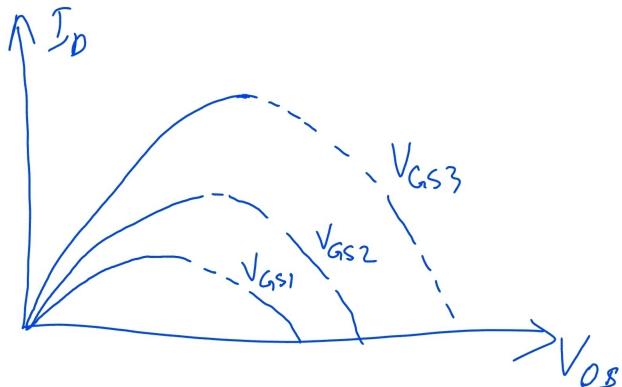
$$\int_{x=0}^{x=L} I_d dx = \int_{V_D=0}^{V_D} W C_{ox} (V_{GS} - V(x) - V_{TH}) \mu_n dV(x)$$

$$j = \mu E$$

$$= \mu \frac{dV(x)}{dx}$$

-9-

$$I_d = \mu_n C_{ox} \frac{W}{L} \left[ (V_{GS} - V_{TH}) V_{DS} - \frac{1}{2} V_{DS}^2 \right]$$



$$V_{GS} - V_{TH} =$$

For  $V_{DS} \ll V_{GS} - V_{TH}$

$$I_d = \mu_n C_{ox} \frac{W}{L} [(V_{GS} - V_{TH}) V_{DS}]$$

-10-

$$R_o = \frac{1}{\mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})}$$

-11-

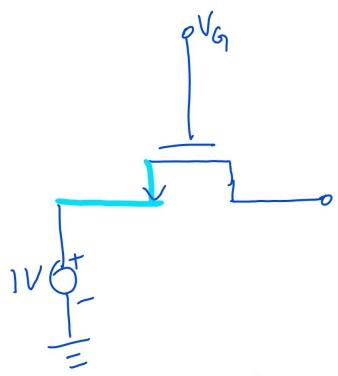
$$V(x) = V_{GS} - V_{TH}$$

$$\int_{x=0}^{x=L'} I_d dx = \int_0^L \mu_n C_{ox} \frac{W}{L} [V_{GS} - V(x) - V_{TH}] dx$$

$$I_d = \frac{\mu_n C_{ox}}{2} \frac{W}{L'} [V_{GS} - V_{TH}]^2$$

$$L \approx L'$$

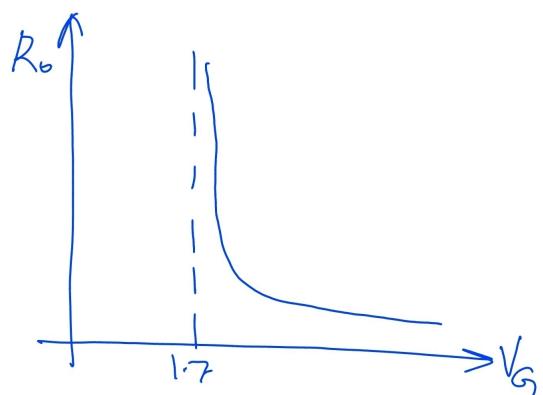
-12-



$$R_o = \frac{1}{\mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})}$$

$$R_o = \frac{1}{50 \times 10^{-4} \times 10 (V_G - 1 - 0.7)} = R_o = \frac{1}{5 \times 10^4 \times (V_G - 1.7)}$$

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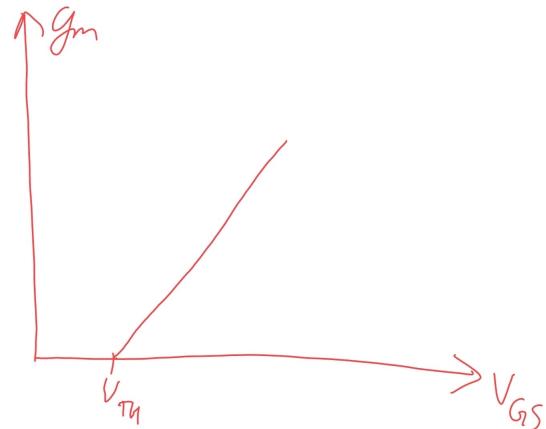


-14-

Saturation Mode ( $V_{DS} > V_{GS} - V_{TH}$ )

$$I_D = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})^2$$

$$g_m = \frac{\partial I_D}{\partial V_{GS}} = \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})$$



$$\boxed{I_D = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})^2}$$

-15-

$$g_m = \sqrt{(\mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH}))^2}$$

$$\Rightarrow g_m = \sqrt{\left(\frac{2}{2} \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})\right)^2}$$

$$\Rightarrow g_m = \sqrt{2 \left(\mu_n C_{ox} \frac{W}{L}\right) \left(\frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})^2\right)}$$

$$\Rightarrow \boxed{g_m = \sqrt{2 \mu_n C_{ox} \frac{W}{L} I_D}}$$

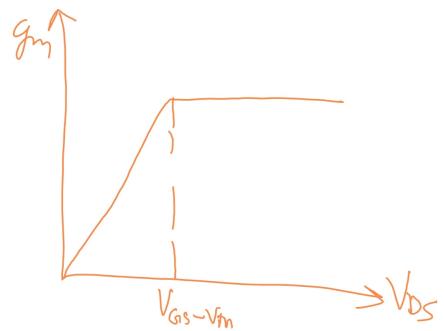
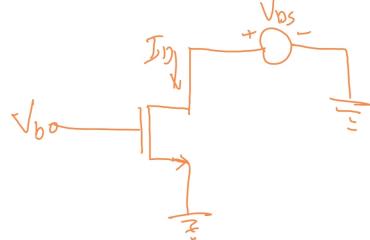
-16-

$$g_m = \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{Th})$$

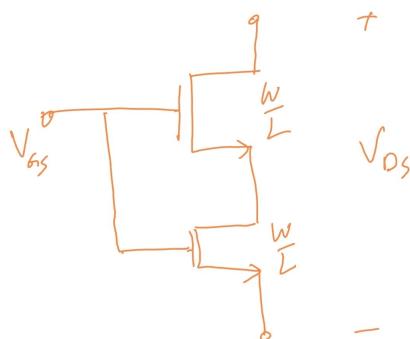
$$\Rightarrow g_m = 2 \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{Th}) \times \frac{(V_{GS} - V_{Th})}{(V_{GS} - V_{DS})}$$

$$\Rightarrow g_m = \frac{2 I_D}{(V_{GS} - V_{DS})}$$

$\delta \rightarrow$



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$$V_{Th} = \phi_{ms} + 2 \phi_F + \frac{q_{dp}}{C_{ox}}$$

$$\epsilon_{Si} = 11.7 \times 8.85 \times 10^{-14}$$

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$$\Rightarrow \frac{1}{L'} = \frac{1}{L} \cdot \frac{1}{U - \Delta U(L)} = \frac{1}{L} (1 - \frac{\Delta U}{U})^{-1}$$

$$\Rightarrow \frac{1}{L'} = \frac{1}{L} (1 + \frac{\Delta U}{U}) \quad \text{--- (J)}$$

$$I_D = \frac{1}{2} \mu_n C_{ox} \frac{W}{L'} (V_{GS} - V_{TH})^2$$

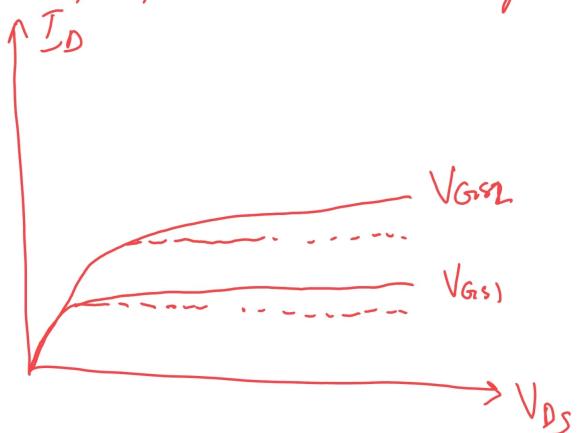
$$\Rightarrow I_D = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})^2 (1 + \frac{\Delta U}{U})$$

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$$\therefore \frac{\Delta U}{U} \propto V_{DS}$$

$$I_D = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})^2 (1 + \lambda V_{DS})$$

where,  $\lambda$  = channel length coefficient.



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$$g_m = \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{Th})(1 + \lambda V_{DS})$$

$$\lambda \propto \frac{1}{L}$$

$$r_o = \frac{1}{\lambda I_o}$$

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$\Rightarrow$  Given:  $W/L = 50 \times 5$   
 $|V_{GS}| \Rightarrow (0, 3V)$   
 $V_{DS} = 3V$

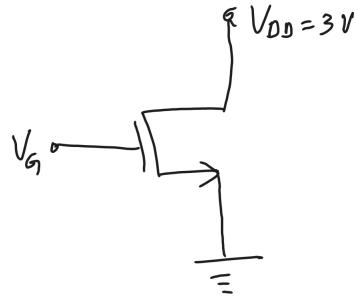
NFET:-

For  $V_{GS} < V_{Th}$   
 $I_D = 0$

For  $V_{GS} > V_{Th}$

Since,  $V_{DS} = 3V$  and  $V_{GS} > V_{Th}$

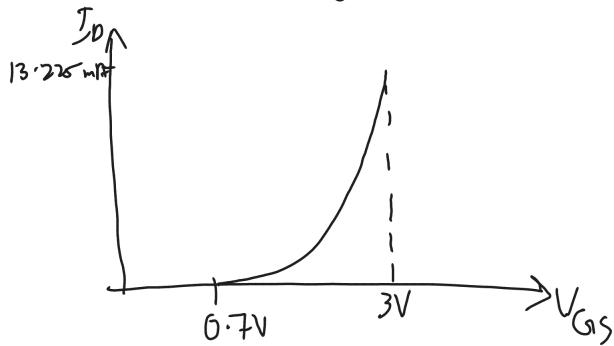
$\Rightarrow V_{DS} > V_{GS} - V_{Th}$  for all values of  $V_{Th} < V_{GS} < 3V$



-22-

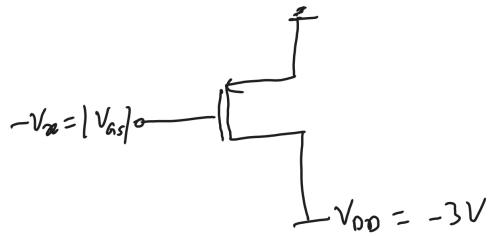
Therefore, the transistor is in saturation regime. Hence,

$$\begin{aligned}
 I_D &= \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})^2 \\
 &= \frac{1}{2} \times 50 \left( \frac{\mu A}{V^2} \right) \cdot \frac{50}{0.5} (V_{GS} - V_{TH})^2 \\
 &= 2.5 \left( \frac{mA}{V^2} \right) (V_G - 0.7)^2
 \end{aligned}$$



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PFET:

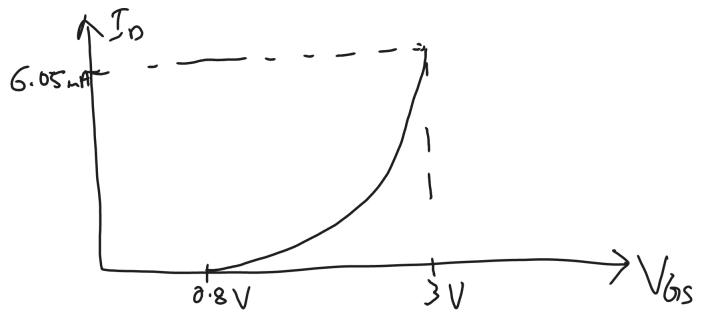


For  $|V_{GS}| < V_{TH}$ ;  $I_D = 0$

For  $|V_{GS}| > V_{TH}$ ;

$$I_D = \frac{1}{2} \mu_p C_{ox} \frac{W}{L} (V_x - V_{TH})^2 = \frac{1}{2} \left( 25 \left( \frac{\mu A}{V^2} \right) \right) \left( \frac{50}{0.5} \right) (V_x - 0.8)^2$$

$$\Rightarrow 1.25 (V_x - 0.8)^2 \text{ (mA)}$$



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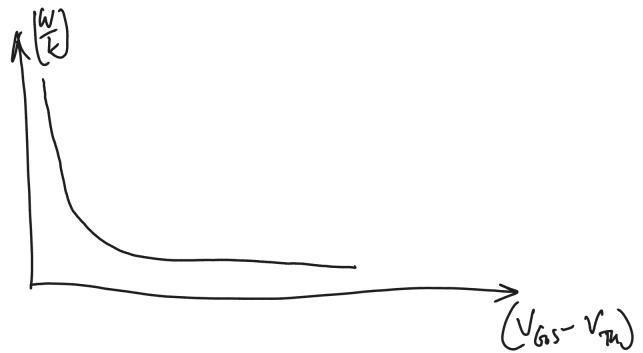
Q: Given: NMOS is in saturation mode.  
Plot  $\frac{W}{L}$  vs  $(V_{GS} - V_{TH})^2$

a)  $I_D$  is constant:

In saturation,

$$I_D = \frac{1}{2} \mu_n C_o x \frac{W}{L} (V_{GS} - V_{TH})^2$$

$$\Rightarrow \left( \frac{W}{L} \right) = \frac{2 I_D}{\mu_n C_o (V_{GS} - V_{TH})^2}$$

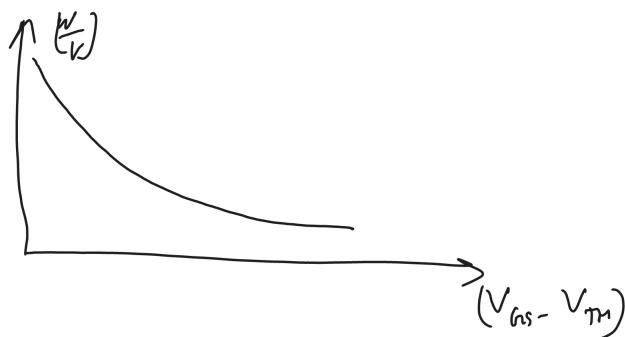


b)  $g_m$  is constant:

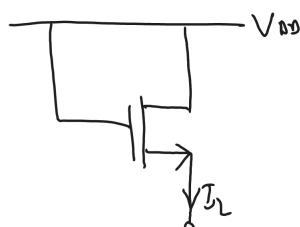
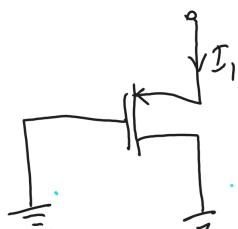
For saturation mode,  $g_m = \mu_n C_o \frac{W}{L} (V_{GS} - V_{TH})$

$$\left( \frac{W}{L} \right) = \frac{g_m}{\mu_n C_o (V_{GS} - V_{TH})}$$

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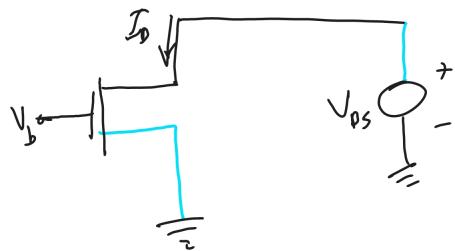


Q:- Explain why the structures shown in the figure below cannot operate as current source even the transistors are in saturation.



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R:

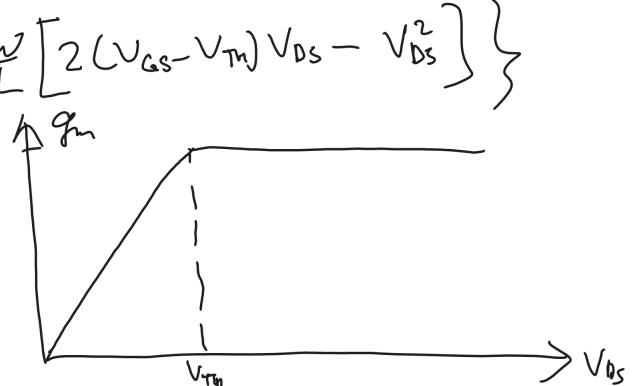


If,  $V_{DS} > V_b - V_{TH}$ , the transistor is in saturation mode.

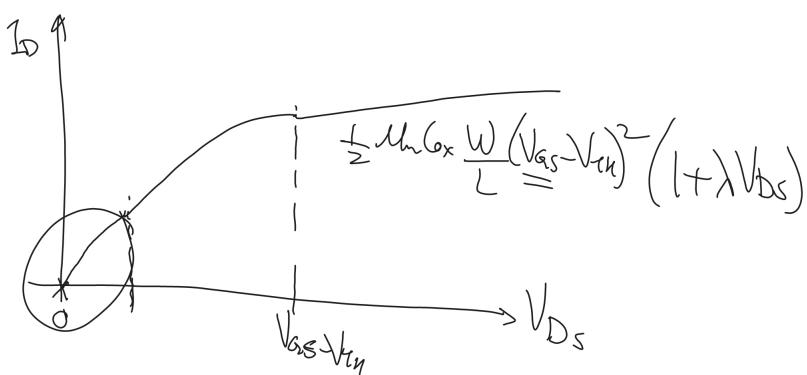
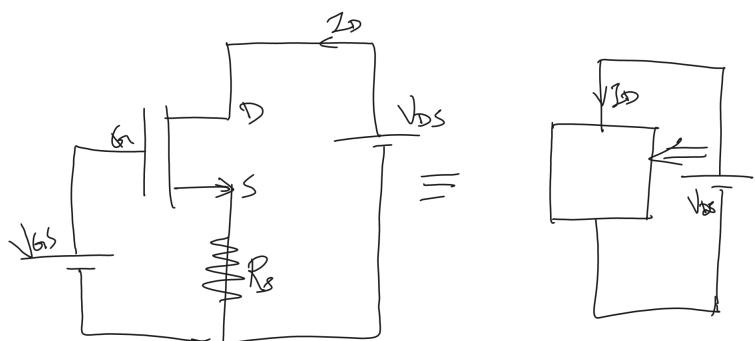
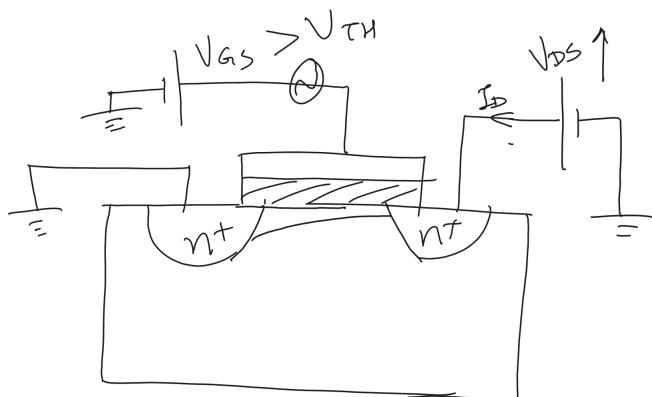
for,  $V_{DS} < V_b - V_{TH}$ , the transistor is <sup>working</sup> in triode region.

$$g_m = \frac{\partial}{\partial V_{GS}} \left\{ \frac{1}{2} \mu_n C_{ox} \frac{W}{L} [2(V_{GS} - V_{TH})V_{DS} - V_{DS}^2] \right\}$$

$$= \mu_n C_{ox} \frac{W}{L} V_{DS}$$



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$$I_D \propto f(V_{DS})$$

$$R_o = \frac{dV_{DS}}{dI_D} = \frac{1}{dI_D/dV_{DS}}$$

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$$R_o = \frac{1}{\left\{ \frac{1}{2} M_n C_s \frac{W}{L} (V_{GS} - V_{TH})^2 \right\} \lambda}$$

$$R_o \approx \frac{1}{\lambda I_D}$$

### ~~Small Signal Model~~

- SSM represents time varying quantities
- All constant sources should be made zero

