Publication List

This thesis is based in part on the following published papers:

① **Rajiv Nishtala**, Marc Gonzalez Tallada, Xavier Martorell, "A Methodology to Build Models and Predict Performance-Power in CMPs", in the 44th International Conference on Parallel Processing Workshops (ICPPW), Beijing, China, Sep. 1-4, 2015.

② **Rajiv Nishtala**, Xavier Martorell, Vinicius Petrucci, Daniel Mossé, "REPP-H: Runtime Estimation of Power and Performance on Heterogeneous Data Centers", in the 28th International Symposium on Computer Architecture and High Performance Computing (SBAC-PAD), Los Angeles, USA, Oct. 26-28, 2016.

③ **Rajiv Nishtala**, Xavier Martorell, "RePP-C: Runtime Estimation of Performance-Power with Workload Consolidation in CMPs", in the 7th International Green and Sustainable Computing Conference (IGSC), Hangzhou, China, Nov. 7-9, 2016.

④ **Rajiv Nishtala**, Paul Carpenter, Vinicius Petrucci, Xavier Martorell, "Hipster: Hybrid Task Manager for Latency-Critical Cloud Workloads", in the 23rd IEEE Symposium on High Performance Computer Architecture (HPCA) 2017, Austin, USA, Feb. 4-8, 2017.

The following publication is under review:

① **Rajiv Nishtala**, Paul Carpenter, Vinicius Petrucci, Xavier Martorell, "The Hipster Approach for Improving Cloud System Efficiency" in ACM Transactions of Computer Systems (ACM TOCS) Feb. 2017.

The following paper was my masters' work, and not included in this thesis:

① **Rajiv Nishtala**, Daniel Mossé, Vinicius Petrucci, "Energy-aware thread co-location in heterogeneous multicore processors", in the 11th ACM International Conference on Embedded Software (EMSOFT), Montreal, Canada, Sep. 29 - Oct. 4, 2013.