

Miquel Pericàs is a assistant professor in the computer engineering division at the department of computer science and engineer at the Chalmers University (Sweden). Pericàs received his MSE degree in Telecommunications (2002) and PhD degree in Computer Architecture (2008) from the Universitat Politècnica de Catalunya (Spain). His research interests focus on computer architecture, runtime, and power modeling. He has served as program committee member in several leading conferences including ICS, ICPP, HPCA, etc.

CV: https://www.dropbox.com/s/mj3kxuj0hbnwur3/miquelCVen.pdf