Dr. P. Karuppanan, ECED





Designation & Affilation	Assistant Professor
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Area of Interest

- Electronic Devices and Circuits,
- Analog and Digital VLSI design,
- Embedded system,
- Industrial Electronics,
- Power Quality,
- Renewable Energy System

Educational Qualification

Ph.D (Industrial Electronics)	National Institute of Technology, Rourkela
M.E (VLSI Design)	Anna University, Chennai
B.E (Electronics and Communication)	Madurai Kamaraj University, Madurai

Research Activities

Publications

International Journals

- 1. P. Karuppanan and Kamala Kanta Mahapatra "Erratum to "PI and fuzzy logic controllers for shunt active power filter—A report" [ISA Transactions 51, 163–169]" ISA Transactions, Vol.51, No.5, pp.667-671, Sept-2012
- 2. Karuppanan P and Kamala Kanta Mahapatra 'PI and fuzzy logic controllers for shunt active power filter A report' *ISA Transactions* Vo.51, No.1, pp.163–169, Jan-2012
- 3. Karuppanan P and Kamala Kanta Mahapatra 'PI, PID and Fuzzy Logic Controlled Cascaded Voltage Source Inverter based Active Filter for Power Line Conditioners' WSEAS Transaction on Power Systems, Vol.6, No.4, pp.100-109, Oct-2011
- 4. Karuppanan P, Kamala Kanta Mahapatra, Jeraldine Viji and Bhuyan Kanhu Charan 'Sinusoidal Extraction Control Strategy based Shunt Active Power Line Conditioners for Enhancing Power Quality' *Journal of Electrical and Electronics Engineering*, Vol.4, No.2, pp.83-88, Oct-2011.
- 5. Karuppanan P, Ayas Kanta Swain, Kamala Kanta Mahapatra 'FPGA based Single-phase Cascaded Multilevel Voltage Source Inverter Fed ASD Applications' *Journal of Electrical Engineering*, Vol.11, No.3, pp.102-107, Oct-2011.
- 6. Karuppanan P and Kamala Kanta Mahapatra 'Adaptive-Fuzzy Controller Based Shunt Active Filter for Power Line Conditioners' *TELKOMNIKA Journal of Electrical Engineering*, Vol. 9, No.2, pp. 201-208, Aug-2011.

International Conference

- 7. Karuppanan P, Kamala Kanta Mahapatra, Jeyaraman.K and Jeraldine Viji 'Fryze Power Theory with Adaptive-HCC based Active Power Line Conditioners', *IEEE International Conference on Power and Energy Systems* (ICPS),2011
- 8. Karuppanan P, Rajesh Kumar Patjoshi, Kamalakanta Mahapatra and Ajay-D-Vimalraj 'Sinusoidal Extraction Controller based on Cascaded VSI for Active Power Filter' *IEEE-INDICON-*2011

- 9. Karuppanan P, Smrtuti Ranjan Prusty and Kamala Kanta Mahapatra 'Adaptive-Hysteresis Current Controller based Active Power Filter for Power Quality Enhancement' IET International Conference on Sustainable Energy and Intelligent System (SEISCON) -2011
- 10. Karuppanan P, Saswat Kumar Ram and Kamala Kanta Mahapatra 'Three level hysteresis current controller based active power filter for harmonic compensation' *IEEE Emerging Trends in Electrical and Computer Technology* (ICETECT), pp.407-412, 2011
- 11. Karuppanan P and Kamala kanta Mahapatra 'Cascaded Multilevel Inverter based Active Filter for Power Line Conditioners using Instantaneous Real-Power Theory' *IEEE India International Conference on Power Electronics* (IICPE)- 2011
- 12. Karuppanan P and Kamala kanta Mahapatra 'PLL with PI, PID and Fuzzy Logic Controllers based Shunt Active Power Line Conditioners' *IEEE International Conference on Power Electronics, Drives and Energy Systems* (PEDES) 2010
- 13. Karuppanan P and Kamala kanta Mahapatra 'A Novel SRF Based Cascaded Multilevel Active Filter for Power Line Conditioners' *IEEE-INDICON* 2010
- 14. Karuppanan P and Kamala Kanta Mahapatra 'A Novel Control Strategy based Shunt APLC for Power Quality Improvements' *IEEE International Conference on Power, Control and Embedded Systems* (ICPCES) 2010
- 15. Karuppanan P, Rajasekar S and Kamala Kanta Mahapatra 'Five-Level Cascaded Active Filter for Power Line Conditioners' *IEEE International Conference on Power, Control and Embedded Systems* (ICPCES) 2010
- 16. Karuppanan P and Kamala Kanta Mahapatra 'Shunt Active Power Line Conditioners for Compensating Harmonics and Reactive Power' *IEEE International Conference on Environment and Electrical Engineering* (EEEIC)-Poland, pp.273-276, 2010
- 17. Karuppanan P and Kamala Kanta Mahapatra 'FPGA based Cascaded Multilevel Pulse Width Modulation for Single Phase Inverter' *IEEE International Conference on Environment and Electrical Engineering* (EEEIC)-Poland, pp.273-276, 2010

National Conference

- 18. Karuppanan P, Kamala kanta Mahapatra, Kanhu Charan Bhuyan and Rajesh Kumar Patjoshi 'Cascaded Voltage Source Inverter based Active Power Line Conditioners' *National Systems Conference* (NSC) 2011
- 19. Karuppanan P and Kamala Kanta Mahapatra 'A Control Strategy for Shunt Active Power Line Conditioners' *National Conference on Power Electronics* (NPEC)-2010.
- 20. Karuppanan P and Kamala Kanta Mahapatra 'PID with PLL Synchronization Controlled Shunt APLC under Non-Sinusoidal and Unbalanced Conditions' *National Conference on Power Electronics* (NPEC)-2010.