SAMEER BEHERA

E-mail: beherasameer108@gmail.com Mobile: +91 -9777509163

CAREER OBJECTIVE

To apply my knowledge and 3+ years of experience in PCB designing and to develop efficient and effective systems using my skills in Altium, Kicad, and Proteus to meet the company's needs.

EXPERIENCE

R&D and PCB Designer | Nippen Electricals Instruments Co., Andheri, Mumbai (Nov 2020 - Present)

- Completed PCB designing for various products.
- Perform PCB design testing and analyze test results.
- Provided and performed the following tasks for designing:
 - * Schematic Design, PCB layout Design.
 - * Schematic Component and PCB Footprint library generation.
 - * Gerber files and NC Drill files generation.
- Testing and Troubleshooting of Electronics PCBs/Systems, deep knowledge of Electronics hardware.
- Skilled in Using Test and Measuring Instruments.

SKILLS

- Proficient technical skills using the Altium designer toolset.
- Schematic capturing, Schematic symbol & library generation.
- PCB footprint creation under IPC7351 Std.
- Experience in layout of minimum 4-layer boards designing under IPC2221 Std.
- Good knowledge and understanding of DFM, DFT, and EMI, EMC guidelines.
- Output generation and CAMFILE editing.
- Circuit realization, Debugging, and Troubleshooting.
- Ability of Hardware Testing using Electronics Equipment (DMM, Oscilloscope, LCR meter).

PROJECTS

Digital Ammeter, Volt Meters, Multi-function Meters, Digital Earth Tester, Digital Insulation Tester, and Power Factor Controller.

EDUCATION

Diploma in Electrical Engineering | Gandhi School of Engineering, SCTEVT Odisha | 75.00% | 2019 12th Science | Bashudev Sethi Science College, HSE Odisha | 42.00% | 2017 10th | S.N.P High School, BSE Odisha | 66.00% | 2015

PERSONAL PROFILE

- Name: SAMEER BEHERA

- Date of Birth: 10th Aug 1999

- Gender: Male

- Languages Known: English, Hindi, Marathi

HOBBIES

- Playing cricket
- Singing
- Reading

DECLARATION

I hereby declare that the above-written particulars are best to my knowledge and belief, and I bear the responsibility for the correctness of the above-mentioned particulars.

Date: _				_
Place:	Saki	Naka,	Mum	bai

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