Assignment 1- Matrix Multiplication

Loop Unrolled:

Cycle Count: 575
Logic Elements:1512
Registers: 528
Memory: 6144
Multipliers:64

Flow Status   Successful - Mon Feb 04 09:05:56 2019
Quartus Prime Version   18.1.0 Build 625 09/12/2018 SJ Lite Edition
Revision Name   matmultop
Top-level Entity Name   matmultop
Family Cyclone IV E
Device EP4CE75F29C8
Timing ModelsFinal
Total logic elements   1,512 / 75,408 (2 %)
Total registers 528
Total pins   126 / 427 (30 %)
Total virtual pins   0
Total memory bits   6,144 / 2,810,880 (< 1 %)
Embedded Multiplier 9-bit elements   64 / 400 (16 %)
Total PLLs   0 / 4 (0 %)

 1286 ns: Simulation completed.
@ 1289 ns: Comparing file z.txt
Total simulation cycle count: 575
Total error count: 0

FIFO Multiplication

Cycle Count: 132
Logic Elements:9502
Registers: 5017
Memory: 0
Multipliers:48

Analysis & Synthesis Status Successful - Mon Feb 04 08:48:32 2019
Quartus Prime Version   18.1.0 Build 625 09/12/2018 SJ Lite Edition
Revision Name   matmulfifotop
Top-level Entity Name   matmulfifotop
Family Cyclone IV E

Total logic elements   9,502
Total registers 5017
Total pins   106
Total virtual pins   0
Total memory bits   0
Embedded Multiplier 9-bit elements   48
Total PLLs   0

7 ns: Beginning simulation...
@ 7 ns: Loading file y.txt...
@ 7 ns: Loading file x.txt...
@ 146 ns: Comparing file z.txt
@ 271 ns: Simulation completed.
Total simulation cycle count: 132
Total error count: 0