

## Assignment 3- Edge Detection

Logic Elements:12181

Registers: 11652

Memory: 640

Multipliers:0 ;

Flow Summary ; +-----+-----+ ;

Flow Status ; Successful - Tue Feb 12 22:03:11 2019 ;

; Quartus Prime Version ; 18.1.0 Build 625 09/12/2018 SJ Lite Edition ; ; Revision Name ;

Canny\_Edge\_Detection ;

; Top-level Entity Name ; Canny\_Edge\_Detection ;

; Family ; Cyclone IV E ;

; Device ; EP4CE115F29C8 ;

; Timing Models ; Final ;

; Total logic elements ; 12,181 ;

; Total combinational functions ; 12,181 ;

; Dedicated logic registers ; 11,652 ;

; Total registers ; 11652 ;

; Total pins ; 38 ;

; Total virtual pins ; 0 ;

; Total memory bits ; 640 ;

; Embedded Multiplier 9-bit elements ; 0 ;

; Total PLLs ; 0 ;

Total cycles to complete execution:388802

Number of pixels: 720x540 = 388800

Frames per Second = 24FPS (Approx)