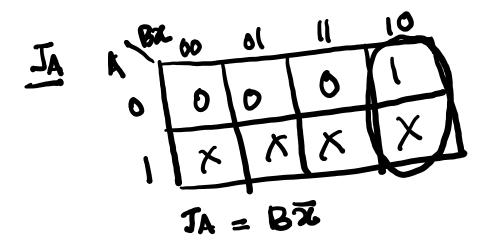
Synthesis using Jk Flip-Flop

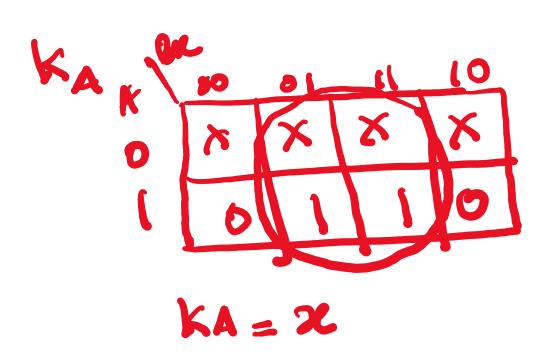
Q. Design a circuit that detects three or more consecutive xeros

EXICTATION TABLE

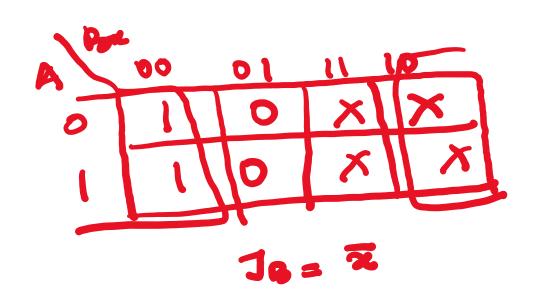
Q(E)	a(Lt1)	JK
0	0	OK
0		IX
(0	x (
	l	X O

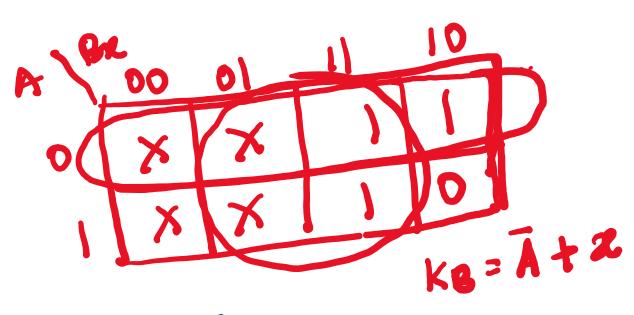
STATE TABLE Present State	loput	Next Stat	e Bltt)	Input to JA KA	PFs Ja ^k a
		00-10-10-10	-10101010101	0 x 0 x 0 x 0 x 1 x 0 x	X X X X X X





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$$J_A = B\overline{x}$$
 $K_A = \overline{x}$
 $J_B = \overline{x}$
 $K_B = \overline{A} + x$

