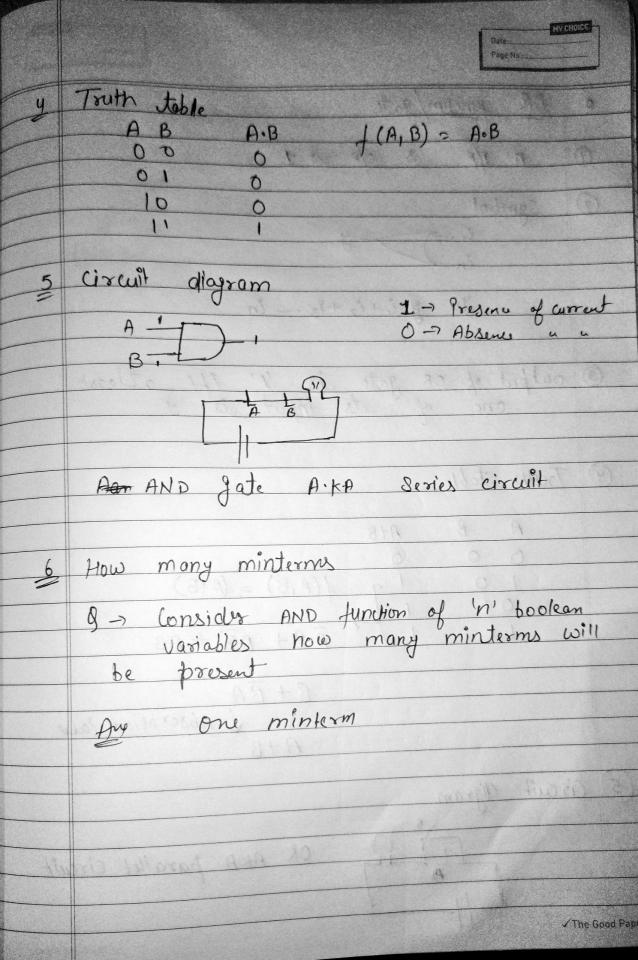
* Logic gates Jate > It will allow or does not allow current to pass through it. It is used to implement logic ternision using gates Basic / fundamental / L) NAND 4> NOT L> NOR AND OR 47 XOR L> XNOR * AND gete 1 n inputes and only "1" output (2) Symbol in J y = i1·i2 -- in 3 output @ AND gate is '1' if all #
inputs are '1' if atleast one input is '0' then out will be 0



OR junction/gate

n 1/p & 0/p is 1 Symbol y in 2 y= y i+i2+13-- in 3 output of OR gate is "1" iff atleast one of its input is I 9 Trum table $\frac{1}{7} + (A_1B) = (A+B)$ AB+ AB+ AB B+BA L Absorption law 3 Circuit digram OR AKA parallel cirul How many minterms n var or function √The Good Pa