

Fig. i) CMOS based D Flip Flop Circuit

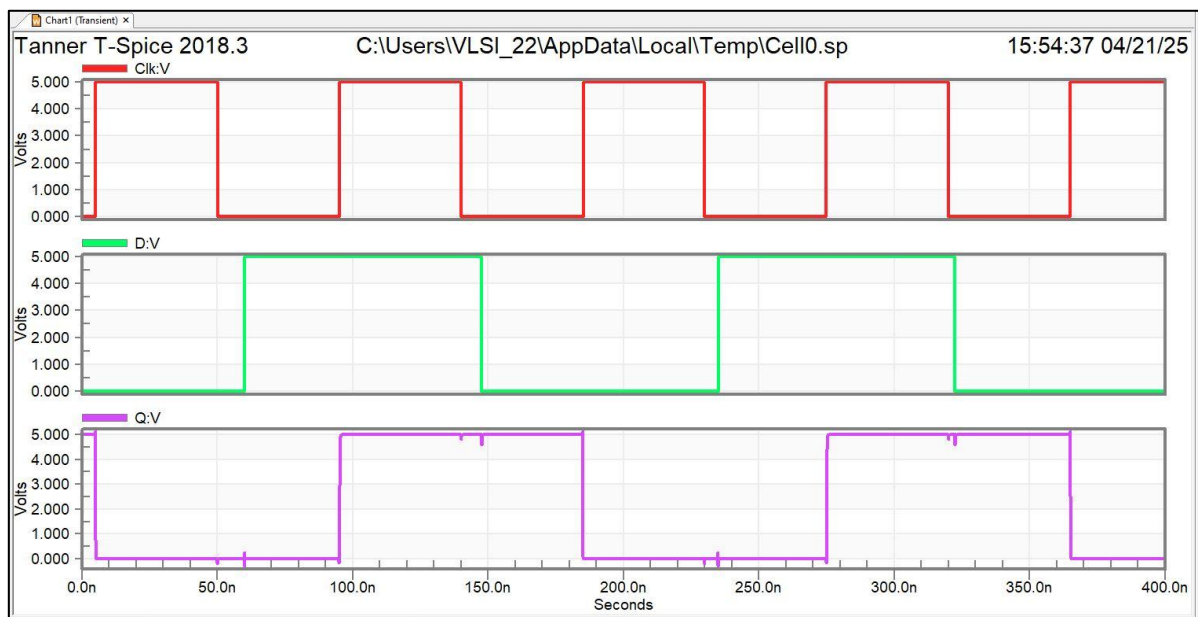


Fig. iii) Transient characteristics of Circuit

Transient/Fourier Analysis	
Stop Time	400n
Maximum Time Step	0.01n
Print Start Time	0
Print Time Step	
Use Initial Conditions	False
Startup Mode	Op

Properties		Properties	
V1 (vpulse)		V2 (vpulse)	
<div> </div>		<div> </div>	
User		User	
AFS		AFS	
ANNOTATE		ANNOTATE	
Neg		Neg	
Pos		Pos	
Delay	60n	Delay	5n
FallTime	1p	FallTime	1p
Freq	5.7142857M	Freq	11.111111M
Period	175n	Period	90n
PulseWidth	87.499n	PulseWidth	44.999n
RiseTime	1p	RiseTime	1p
ROUND	0	ROUND	0
SPICE		SPICE	
VHigh	5	VHigh	5
VLow	0	VLow	0
System		System	
Angle	0	Angle	0
MasterCell	vpulse	MasterCell	vpulse
MasterLibrary	SPICE_Sources	MasterLibrary	SPICE_Sources
MasterView	symbol	MasterView	symbol
Mirror	false	Mirror	false
Name	V1	Name	V2
Scaling	1	Scaling	1
X	9.400	X	8.300
Y	1.800	Y	1.800

Fig. ii) Plot conditions and parameter settings