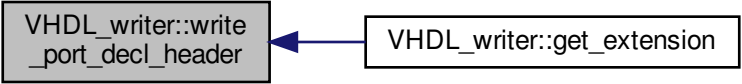


VHDL\_writer::write  
\_port\_decl\_header



```
graph LR; A[VHDL_writer::get_extension] --> B[VHDL_writer::write_port_decl_header];
```

The diagram consists of two rectangular boxes connected by a horizontal arrow. The box on the left is shaded gray and contains the text 'VHDL\_writer::write\_port\_decl\_header'. The box on the right is white with a black border and contains the text 'VHDL\_writer::get\_extension'. A blue arrow points from the right box to the left box, indicating a call or relationship between the two functions.

VHDL\_writer::get\_extension