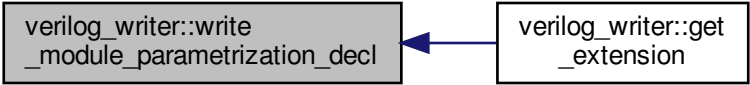


verilog_writer::write
_module_parametrization_decl



```
graph LR; A[verilog_writer::get_extension] --> B[verilog_writer::write_module_parametrization_decl]
```

The diagram consists of two rectangular boxes. The box on the left is shaded gray and contains the text 'verilog_writer::write' on the first line and '_module_parametrization_decl' on the second line. The box on the right is white and contains the text 'verilog_writer::get' on the first line and '_extension' on the second line. A dark blue arrow points from the right side of the white box to the left side of the gray box.

verilog_writer::get
_extension