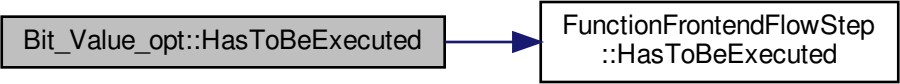


Bit_Value_opt::HasToBeExecuted



```
graph LR; A[Bit_Value_opt::HasToBeExecuted] --> B[FunctionFrontendFlowStep::HasToBeExecuted]
```

FunctionFrontendFlowStep
::HasToBeExecuted