

target\_technology



```
graph BT; FPGA_technology --> target_technology
```

A diagram consisting of two rectangular boxes. The top box is white with a black border and contains the text 'target\_technology'. The bottom box is gray with a black border and contains the text 'FPGA\_technology'. A blue arrow points vertically from the top center of the bottom box to the bottom center of the top box.

FPGA\_technology