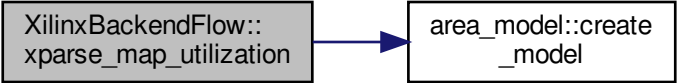


XilinxBackendFlow::  
xparse\_map\_utilization



```
graph LR; A[XilinxBackendFlow::xparse_map_utilization] --> B[area_model::create_model]
```

area\_model::create  
\_model