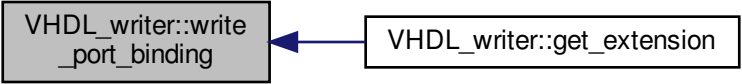


VHDL\_writer::write  
\_port\_binding



```
graph LR; A[VHDL_writer::get_extension] --> B[VHDL_writer::write_port_binding];
```

The diagram consists of two rectangular boxes connected by a horizontal arrow. The box on the left is shaded gray and contains the text 'VHDL\_writer::write' on the top line and '\_port\_binding' on the bottom line. The box on the right is white with a black border and contains the text 'VHDL\_writer::get\_extension'. A blue arrow points from the right side of the white box to the left side of the gray box.

VHDL\_writer::get\_extension