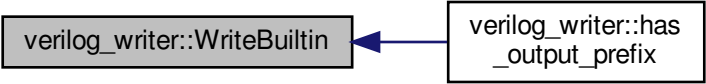


verilog_writer::WriteBuiltin



```
graph LR; A[verilog_writer::has_output_prefix] --> B[verilog_writer::WriteBuiltin]
```

The diagram consists of two rectangular boxes. The box on the left is shaded gray and contains the text 'verilog_writer::WriteBuiltin'. The box on the right is white with a black border and contains the text 'verilog_writer::has_output_prefix'. A dark blue arrow points from the right box to the left box, indicating a relationship or dependency.

verilog_writer::has
_output_prefix