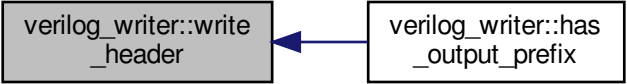


verilog_writer::write
_header



```
graph LR; A[verilog_writer::has_output_prefix] --> B[verilog_writer::write_header]
```

The diagram consists of two rectangular boxes. The box on the left is shaded gray and contains the text 'verilog_writer::write_header'. The box on the right is white and contains the text 'verilog_writer::has_output_prefix'. A dark blue arrow points from the right box to the left box, indicating a call or relationship between the two.

verilog_writer::has
_output_prefix