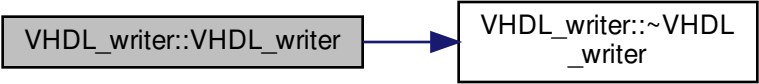


VHDL_writer::VHDL_writer



```
graph LR; A[VHDL_writer::VHDL_writer] --> B[VHDL_writer::~~VHDL_writer];
```

The diagram consists of two rectangular boxes connected by a horizontal arrow. The left box is shaded gray and contains the text 'VHDL_writer::VHDL_writer'. The right box is white and contains the text 'VHDL_writer::~~VHDL_writer'. A blue arrow points from the right side of the left box to the left side of the right box.

VHDL_writer::~~VHDL
_writer