

```

┌────────────────────────────────── MODULE t2pc ───────────────────────────────────┐
EXTENDS Integers, Sequences, FiniteSets, TLC
CONSTANT RM,           The set of participating resource managers
          RMMAYFAIL,
          TMMAYFAIL,
          BTMENABLE    TRUE when backup is needed, FALSE otherwise. Used to check problem 1.1

--algorithm TransactionCommit{
  variable rmState = [rm ∈ RM ↦ "working"],  initially all RMs are working
          tmState = "init";  TM is in "init" state initially
          btmState = "init";  BTM is also ready to take over if TM fails.

  define {
    The TM or BTM can only commit when rmState is prepared, committed or failed.
    canCommit ≜ ∀ rm ∈ RM : rmState[rm] ∈ { "prepared", "committed", "failed" }

    The TM or BTM can only abort when no other RM is committed.
    canAbort ≜ ∀ rm ∈ RM : rmState[rm] ≠ "committed"  TM can abort when no RM is in committed state

  }

  macro Prepare( p )
  {
    await rmState[p] ∈ { "working", "prepared" };
    rmState[p] := "prepared";

  }

  macro Decide( p )
  {
    if ( tmState ≠ "hidden" )  If TM state is not hidden then execute this
    {
      either
      {
        when tmState = "commit";
        if ( rmState[p] = "prepared" ) rmState[p] := "committed"

      }
      or
      {
        when tmState = "abort";
        if ( rmState[p] ∈ { "prepared", "working" } ) rmState[p] := "aborted"

      }
      or
      {

```

```

        when  $rmState[p]$  = "working" ;
         $rmState[p] := \text{"aborted"}$ 
    }
}
else {
    If  $TM$  state is hidden then check for  $BTM$  state.
    {
        if (  $BTMENABLE$  )
        {
            either
            {
                when  $btmState$  = "commit" ;
                if (  $rmState[p]$  = "prepared" )  $rmState[p] := \text{"committed"}$ 
            }
            or
            { When  $BTM$  state is "abort", then  $RM$  state goes to "aborted"
              when  $btmState$  = "abort" ;
              if (  $rmState[p] \in \{\text{"prepared"}, \text{"working"}\}$  )  $rmState[p] := \text{"aborted"}$ 
            }
            or
            {  $RM$  can spontaneously abort when it's in the working state.
              when  $rmState[p]$  = "working" ;
               $rmState[p] := \text{"aborted"}$ 
            }
        }
    }
}

```

```

}
macro Fail(  $p$  )
{
    if (  $RMMAYFAIL$  )
    {
         $rmState[p] := \text{"failed"}$ 
    }
}

```

```

fair process (  $RManager \in RM$  )
{
    start: while (  $rmState[self] \in \{\text{"working"}, \text{"prepared"}\}$  ) {
        either Prepare( $self$ ) or Decide( $self$ ) or Fail( $self$ ) }
    }
}

```

```

fair process (  $TManager = 0$  )

```

{

*TS*: **either**

{

**await** *canCommit* ;

*TC*: **if** ( *tmState*  $\neq$  "hidden"  $\wedge$  *canCommit* ) { *tmState* := "commit" } ;

*F1*: **if** ( *TMMAYFAIL* )

{

Change the *tmState* to hidden only if it's not already hidden (does not really matter)

**if** ( *tmState*  $\neq$  "hidden" ) {

Transfer the *TM*'s state to *BTM* before going to hidden.

**if** ( *BTMENABLE* ) *btmState* := *tmState* ;

*tmState* := "hidden" ;

}

}

}

**or**

{

**await** *canAbort* ;

*TA*: **if** ( *tmState*  $\neq$  "hidden"  $\wedge$  *canAbort* ) { *tmState* := "abort" } ;

*F2*: **if** ( *TMMAYFAIL* )

{

**if** ( *tmState*  $\neq$  "hidden" )

{

Transfer *BTM* state to *TM*

**if** ( *BTMENABLE* ) *btmState* := *tmState* ;

*tmState* := "hidden" ; *TM* fails

}

}

}

}

**fair process** ( *BTManager* = 10 )

```

{
  BTS: either
  {
    await canCommit ;

    BTM can commit only when BTM is enabled by the user, tmState is hidden and canCommit is true
    BTC: if ( tmState = "hidden"  $\wedge$  BTMENABLE  $\wedge$  canCommit )
    {
      print ⟨"committing when rmstate is", rmState⟩ ;
      btmState := "commit" ;
    }
  }
  or
  {
    await canAbort ;
    BTA: if ( tmState = "hidden"  $\wedge$  BTMENABLE  $\wedge$  canAbort )
    {
      btmState := "abort" ;
    }
  }
}
}

```

**BEGIN TRANSLATION**

VARIABLES  $rmState, tmState, btmState, pc$

**define statement**

$canCommit \triangleq \forall rm \in RM : rmState[rm] \in \{\text{"prepared"}, \text{"committed"}, \text{"failed"}\}$

$canAbort \triangleq \forall rm \in RM : rmState[rm] \neq \text{"committed"}$

$vars \triangleq \langle rmState, tmState, btmState, pc \rangle$

$ProcSet \triangleq (RM) \cup \{0\} \cup \{10\}$

$Init \triangleq$  **Global variables**

$\wedge rmState = [rm \in RM \mapsto \text{"working"}]$   
 $\wedge tmState = \text{"init"}$   
 $\wedge btmState = \text{"init"}$   
 $\wedge pc = [self \in ProcSet \mapsto \text{CASE } self \in RM \rightarrow \text{"start"}$   
 $\quad \square \quad self = 0 \rightarrow \text{"TS"}$   
 $\quad \square \quad self = 10 \rightarrow \text{"BTS"}]$

$$\begin{aligned}
start(self) \triangleq & \wedge pc[self] = \text{"start"} \\
& \wedge \text{IF } rmState[self] \in \{\text{"working"}, \text{"prepared"}\} \\
& \quad \text{THEN } \wedge \vee \wedge rmState[self] \in \{\text{"working"}, \text{"prepared"}\} \\
& \quad \quad \wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"prepared"}] \\
& \quad \vee \wedge \text{IF } tmState \neq \text{"hidden"} \\
& \quad \quad \text{THEN } \wedge \vee \wedge tmState = \text{"commit"} \\
& \quad \quad \quad \wedge \text{IF } rmState[self] = \text{"prepared"} \\
& \quad \quad \quad \quad \text{THEN } \wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"commit"}] \\
& \quad \quad \quad \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \quad \quad \quad \wedge \text{UNCHANGED } rmState \\
& \quad \vee \wedge tmState = \text{"abort"} \\
& \quad \quad \wedge \text{IF } rmState[self] \in \{\text{"prepared"}, \text{"working"}\} \\
& \quad \quad \quad \text{THEN } \wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"abort"}] \\
& \quad \quad \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \quad \quad \wedge \text{UNCHANGED } rmState \\
& \quad \vee \wedge rmState[self] = \text{"working"} \\
& \quad \quad \wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"aborted"}] \\
& \text{ELSE } \wedge \text{IF } BTMENABLE \\
& \quad \text{THEN } \wedge \vee \wedge btmState = \text{"commit"} \\
& \quad \quad \wedge \text{IF } rmState[self] = \text{"prepared"} \\
& \quad \quad \quad \text{THEN } \wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"commit"}] \\
& \quad \quad \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \quad \quad \wedge \text{UNCHANGED } rmState \\
& \quad \vee \wedge btmState = \text{"abort"} \\
& \quad \quad \wedge \text{IF } rmState[self] \in \{\text{"prepared"}, \text{"working"}\} \\
& \quad \quad \quad \text{THEN } \wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"abort"}] \\
& \quad \quad \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \quad \quad \wedge \text{UNCHANGED } rmState \\
& \quad \vee \wedge rmState[self] = \text{"working"} \\
& \quad \quad \wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"abort"}] \\
& \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \quad \wedge \text{UNCHANGED } rmState \\
& \vee \wedge \text{IF } RMMAYFAIL \\
& \quad \text{THEN } \wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"failed"}] \\
& \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \quad \wedge \text{UNCHANGED } rmState \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"start"}] \\
& \text{ELSE } \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"Done"}] \\
& \quad \wedge \text{UNCHANGED } rmState \\
& \wedge \text{UNCHANGED } \langle tmState, btmState \rangle \\
RManager(self) \triangleq & start(self) \\
TS \triangleq & \wedge pc[0] = \text{"TS"} \\
& \wedge \vee \wedge canCommit
\end{aligned}$$

$$\begin{aligned}
& \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"TC"}] \\
& \vee \wedge canAbort \\
& \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"TA"}] \\
& \wedge \text{UNCHANGED } \langle rmState, tmState, btmState \rangle \\
TC \triangleq & \wedge pc[0] = \text{"TC"} \\
& \wedge \text{IF } tmState \neq \text{"hidden"} \wedge canCommit \\
& \quad \text{THEN } \wedge tmState' = \text{"commit"} \\
& \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \wedge \text{UNCHANGED } tmState \\
& \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"F1"}] \\
& \wedge \text{UNCHANGED } \langle rmState, btmState \rangle \\
F1 \triangleq & \wedge pc[0] = \text{"F1"} \\
& \wedge \text{IF } TMMAYFAIL \\
& \quad \text{THEN } \wedge \text{IF } tmState \neq \text{"hidden"} \\
& \quad \quad \text{THEN } \wedge \text{IF } BTMENABLE \\
& \quad \quad \quad \text{THEN } \wedge btmState' = tmState \\
& \quad \quad \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \quad \quad \wedge \text{UNCHANGED } btmState \\
& \quad \quad \wedge tmState' = \text{"hidden"} \\
& \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \wedge \text{UNCHANGED } \langle tmState, btmState \rangle \\
& \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \wedge \text{UNCHANGED } \langle tmState, btmState \rangle \\
& \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"Done"}] \\
& \wedge \text{UNCHANGED } rmState \\
TA \triangleq & \wedge pc[0] = \text{"TA"} \\
& \wedge \text{IF } tmState \neq \text{"hidden"} \wedge canAbort \\
& \quad \text{THEN } \wedge tmState' = \text{"abort"} \\
& \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \wedge \text{UNCHANGED } tmState \\
& \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"F2"}] \\
& \wedge \text{UNCHANGED } \langle rmState, btmState \rangle \\
F2 \triangleq & \wedge pc[0] = \text{"F2"} \\
& \wedge \text{IF } TMMAYFAIL \\
& \quad \text{THEN } \wedge \text{IF } tmState \neq \text{"hidden"} \\
& \quad \quad \text{THEN } \wedge \text{IF } BTMENABLE \\
& \quad \quad \quad \text{THEN } \wedge btmState' = tmState \\
& \quad \quad \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \quad \quad \wedge \text{UNCHANGED } btmState \\
& \quad \quad \wedge tmState' = \text{"hidden"} \\
& \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \wedge \text{UNCHANGED } \langle tmState, btmState \rangle
\end{aligned}$$

$$\begin{aligned}
& \text{ELSE } \wedge \text{TRUE} \\
& \quad \wedge \text{UNCHANGED } \langle tmState, btmState \rangle \\
& \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"Done"}] \\
& \wedge \text{UNCHANGED } rmState \\
\\
TManager & \triangleq TS \vee TC \vee F1 \vee TA \vee F2 \\
\\
BTS & \triangleq \wedge pc[10] = \text{"BTS"} \\
& \quad \wedge \vee \wedge canCommit \\
& \quad \quad \wedge pc' = [pc \text{ EXCEPT } ![10] = \text{"BTC"}] \\
& \quad \vee \wedge canAbort \\
& \quad \quad \wedge pc' = [pc \text{ EXCEPT } ![10] = \text{"BTA"}] \\
& \quad \wedge \text{UNCHANGED } \langle rmState, tmState, btmState \rangle \\
\\
BTC & \triangleq \wedge pc[10] = \text{"BTC"} \\
& \quad \wedge \text{IF } tmState = \text{"hidden"} \wedge BTMENABLE \wedge canCommit \\
& \quad \quad \text{THEN } \wedge PrintT(\langle \text{"committing when rmstate is"}, rmState \rangle) \\
& \quad \quad \quad \wedge btmState' = \text{"commit"} \\
& \quad \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \quad \quad \wedge \text{UNCHANGED } btmState \\
& \quad \wedge pc' = [pc \text{ EXCEPT } ![10] = \text{"Done"}] \\
& \quad \wedge \text{UNCHANGED } \langle rmState, tmState \rangle \\
\\
BTA & \triangleq \wedge pc[10] = \text{"BTA"} \\
& \quad \wedge \text{IF } tmState = \text{"hidden"} \wedge BTMENABLE \wedge canAbort \\
& \quad \quad \text{THEN } \wedge btmState' = \text{"abort"} \\
& \quad \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \quad \quad \wedge \text{UNCHANGED } btmState \\
& \quad \wedge pc' = [pc \text{ EXCEPT } ![10] = \text{"Done"}] \\
& \quad \wedge \text{UNCHANGED } \langle rmState, tmState \rangle \\
\\
BTManager & \triangleq BTS \vee BTC \vee BTA \\
\\
Next & \triangleq TManager \vee BTManager \\
& \quad \vee (\exists self \in RM : RManager(self)) \\
& \quad \vee \text{Disjunct to prevent deadlock on termination} \\
& \quad ((\forall self \in ProcSet : pc[self] = \text{"Done"}) \wedge \text{UNCHANGED } vars) \\
\\
Spec & \triangleq \wedge Init \wedge \square [Next]_{vars} \\
& \quad \wedge \forall self \in RM : WF_{vars}(RManager(self)) \\
& \quad \wedge WF_{vars}(TManager) \\
& \quad \wedge WF_{vars}(BTManager) \\
\\
Termination & \triangleq \diamond (\forall self \in ProcSet : pc[self] = \text{"Done"}) \\
\\
& \text{END TRANSLATION}
\end{aligned}$$

Consistency property for *RM*s. Two *RM*s cannot be in aborted and committed state simultaneously.  
 $ConsistentRM \triangleq$

A state predicate asserting that two *RM*s have not arrived at conflicting decisions.

$$\forall rm1, rm2 \in RM : \neg \wedge rmState[rm1] = \text{"aborted"} \\ \wedge rmState[rm2] = \text{"committed"}$$

When the *TM* is active, then this needs to be satisfied for consistency

$$ConsistentTM \triangleq ((\neg tmState = \text{"hidden"}) \wedge (\forall rm \in RM : \neg \wedge rmState[rm] = \text{"committed"} \wedge tmState = \text{"aborted"})) \\ \vee (tmState = \text{"hidden"} \wedge (\neg BTMENABLE))$$

When *TM* is not active, *i.e.* in hidden state, then this property needs to be satisfied.

$$ConsistentBTM \triangleq (BTMENABLE \wedge tmState = \text{"hidden"}) \wedge (\forall rm \in RM : \neg \wedge rmState[rm] = \text{"committed"})$$

This will be the overall consistent property taking into account the *RM*s, *TM* and *BTM*.

$$Consistent \triangleq ConsistentRM \wedge (ConsistentTM \vee ConsistentBTM)$$

---

Details of group members: Nitin Nataraj - nitinnat@buffalo.edu, 50246850 Arun Krishnamurthy - arunkris@buffalo.edu, 50247445

1.1 When *RM*MAYFAIL and *TM*MAYFAIL are both false, then the program runs with no errors. When *RM*MAYFAIL is true and *TM*MAYFAIL is false, the program still runs with no errors. This is correct because even though some *RM*s fail, the *TM* will look if other *RM*s have all committed or aborted.

1.2 When *RM*MAYFAIL is false and *TM*MAYFAIL is true, the temporal property is violated, *i.e.* Termination is not satisfied. On examining the stack trace, we see that when a state of *< prepared, aborted, aborted >* is reached and the *TM* fails (becomes hidden), the state does not change for a few more iterations. This is because there is no transaction manager to handle the *RM* requests. Since there is no way this state will change, it violates the termination property.

1.3 When both *TM* and *RM* are allowed to fail, and the *BTM* is enabled, then the program reaches termination and also does not violate consistency. When both *TM* and *RM* are both true and *BTM* is enabled, then the program still reaches termination since if *TM* fails, the *BTM* takes over and makes sure that the program reaches termination