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| 1 | [test3](#1.0) |  | 0x0 |  |
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| 1 | [test3](#1.0) |  | 0x0 |  |
| 1.1 | [reg\_name](#1.1.0) | 0x00000000 | 0x0 |  |
| 1 | [test3](#1.0) |  | 0x0 |  |
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|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| 1.0 | | | test3 | | block |  |
| offset | 0 | external | |  | size | 4 |
|  | | | | | | |
|  | | | | | | |
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| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 1.1.0 | | | | | | | | reg\_name | | | | | reg32 | |  |
| offset | | 0 | external | | |  | | | size | | 32 |  | |  | |
|  | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 1 | rrw | | | rw | ro | | 0x0 | | |  | | | | | |