|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
|  | | | chip\_name | | chip |  |
| offset |  | external | |  | size |  |
|  | | | | | | |
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|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
|  | | | block\_name | | block |  |
| offset |  | external | |  | size |  |
|  | | | | | | |
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|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | reg\_name | | | | | reg32 | |  |
| offset | |  | external | | |  | | | size | | 32 |  | |  | |
|  | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
|  |  | | |  |  | |  | | |  | | | | | |

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | reg\_1 | | | section | |  | |
| offset |  | external |  | | repeat |  | | size | |  |
|  | | | | | | | | | | |
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| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | reg\_name2 | | | | reg8 | |  | |
| offset | | |  | | | | | external | | | |  | |  | |  | |
|  | | | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | | | |
| 7 | | 6 | | | 5 | | | | 4 | | | 3 | 2 | | 1 | | 0 |
| bits | name | | | s/w | | h/w | default | | | | description | | | | | | |
| 7:0 | F | | | Rw | |  | 0 | | | |  | | | | | | |

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | reg\_name3 | | | | | reg32 | |  |
| offset | |  | external | | |  | | | size | | 32 |  | |  | |
|  | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | |
| bits | name | | | s/w | h/w | | default | | | description | | | | | |
| 0:15 | F | | | Rw |  | | 0 | | |  | | | | | |

|  |
| --- |
| End RegGroup |